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AM50RA-MW500-C

Product Description

- 5.0-inch AMOLED (64.12x116.72mm)
- 16.7M Colors / 60000:1 Contrast Ratio
- 720x1280 Pixels
- 4-Lane MIPI Interface
- Wide Temperature Range

- Hard Coating Polarizer
- 3.3V (Analog) / 1.8V (Logic)
- Driver IC: RM67295, CTP IC: CST3640
- RoHS Compliant

Revision History

Date	Rev. No	Page	Summary
04/27/2025	1.0	All	First issue

Description

This is a color active matrix AMOLED module using Low Temperature Poly-silicon Thin Film Transistors (LTPS TFT) as active switching devices. This module features a 5.0-inch diagonally measured active display area with a resolution of 720 horizontal by 1280 vertical pixels and a rendering pixel arrangement. The module is capable of displaying up to 16.7M colors.

Features

Input Voltage: 3.3V

Display Interface: 4-Lane MIPI

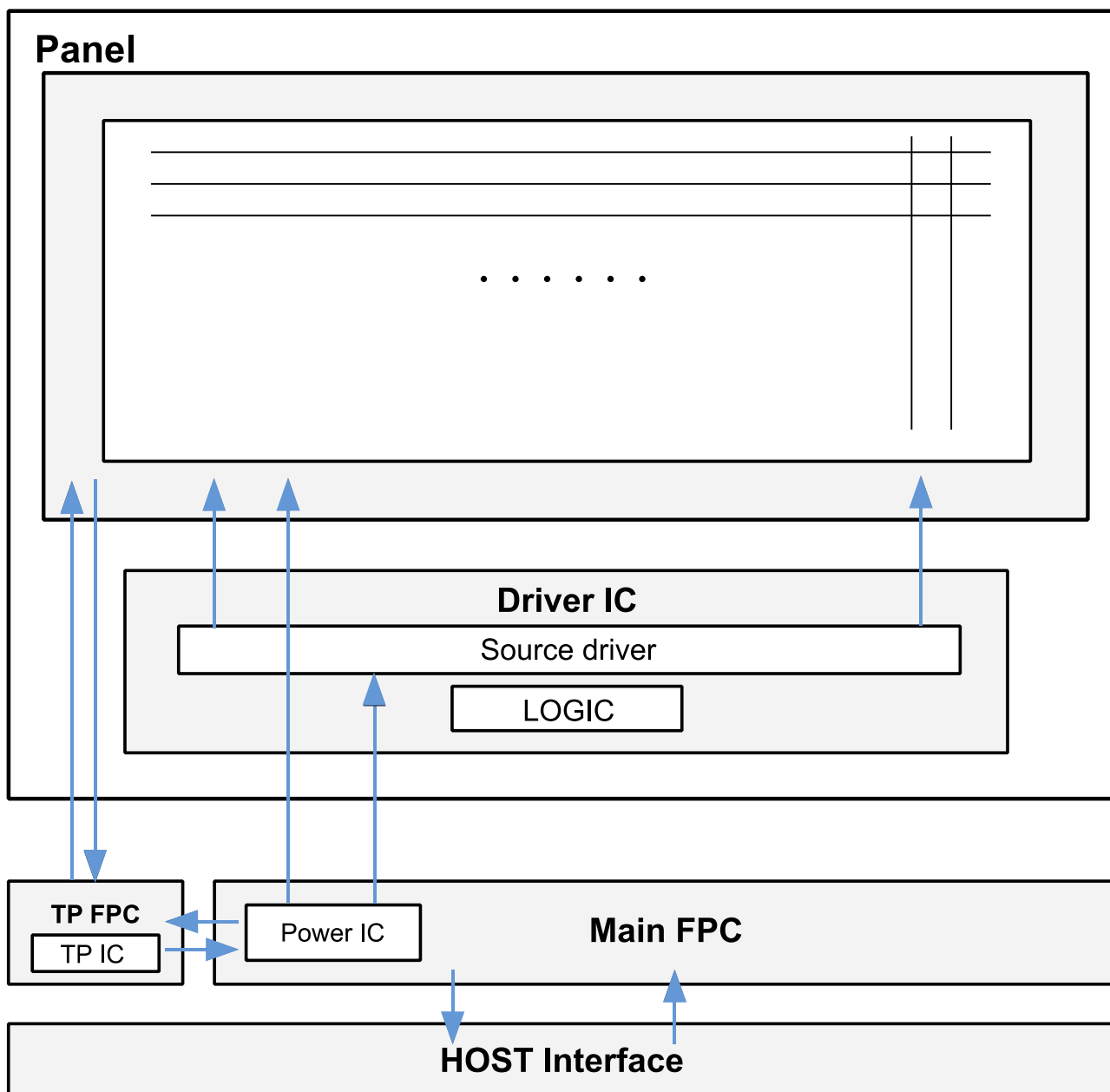
CTP Interface: I2C

General Information Items	Specification	Unit	Note
	Main Panel		
Display Area (AA)	61.88(H) x 110.02(V) (5.0 inch)	mm	--
CTP View Area	63.76(H) x 111.84(V)	mm	--
Display Colors	16.7M	colors	--
Number of Pixels	720(RGB)x1280	dots	--
Pixel Arrangement	Rendering	--	--
Pixel Pitch	0.08595 (H) x 0.08595 (V)	mm	--
Viewing Angle	ALL	o'clock	--
Display Driver IC	RM67295	--	--
Touch Screen Type	On-Cell	--	--
Touch IC	CST3640	--	--
Display Mode	Normally Black	--	--
Operating Temperature	-20 to +70	°C	--
Storage Temperature	-30 to +80	°C	--

Mechanical Information

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal (H)	--	64.12	--	mm	--
	Vertical (V)	--	116.72	--	mm	--
	Depth (D)	--	0.94	--	mm	--
Weight		--	14	--	g	Approximate

2. Block Diagram



3. Input Terminal Pin Assignment

3.1 Display Pin Assignment

NO.	Symbol	Description	I/O
1	GND	Ground.	P
2	VBAT	Power IC input voltage.	P
3	VBAT	Power IC input voltage.	P
4	VBAT	Power IC input voltage.	P
5	VBAT	Power IC input voltage.	P
6	VBAT	Power IC input voltage.	P
7	GND	Ground.	P
8	GND	Ground.	P
9	GND	Ground.	P
10	MTP_PWR	Power supply for OTP. Leave the pin to open when not in use.	P
11	TE	Tear effect output.	O
12	RESX	This signal will reset the device and must be applied to properly initialize the chip. Active low.	I
13	VDDIO	Driver IC digital I/O supply.	P
14	GND	Ground.	P
15	D2P	MIPI DSI data2+.	I/O
16	D2N	MIPI DSI data2-.	I/O
17	GND	Ground.	P
18	D1P	MIPI DSI data1+.	I/O
19	D1N	MIPI DSI data1-.	I/O
20	GND	Ground.	P
21	CLKP	MIPI DSI clock+.	I
22	CLKN	MIPI DSI clock-.	I
23	GND	Ground.	P
24	D0P	MIPI DSI data0+.	I/O
25	D0N	MIPI DSI data0-.	I/O
26	GND	Ground.	P
27	D3P	MIPI DSI data3+.	I/O
28	D3N	MIPI DSI data3-.	I/O
29	GND	Ground.	P
30	VCI	Driver IC analog supply.	P

NO.	Symbol	Description	I/O
31	GND	Ground.	P
32	TP_VCC	TP IC digital power supply.	P
33	TP_VDDI/NC	TP IC digital I/O supply. 3.3V or OPEN. OPEN: IIC bus voltage defaults to 1.8V.	P
34	TP_INT	Interrupt request to the host or wakeup request from the host.	I/O
35	TP_SDA	I2C data input & output.	I/O
36	TP_SCL	I2C clock input.	I
37	TP_RESX	External reset. Low is active.	I
38	NC	No connection.	--
39	GND	Ground.	P

3.2 CTP Pin Assignment

NO.	Symbol	Description	I/O
1	GND	Ground.	P
2	TP_VCC(3.3V)	TP IC digital power supply.	P
3	TP_VDDI/NC	TP IC digital I/O supply.	P
4	TP_INT	Interrupt request to the host or wakeup request from the host.	I/O
5	TP_SDA	I2C data input and output.	I/O
6	TP_SCL	I2C clock input.	I
7	TP_RESX	External reset (low active).	I
8	GND	Ground.	P

4. AMOLED Optical Characteristics

4.1 Optical Specifications

Item		Symbol	Condition	Min	Typ.	Max	Unit	Note	
Color Gamut		S(%)	--	90	100	--	%	(5)	
LCM Luminance		L _V	--	450	500	--	cd/m ²	(6)	
Uniformity		ΔL	--	75	--	--	%	(3)	
Color Temperature		T _C	--	6537	7437	8337	K	--	
Contrast Ratio		CR	θ=0° Normal viewing angle	60000	--	--	%	(2)	
Color Filter Chromaticity	White	W _X		0.2640	0.3040	0.3440		(5)(6)	
		W _Y		0.2830	0.3230	0.3630			
	Red	R _X		0.6370	0.6770	0.7170			
		R _Y		0.2830	0.3230	0.3630			
	Green	G _X		0.1940	0.2340	0.2740			
		G _Y		0.6430	0.6830	0.7230			
	Blue	B _X		0.1000	0.1400	0.1800			
		B _Y		0.0070	0.0470	0.0870			
Viewing Angle	Hor.	Θ _L	CR≥10	80	--	--	degree	(1)(6)	
		Θ _R		80	--	--			
	Ver.	Θ _T		80	--	--			
		Θ _B		80	--	--			
OLED Life Time		Hr	Ta=25°C	30000	--	--	hours	--	
Option View Direction		ALL							(1)

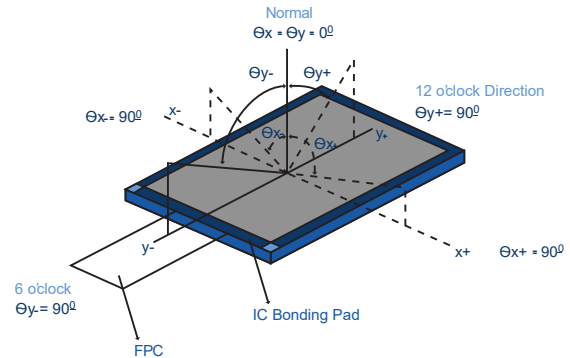
Measuring Conditions:

1. Dark Room
2. Ambient Temperature of 25±2°C
3. 15 Minute Warm up

Optical Specification Reference Notes:

(1) Definition of Viewing Angle:

The viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3,9 o'clock direction and the vertical or 6,12 o'clock direction with respect to the optical axis which is normal to the LCD surface.

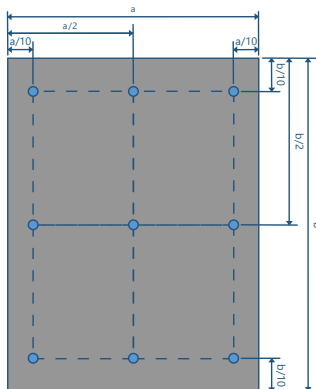


(2) Definition of Contrast Ratio:

Measured at the center point of panel. The contrast ratio (Cr) measured on a module, is the ratio between the luminance (Lw) in a full white area (R=G=B=1) and the luminance (Ld) in a dark area (R=G=B=0).

$$Cr = \frac{Lw}{Ld}$$

(3) Definition of 9-points Luminance Uniformity Measurement Setup:

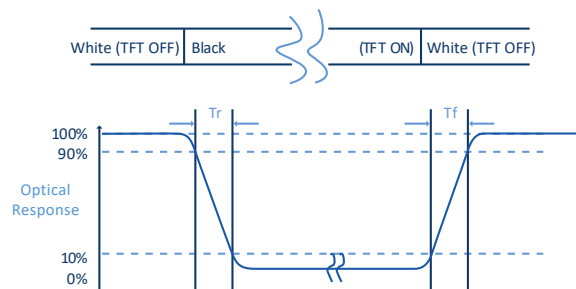


$$Luminance = \frac{(Total\ Luminance\ of\ 9\ Points)}{9}$$

$$Uniformity = \frac{Minimum\ Luminance\ in\ 9\ Points\ (1-9)}{Maximum\ Luminance\ in\ 9\ Points\ (1-9)}$$

(4) Definition of Response Time (TR, TF):

The rise time 'Tr' is defined as the time for luminance to change from 90% to 10% as a result of a change of the electrical condition. The fall time 'Tf' is defined as the time for luminance to change from 10% to 90% as a result of a change of the electrical condition.



(5) Definition of Color Gamut:

Measuring machine CFT-01. NTSC's Primaries: $R(x,y,Y)$, $G(x,y,Y)$, $B(x,y,Y)$. FPM520 of Westar Display Technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics. The color chromaticity shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.

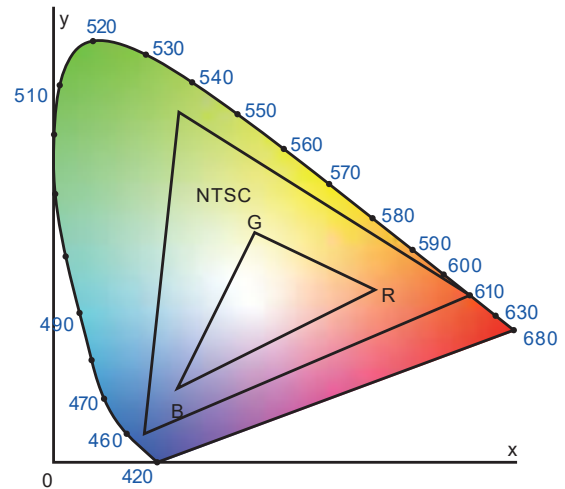
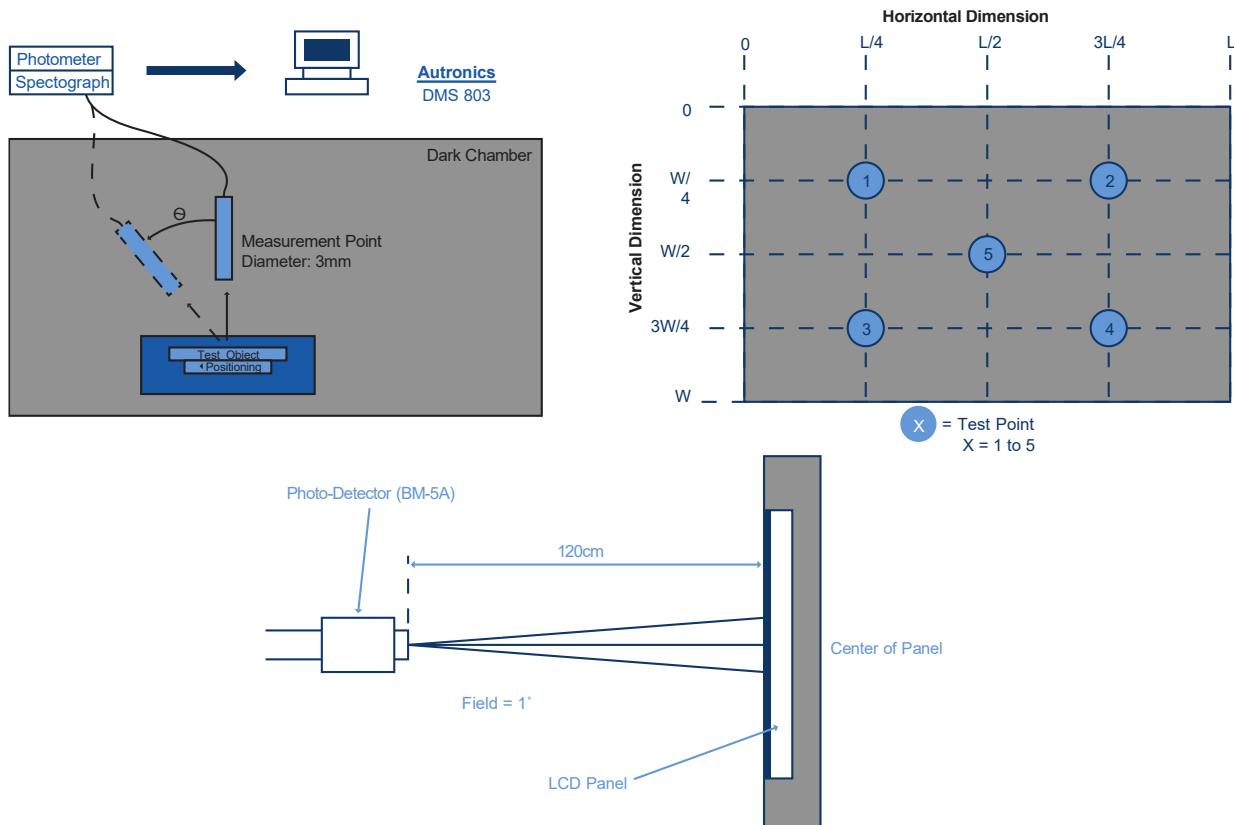


Fig. 1931 CIE Chromaticity Diagram

$$\text{Color Gamut: } S = \frac{\text{Area of RGB Triangle}}{\text{Area of NTSC Triangle}} \times 100\%$$

(6) Definition of Optical Measurement Setup:

The display module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature changes during measuring. In order to stabilize the luminance, the measurement should be executed after displaying a full white pattern for 20 minutes.



5. AMOLED Electrical Characteristics

5.1 Absolute Maximum Rating (VSS=0V, Ta=25°C)

Characteristics	Symbol	Min	Max	Unit
Digital I/O Supply Voltage	VDDIO	-0.3	5.5	V
Analog Supply Voltage	VCI	-0.3	5.5	V
Power IC Input Voltage	VBAT	-0.3	6.0	V
Operating Temperature	T _{OP}	-20	+70	°C
Storage Temperature	T _{ST}	-30	+80	°C

NOTE: If the absolute maximum rating of the above parameters is exceeded, even momentarily, the quality of the product may be degraded. Absolute maximum ratings specify the values which the product may be physically damaged if exceeded. Be sure to use the product within the range of the absolute maximum ratings.

5.2 DC Electrical Characteristics

Characteristics	Symbol	Min	Typ.	Max	Unit	Note
Power IC Input Voltage	VBAT	2.9	--	4.5	V	--
Digital Power Supply	VDDIO	1.65	1.8	3.6	V	--
Analog Power Supply	VCI	2.5	3.3	4.8	V	--
TP Power Supply Voltage	TP_VCC	2.7	--	3.6	V	--
TP I/O Digital Voltage	TP_VDDI/NC	2.7	--	3.6	V	--

6. AMOLED AC Characteristics

6.1 MIPI Interface Timing Characteristics

For detailed High-Speed data transmission burst timing, HS clock transmission timing, and signal line transition characteristics of the 4-Lane MIPI DSI interface, see the table and diagrams on pages 109-110 of the data sheet for controller IC RM67295. The data sheet can be found here:

<https://focuslcds.com/wp-content/uploads/Drivers/RM67295.pdf>

6.2 Turnaround Procedure

For turnaround procedure timing diagrams, see diagrams on page 111 of the data sheet for controller IC RM67295. The data sheet can be found here:

<https://focuslcds.com/wp-content/uploads/Drivers/RM67295.pdf>

6.3 Reset Timing Characteristics

For hardware reset timing parameters, including minimum reset pulse width, reset completion time, and timing sequence diagrams, see page 113 of the data sheet for controller IC RM67295. The data sheet can be found here: <https://focuslcds.com/wp-content/uploads/Drivers/RM67295.pdf>

7. Operating Sequences

7.1 Power On/Off Sequence

For the power-on and power-off timing sequences, see diagrams on pages 115-116 of the data sheet for controller IC RM67295. The data sheet can be found here:

<https://focuslcds.com/wp-content/uploads/Drivers/RM67295.pdf>

8. CTP Electrical Characteristics

8.1 Absolute Maximum Rating

Item	Symbol	Min	Max	Unit	Note
Power Supply Voltage	VDD	2.7	5.5	V	
Operating Temperature	T _{OP}	-40	+85	°C	
Storage Temperature	T _{ST}	-60	+125	°C	

8.2 DC Electrical Characteristics

Ambient temperature: 25°C, VDDA=2.8V

Item	Min	Typ.	Max	Unit	Note
Normal Mode Operating Current	--	9	--	mA	
Sleep Mode Operating Current	--	30	--	uA	
Digital Input Low Voltage/V _{IL}	-0.3	--	0.3*VDDIO	V	
Digital Input High Voltage/V _{IH}	0.7*VDDIO	--	VDDIO	V	
Digital Output Low Voltage/V _{OL}	--	--	0.3*VDDIO	V	
Digital Output High Voltage/V _{OH}	0.7*VDDIO	--	--	V	

8.3 I2C Timing

For I2C timing diagrams, fast-mode operational parameters (supporting SCL clock frequencies up to 400 kbps), and data transmission protocol specifications, refer to pages 8-9 of the data sheet for touch panel CST3640. The data sheet for CST3640 can be found here:

<https://focuslcds.com/wp-content/uploads/Drivers/CST3640.pdf>

8.4 Power-On and Host Resetting

For the power-on and host reset timing diagrams, see diagrams and tables on pages 9-10 of the data sheet for touch panel IC CST3640. The data sheet for CST3640 can be found here: <https://focuslcds.com/wp-content/uploads/Drivers/CST3640.pdf>

9. Quality Inspection Standards

For AMOLED quality inspection standards, please see the following link:

<https://focuslcds.com/lcd-resources/amoled-quality-inspection-standards/>

10. Cautions and Handling Precautions

10.1 Handling and Assembly Precautions

1. Mount the module firmly to the system without warping, twisting, or applying pressure to the panel, as twisting or pressing can alter the display thickness or cause color tone shifts.
2. Protect the glass display panel from drops, impacts, or excessive force to prevent physical fracturing or internal component damage.
3. Do not press or scratch the soft, fragile surface of the display polarizer.
4. Clean the surface by blowing on it and wiping gently with a dry, soft cloth or absorbent cotton; if needed, moisten only with ethyl alcohol, IPA, or hexane. Do not use water, ketone (e.g., acetone), or aromatic solvents.
5. Do not disassemble or modify the module, as doing so can cause electric shock, dust adhesion, or damage to sensitive internal parts.
6. Wear soft gloves or finger stalls during inspection and assembly to keep the surface clean, and avoid touching the interface connector pins with bare hands.
7. In case internal module contents come into contact with skin, eyes, or mouth, wipe immediately with paper towels or gauze, wash thoroughly with soap and water, and seek medical attention if necessary.
8. Protect internal cables and connectors from forced folding or extreme structural stress during assembly.
9. Ensure the enclosure window is fixed strictly within the designated active viewing area, as display characteristics outside this zone are not guaranteed.

10.2 Electrical and Operational Operating Precautions

1. Maintain an optimum grounded working environment; all operators, assembly tools (e.g., soldering irons), and equipment must be properly grounded to prevent static damage to the C-MOS LSI drivers.
2. Peel off the surface protective film slowly just before final assembly to minimize the generation of electrostatic charges.
3. Connect any unused input terminals to VDD or VSS to protect against static electricity accumulation.
4. Strictly adhere to the recommended Power-On and Power-Off sequences; do not connect or disconnect the module while power is applied, and do not input signals before the logic circuits are powered on.
5. System design must include current-limiting protection on the VDD rail to protect the module against electrical surges.
6. Incorporate a voltage-controllable function in the set module to ensure optimal operating voltages and stable contrast levels.
7. Design enclosures to shield the AMOLED panel from surrounding electromagnetic or environmental interference, and design the module layout considering emitting energy to satisfy EMI standards.
8. Avoid displaying static images for extended periods to mitigate the risk of permanent AMOLED image-sticking (burn-in).
9. Note that display response times may experience severe delays at temperatures lower than the operating range, and normal operation may be disrupted at excessively high temperatures.

10.3 Storage and Transportation Precautions

1. Store modules in environments with temperatures maintained between 0°C to 35°C (recommended) or up to 40°C max, with a relative humidity level below 70% to 80%.
2. Keep modules in a dark place, ensuring they are not directly exposed to sunlight, strong ultraviolet radiation, or prolonged fluorescent light.
3. Avoid storing the module under conditions where condensation can form, as electro-chemical reactions may occur on the pads under high humidity. Never operate the display if dew condensation has occurred inside.
4. Store the modules in clean rooms free from acids, alkalis, or harmful, corrosive gases.
5. During transit, protect modules from falls, violent mechanical shocks, excessive pressing, moisture, and direct sunshine. Handle with care to prevent stressing the circuitry FPC on the bottom side of the panel.