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ISO 9001

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E26RB-T-XS420-C

Product Description

- | | |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <ul style="list-style-type: none">• 2.6-inch TFT (46.76x65.85mm)• 320x432 Pixels• 8/9/16/18/24-Bit MCU, 3/4-SPI +16/18/24-Bit RGB, 3/4-line Serial Interfaces• Special Temperature Range• Super Wide Viewing Angle | <ul style="list-style-type: none">• Transflective• Capacitive Touch Panel• 420 NITS• TFT IC: ILI9488, CTP IC: FT5436• RoHS Compliant |
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Revision History

Date	Rev. No	Page	Summary
07/17/2019	1.0	All	First issue

Description

This is a color active matrix TFT (Thin Film Transistor) LCD (Liquid Crystal Display) that uses amorphous silicon TFT as a switching device. This model is composed of a transfective type TFT-LCD Panel, driver circuit, capacitive touch panel, and backlight unit. The resolution of the 2.6" TFT-LCD contains 480x854 pixels and can display up to 65K/262K/16.7M colors.

Features

Input Voltage: 3.3V

TFT Interface: 8/9/16/18/24-Bit MCU, 3/4-SPI +16/18/24-Bit RGB, 3/4-line Serial

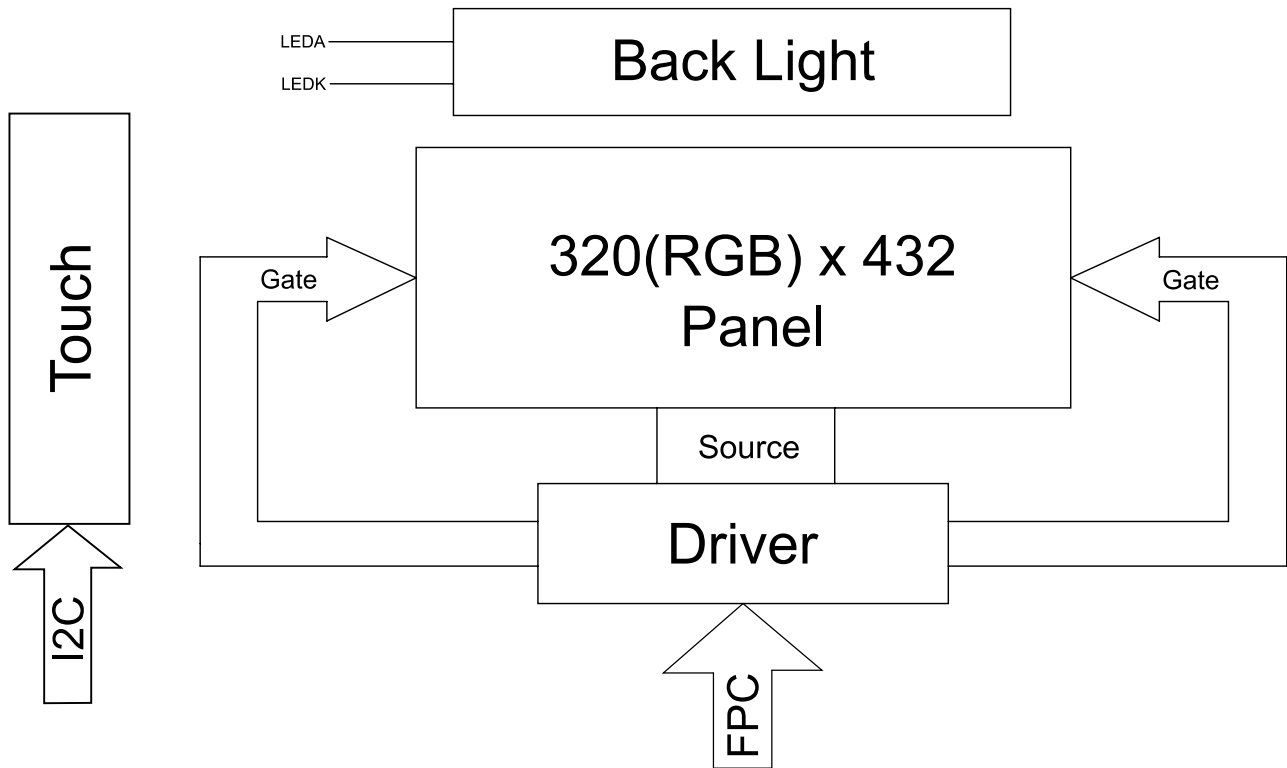
CTP Interface: I2C

General Information Items	Specification	Unit	Note
	Main Panel		
TFT Display Area (AA)	39.84(H) x 53.78(V) (2.6 inch)	mm	--
CTP View Area	40.44(H) x 54.38(V)	mm	--
Driver Element	TFT active matrix	--	--
Display Colors	65K/262K/16.7M	colors	--
Number of Pixels	320(RGB)x432	dots	--
TFT Pixel Arrangement	RGB vertical stripe	--	--
Pixel Pitch	0.1245 (H) x 0.1245 (V)	mm	--
Viewing Angle	Super Wide	o'clock	--
TFT Controller IC	ILI9488	--	--
CTP Driver IC	FT5436	--	--
Display Mode	Transflective/Normally Black	--	--
Touch Points	Multipoint and Gestures	--	--
Operating Temperature	-30 to +85	°C	--
Storage Temperature	-40 to +85	°C	--

Mechanical Information

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal (H)	--	46.76	--	mm	--
	Vertical (V)	--	65.85	--	mm	--
	Depth (D)	--	4.26	--	mm	--
Weight		--	25	--	g	Approximate

2. Block Diagram



3. Input Terminal Pin Assignment

3.1 TFT Pin Assignment

NO.	Symbol	Description	I/O																																								
1	GND	Ground.	P																																								
2	LEDA	Anode pin of backlight.	P																																								
3	LEDK	Cathode pin of backlight.	P																																								
4	NC	No connection.	--																																								
5	CSX	Chip select input pin ("Low" enable). Fix this pin to IOVCC or GND when not in use.	I																																								
6	DCX	Display data/command selection pin.	I																																								
7	WR(SPI-SCL)	DBI Type B: WRX pin, serves as a write signal. DBI Type C: SCL pin as serial clock when operates in the serial interface.	I																																								
8	RDX	Serves as a read signal and MCU read data at the rising edge. Fix this pin to IOVCC or GND when not in use.	I																																								
9	SDA	Serial input signal. The data is applied on the rising edge of the SCL signal. Fix this pin to IOVCC or GND when not in use.	I/O																																								
10	SDO	Serial data output pin in serial bus system interface. Fix this pin to IOVCC or GND when not in use.	O																																								
11-34	DB0-DB23	24-bit parallel bi-directional data bus for MCU system and RGB interface. Fix pins to GND level if not in use.	I/O																																								
35	DE	Data enable signal for RGB interface operation. Fix this pin to IOVCC or GND when not in use.	I																																								
36	PCLK	Dot clock signal for RGB interface operation. Fix this pin to IOVCC or GND when not in use.	I																																								
37	HSYNC	Line synchronizing signal for RGB interface operation. Fix this pin to IOVCC or GND when not in use.	I																																								
38	VSYNC	Frame synchronizing signal for RGB interface operation. Fix this pin to IOVCC or GND when not in use.	I																																								
39	RESX	This signal will reset the device and must be applied to properly initialize the chip.	I																																								
40	IM2	MPU Parallel interface bus and serial interface.	I																																								
		<table border="1"> <thead> <tr> <th>IM2</th> <th>IM1</th> <th>IM0</th> <th>Interface type</th> <th>DB Pin in use</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>0</td> <td>MIPI-DBI Type B 24-bit bus (DB_EN=1)</td> <td>DB23-DB0</td> </tr> <tr> <td>0</td> <td>0</td> <td>0</td> <td>MIPI-DBI Type B 18-bit bus (DB_EN = 0)</td> <td>DB17-DB0</td> </tr> <tr> <td>0</td> <td>0</td> <td>1</td> <td>DBI Tyb_ 9-bit interface</td> <td>DB8-DB0</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>DBI Tyb_ 16-bit interface</td> <td>DB15-DB0</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> <td>DBI Tyb_ 8-bit interface</td> <td>DB7-DB0</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> <td>3-Wire 9 BIT data serial interface</td> <td>SDI SCL CS</td> </tr> <tr> <td>1</td> <td>1</td> <td>1</td> <td>4-Wire 8 BIT data serial interface</td> <td>SDI SCL CS RS</td> </tr> </tbody> </table>		IM2	IM1	IM0	Interface type	DB Pin in use	0	0	0	MIPI-DBI Type B 24-bit bus (DB_EN=1)	DB23-DB0	0	0	0	MIPI-DBI Type B 18-bit bus (DB_EN = 0)	DB17-DB0	0	0	1	DBI Tyb_ 9-bit interface	DB8-DB0	0	1	0	DBI Tyb_ 16-bit interface	DB15-DB0	0	1	1	DBI Tyb_ 8-bit interface	DB7-DB0	1	0	1	3-Wire 9 BIT data serial interface	SDI SCL CS	1	1	1	4-Wire 8 BIT data serial interface	SDI SCL CS RS
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43	VCI	Supply voltage(3.3V).	P
44	VCI		
45	IOVCC	Supply voltage for IO (1.8-3.3V)	P
46	IOVCC		
47	YU	Touch panel top film terminal.	A/D
48	XL	Touch panel left glass terminal.	A/D
49	YD	Touch panel bottom film terminal.	A/D
50	XR	Touch panel right glass terminal.	A/D
51	GND	Ground.	P

3.2 CTP Pin Assignment

NO.	Symbol	Description	I/O
1	GND	Ground.	P
2	VDDIO	I/O power supply voltage.	P
3	VDD	Supply voltage.	P
4	SCL	I2C clock input.	I
5	SDA	I2C data input and output.	I
6	INT	External interrupt to the host.	I
7	RST	External reset, low is active.	I
8	GND	Ground.	P

4. LCD Optical Characteristics

4.1 Optical Specifications (Reflective Mode)

Item	Symbol	Condition	Min	Typ.	Max	Unit	Note
Reflection Ratio (with polarizer)	R(%)	$\theta=0$ Normal viewing angle	1	2	--	%	
Contrast Ratio	CR		--	5	--	%	(2)
Viewing Angle	Hor.	Θ_L	--	45	--	degree	(1)(6)
		Θ_R	--	45	--		
	Ver.	Θ_T	--	45	--		
		Θ_B	--	45	--		

4.2 Optical Specifications (Transmissive Mode)

Item	Symbol	Condition	Min	Typ.	Max	Unit	Note	
Color Gamut	S(%)		--	50	--	%	(5)	
Contrast Ratio	CR		400	500	--	%	(2)	
Response Time	Rising	T_R	--	25	50	ms	(4)	
	Falling	T_F						
Color Filter Chromaticity	White	W_X	$\theta=0$ Normal viewing angle	0.2405	0.2805	0.3205	(5)(6)	
		W_Y		0.2717	0.3117	0.3517		
	Red	R_X		0.5412	0.5812	0.6212		
		R_Y		0.2999	0.3399	0.3799		
	Green	G_X		0.2919	0.3319	0.3717		
		G_Y		0.5311	0.5711	0.6111		
	Blue	B_X		0.1152	0.1552	0.1952		
		B_Y		0.0478	0.0878	0.1278		
Viewing Angle	Hor.	Θ_L	CR>10	60	80	--	degree	(1)(6)
		Θ_R		60	80	--		
	Ver.	Θ_T		60	80	--		
		Θ_B		60	80	--		
Option View Direction	SUPER WIDE						(1)	

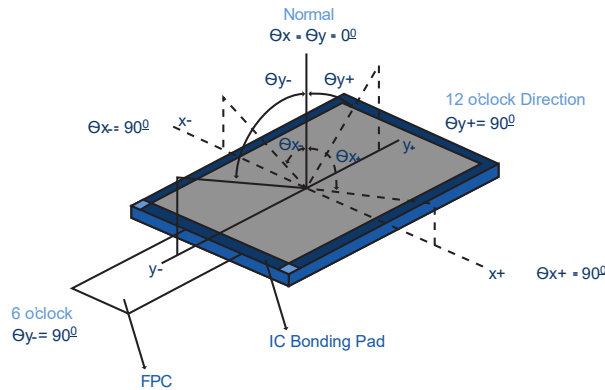
Measuring Conditions:

1. Dark Room
2. Ambient Temperature of 25±2°C
3. 15 Minute Warm up

Optical Specification Reference Notes:

(1) Definition of Viewing Angle:

The viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3,9 o'clock direction and the vertical or 6,12 o'clock direction with respect to the optical axis which is normal to the LCD surface.



(2) Definition of Contrast Ratio:

Measured at the center point of panel. The contrast ratio (Cr) measured on a module, is the ratio between the luminance (Lw) in a full white area (R=G=B=1) and the luminance (Ld) in a dark area (R=G=B=0).

$$Cr = \frac{L_w}{L_d}$$

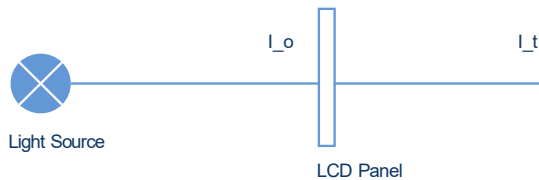
(3) Definition of Transmittance (T%):

The transmittance of the panel including the polarizers is measured with electrical driving. The equation for transmittance Tr is:

$$Tr = \frac{I_t}{I_o} \times 100\%$$

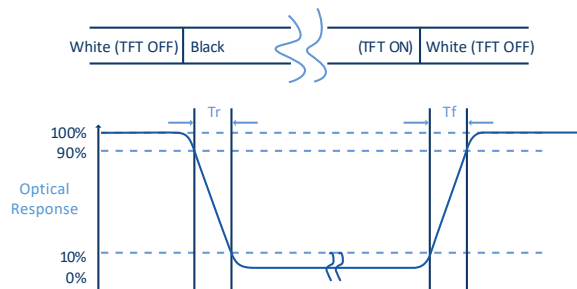
I_o = the brightness of the light source.

I_t = the brightness after panel transmission



(4) Definition of Response Time (TR, TF):

The rise time 'Tr' is defined as the time for luminance to change from 90% to 10% as a result of a change of the electrical condition. The fall time 'Tf' is defined as the time for luminance to change from 10% to 90% as a result of a change of the electrical condition.



(5) Definition of Color Gamut:

Measuring machine CFT-01. NTSC's Primaries: $R(x,y,Y)$, $G(x,y,Y)$, $B(x,y,Y)$. FPM520 of Westar Display Technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics. The color chromaticity shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.

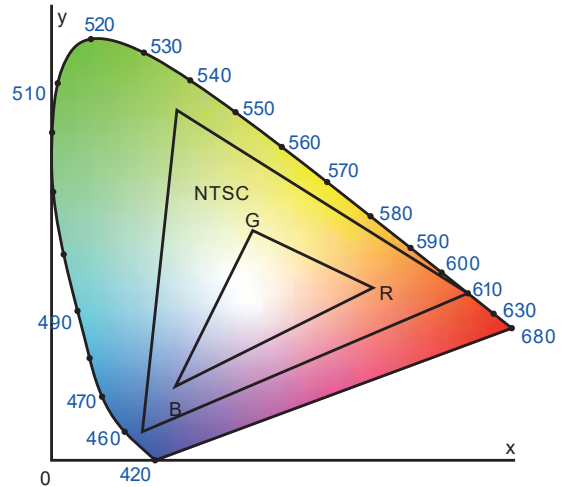
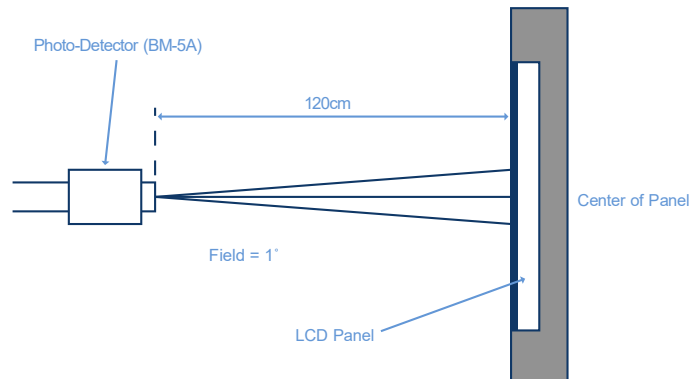
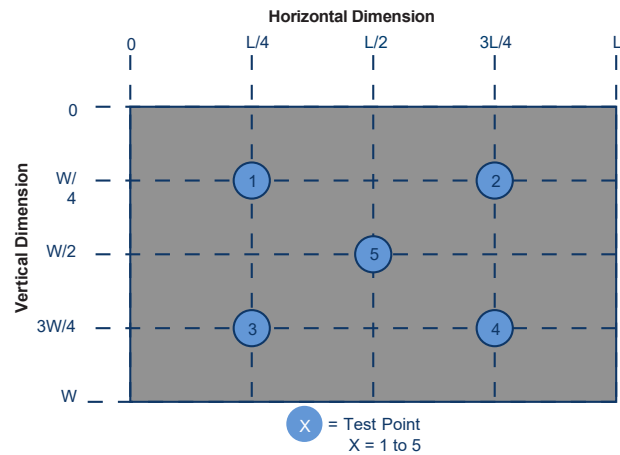
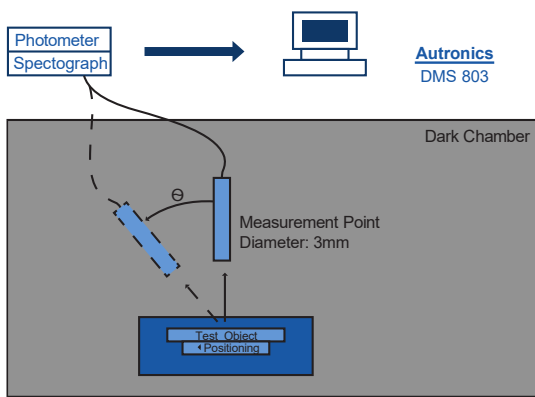


Fig. 1931 CIE Chromaticity Diagram

$$\text{Color Gamut: } S = \frac{\text{Area of RGB Triangle}}{\text{Area of NTSC Triangle}} \times 100\%$$

(6) Definition of Optical Measurement Setup:

The LCD module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 20 minutes.



5. TFT Electrical Characteristics

5.1 Absolute Maximum Rating (Ta=25°C, VSS=0V)

Characteristics	Symbol	Min	Max	Unit
Digital Supply Voltage	VCI	-0.3	3.3	V
Supply Voltage (Logic)	IOVCC	-0.3	3.3	V
Operating Temperature	T _{OP}	-30	+85	°C
Storage Temperature	T _{ST}	-40	+85	°C

NOTE: If the absolute maximum rating of the above parameters is exceeded, even momentarily, the quality of the product may be degraded. Absolute maximum ratings specify the values which the product may be physically damaged if exceeded. Be sure to use the product within the range of the absolute maximum ratings.

5.2 DC Electrical Characteristics

Characteristics	Symbol	Min	Typ.	Max	Unit	Note
Digital Supply Voltage	VCI	2.5	3.3	3.6	V	--
Supply Voltage (Logic)	IOVCC	1.65	1.8	3.3	V	--
Normal Mode Current Consumption	IDD	--	8	--	mA	--
Level Input Voltage	V _{IH}	0.7*IOVCC	--	IOVCC	V	--
	V _{IL}	-0.3	--	0.3*IOVCC	V	--
Level Output Voltage	V _{OH}	0.8*IOVCC	--	IOVCC	V	--
	V _{OL}	GND	--	0.2*IOVCC	V	--

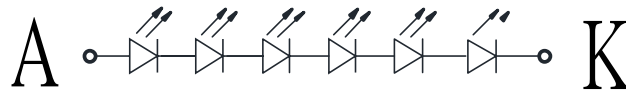
5.3 LED Backlight Characteristics

The backlight system is edge lighting type with 6 LED Chips

Item	Symbol	Min	Typ.	Max	Unit	Note
Forward Current	I_F	15	20	--	mA	--
Forward Voltage	V_F	--	19.2	--	V	--
LCM Luminance	L_V	370	420	--	cd/m ²	(3)
LED Lifetime	Hr	50000	--	--	hour	(1)(2)
Uniformity	Avg	80	--	--	%	(3)

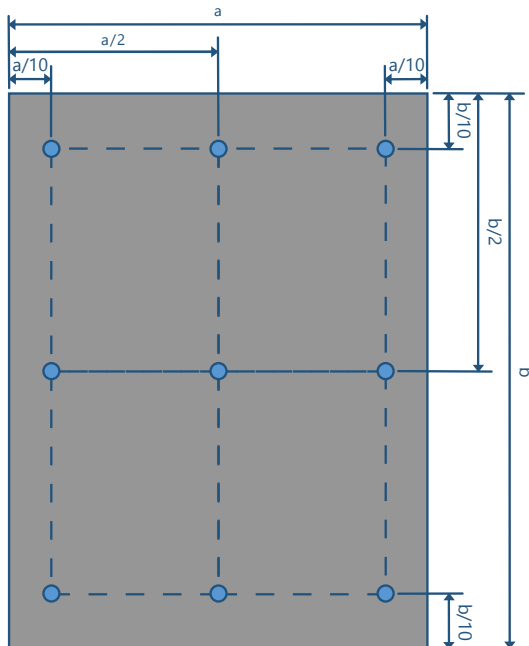
Note 1: LED lifetime (Hr) can be defined as the time in which it continues to operate under the condition: $T_a=25 \pm 3^\circ\text{C}$, typical IL value indicated in the above table until the brightness becomes less than 50%.

Note 2: The "LED lifetime" is defined as the module brightness decrease to 50% original brightness at $T_a=25^\circ\text{C}$ and $I_F = 20\text{mA}$. The LED lifetime could be decreased if operating I_F is larger than 20mA. The constant current driving method is suggested.



BL CIRCUIT DIAGRAM

Note 3: Luminance Uniformity of these 9 points is defined as below:



$$\text{Luminance} = \frac{(\text{Total Luminance of 9 Points})}{9}$$

$$\text{Uniformity} = \frac{\text{Minimum Luminance in 9 Points (1-9)}}{\text{Maximum Luminance in 9 Points (1-9)}}$$

6. TFT AC Characteristics

6.1 High Speed Data Transmission: Data-Clock Timing

For high-speed data transmission data-clock timing diagrams and tables, see table and diagram on page 330 of the data sheet for controller IC ILI9488. The data sheet can be found here:

<https://focuslcds.com/wp-content/uploads/Drivers/ILI9488.pdf>

6.2 High Speed Data Transmission

For high-speed data transmission timings diagrams and tables, see diagram and table on page 331 of the data sheet for controller IC ILI9488. The data sheet can be found here:

<https://focuslcds.com/wp-content/uploads/Drivers/ILI9488.pdf>

6.3 Turnaround Procedure

For turnaround procedure diagrams and tables, see page 331 of the data sheet for controller IC ILI9488.

The data sheet can be found here: <https://focuslcds.com/wp-content/uploads/Drivers/ILI9488.pdf>

6.4 Switching the Clock Lane Between Clock Transmission and Low-Power Mode

For switching the clock lane between clock transmission and low-power mode tables and diagrams, see page 334 of the data sheet for controller IC ILI9488. The data sheet can be found here:

<https://focuslcds.com/wp-content/uploads/Drivers/ILI9488.pdf>

6.5 Reset Timing Characteristics

For the reset timing characteristics diagrams and tables, see table and diagram on page 305 of the data sheet for controller IC ILI9488. The data sheet can be found here:

<https://focuslcds.com/wp-content/uploads/Drivers/ILI9488.pdf>

7. CTP Electrical Characteristics

7.1 Absolute Maximum Rating

Item	Symbol	Min	Max	Unit	Note
Power Supply Voltage	VDD	2.7	3.6	V	
Operating Temperature	T _{OP}	-30	+85	°C	
Storage Temperature	T _{ST}	-40	+85	°C	

7.2 DC Electrical Characteristics

Ambient temperature: 25°C, AVDD=2.8V, VDDIO=1.8V or VDDIO=AVDD

Item	Min	Typ.	Max	Unit	Note
Normal Mode Operating Current	--	11	--	mA	
Monitor Mode Operating Current	--	4.3	--	mA	
Sleep Mode Operating Current	--	42	--	uA	
Digital Input Low Voltage/V _{IL}	-0.3	--	0.3*IOVCC	V	
Digital Input High Voltage/V _{IH}	0.7*IOVCC	--	IOVCC	V	
Digital Output Low Voltage/V _{OL}	--	--	0.3*IOVCC	V	
Digital Output High Voltage/V _{OH}	0.7*IOVCC	--	--	V	

7.3 AC Characteristics

Ambient temperature: 25°C, AVDD=2.8V, VDDIO=1.8V

Parameter	Min	Typ.	Max	Unit
OSC Oscillation Frequency	49	50	51	MHZ
I/O Output Rise Time, Low to High	--	210	--	ns
I/O Output Fall Time, High to Low	--	210	--	ns

7.4 I2C Timing Characteristics

For I2C timing characteristics table, see table on page 6 of the data sheet for touch panel IC FT5436. The data sheet for the FT5436 can be found here:

<https://focuslcds.com/wp-content/uploads/Drivers/FT5436.pdf>

7.5 Power-On and Reset Timings

For power on and reset sequence timing diagrams, see diagrams and tables on pages 8-9 of the data sheet for touch panel IC FT5436. The data sheet for the FT5436 can be found here:

<https://focuslcds.com/wp-content/uploads/Drivers/FT5436.pdf>

8. Quality Inspection Standards

For TFT quality inspection standards, please see the following link:
<https://focuslcds.com/lcd-resources/tft-quality-inspection-standards/>

9. Cautions and Handling Precautions

9.1 Handling and Operating the Module

1. When the module is assembled, it should be attached to the system firmly. Do not warp or twist the module during assembly work.
2. Protect the module from physical shock or any force. In addition to damage, this may cause improper operation or damage to the module and back-light unit.
3. Note that polarizer is very fragile and could be easily damaged. Do not press or scratch the surface.
4. Do not allow drops of water or chemicals to remain on the display surface. If you have the droplets for a long time, staining and discoloration may occur.
5. If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
6. The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane. Do not use ketene type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
7. If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs, or clothes, it must be washed away thoroughly with soap.
8. Protect the module from static; it may cause damage to the CMOS ICs.
9. Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
10. Do not disassemble the module.
11. Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
12. Pins of I/F connector shall not be touched directly with bare hands.
13. Do not connect, disconnect the module in the "Power ON" condition.
14. Power supply should always be turned on/off by the item Power On Sequence & Power Off Sequence.

9.2 Storage and Transportation

1. Do not leave the panel in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 0 to 35°C and relative humidity of less than 70%.
2. Do not store the TFT-LCD module in direct sunlight.
3. The module shall be stored in a dark place. When storing the modules for a long time, be sure to adopt effective measures for protecting the modules from strong ultraviolet radiation, sunlight, or fluorescent light.
4. It is recommended that the modules should be stored under a condition where no condensation is allowed. Formation of dewdrops may cause an abnormal operation or a failure of the module. In particular, the greatest possible care should be taken to prevent any module from being operated where condensation has occurred inside.
5. This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed.