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## TFT Display Module

Part Number

*E35KA-I-CW850-C*

### Overview:

- 3.5-inch TFT (62.90x88.14mm)
- 320x480 Pixels
- 8/9/16/24-bit MCU, 3/4 SPI & 16-bit RGB selectable Interfaces
- Wide Temperature Range
- All Viewing Angles
- IPS, Transmissive
- Capacitive Touch Panel
- 1200 NITS
- TFT IC: ILI9488
- CTP IC: GT911
- RoHS Compliant

## Description

This is a color active matrix TFT (Thin Film Transistor) LCD (Liquid Crystal Display) that uses amorphous silicon TFT as a switching device. This model is composed of a transmissive type TFT- LCD Panel, driver circuit, capacitive touch panel, and backlight unit. The resolution of the 3.5" TFT- LCD contains 320x480 pixels and can display up to 16.7M colors.

## Features

Input Voltage: 3.3V

TFT Interface: 8/9/16/24-bit MCU, 3/4 SPI & 16-bit RGB selectable

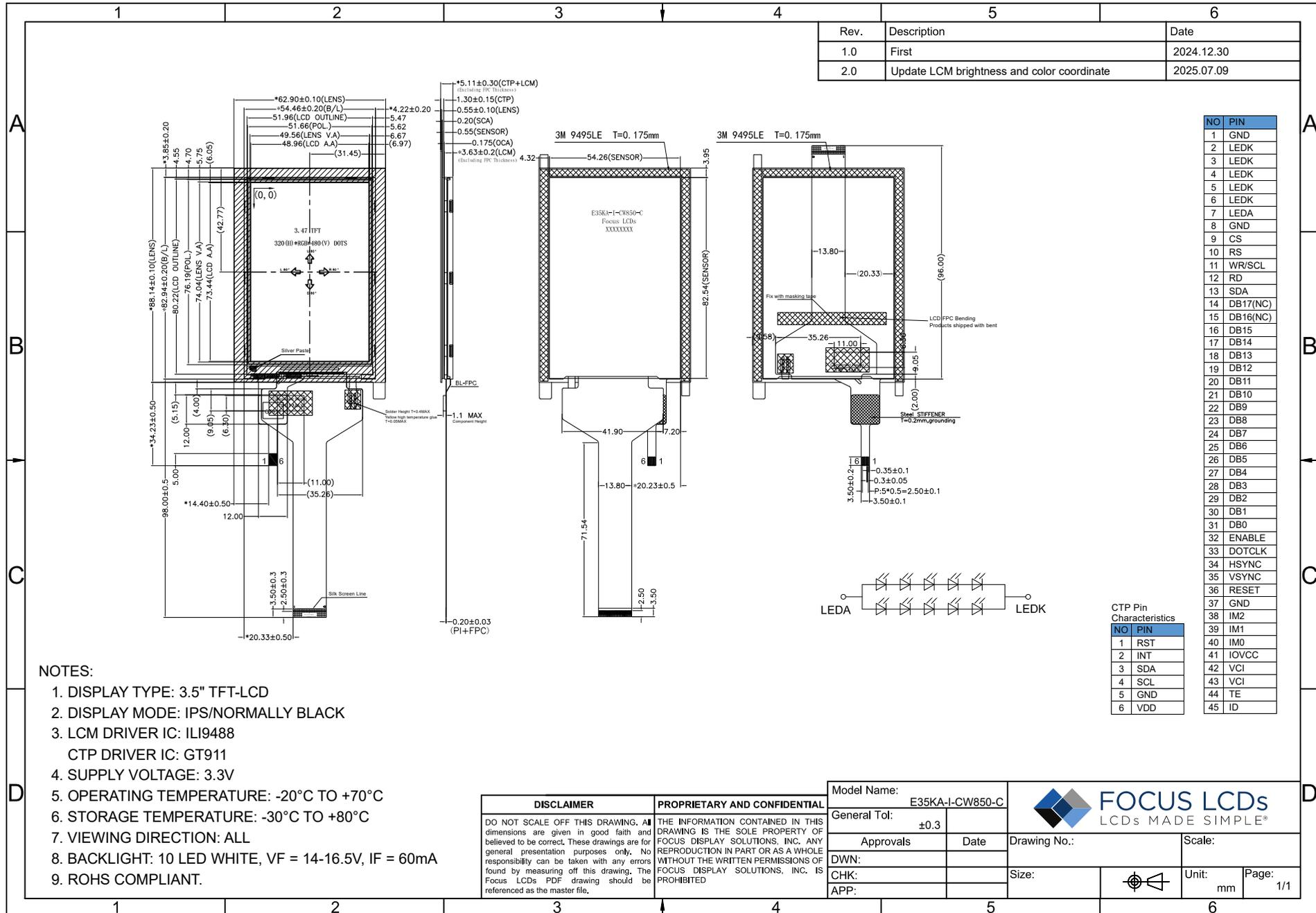
CTP Interface: I2C

General Information Items	Specification	Unit	Note
	Main Panel		
TFT Display Area (AA)	48.96(H) x 73.44(V) (3.5 inch)	mm	--
CTP View Area	49.56(H) x 74.04(V)	mm	--
Driver Element	TFT active matrix	--	--
Display Colors	16.7M	colors	--
Number of Pixels	320(RGB)x480	dots	--
TFT Pixel Arrangement	RGB vertical stripe	--	--
Pixel Pitch	0.051 (H) x 0.153 (V)	mm	--
Viewing Angle	ALL	o'clock	--
TFT Controller IC	ILI9488	--	--
CTP Driver IC	GT911	--	--
Display Mode	Transmissive/Normally Black	--	--
Touch Points	5-point and Gestures	--	--
Operating Temperature	-20 to +70	°C	--
Storage Temperature	-30 to +80	°C	--

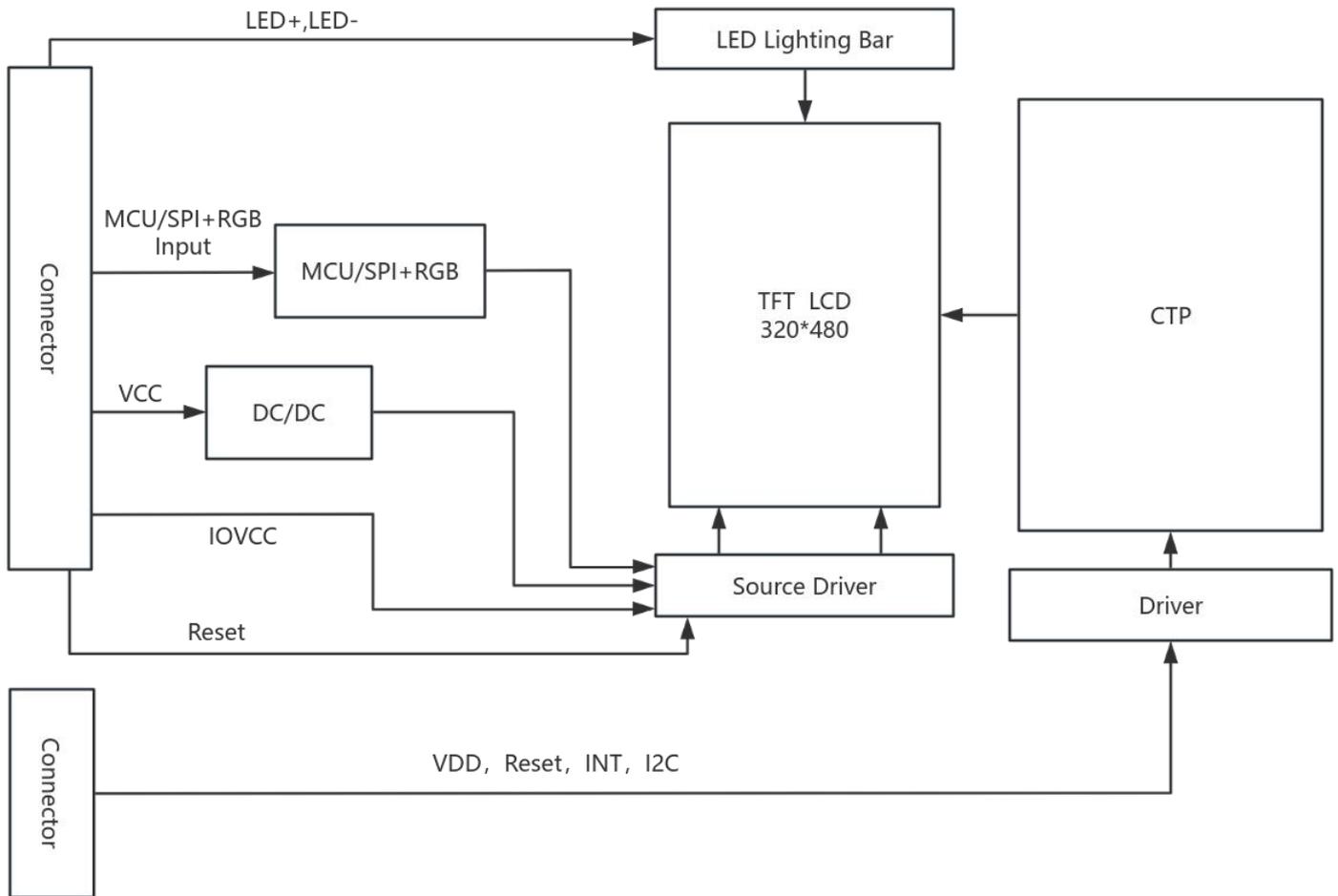
## Mechanical Information

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal (H)	--	62.90	--	mm	--
	Vertical (V)	--	88.14	--	mm	--
	Depth (D)	--	5.11	--	mm	--
Weight		--	48.10	--	g	Approximate

# 1. Outline Dimensions



## 2. Block Diagram



### 3. Input Terminal Pin Assignment

#### 3.1 TFT Pin Assignment

Recommended TFT Connector: FH12S-45S-0.5SH(55)

NO.	Symbol	Description	I/O																																				
1	GND	Ground.	P																																				
2~6	LEDK	Backlight cathode.	P																																				
7	LEDA	Backlight anode.	P																																				
8	GND	Ground.	P																																				
9	CS	Chip select input pin.	I																																				
10	RS	A register select signal.	I																																				
11	SCL/WR	Write enable clock input pin.	I																																				
12	RD	Read enable clock pin.	I																																				
13	SDA	SPI interface input pin.	I																																				
14	DB17(NC)	No connection.	--																																				
15	DB16(NC)	No connection.	--																																				
16~31	DB15~DB0	Data bus.	I/O																																				
32	ENABLE	Data enable signal for RGB interface operation.	I																																				
33	DOTCLK	Dot clock signal for RGB interface operation.	I																																				
34	HSYNC	Horizontal (Line) synchronizing input signal for RGB.	I																																				
35	VSYNC	Vertical (Frame) synchronizing input signal for RGB.	I																																				
36	RESET	Hardware reset pin.	I																																				
37	GND	Ground.	P																																				
38	IM2	Select the MCU interface mode <table border="1"> <thead> <tr> <th>IM2</th> <th>IM1</th> <th>IM0</th> <th>Description</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>0</td> <td>MIPI-DBI Type B 24-bit bus (DB_EN=1)</td> </tr> <tr> <td>0</td> <td>0</td> <td>0</td> <td>MIPI-DBI Type B 24-bit bus (DB_EN=0)</td> </tr> <tr> <td>0</td> <td>0</td> <td>1</td> <td>MIPI-DBI Type B 9-bit bus</td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>MIPI-DBI Type B 16-bit bus</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> <td>MIPI-DBI Type B 8-bit bus</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> <td>MIPI-DBI Type C Option 1 (3-Line SPI)</td> </tr> <tr> <td>1</td> <td>1</td> <td>0</td> <td>MIPI-DSI</td> </tr> <tr> <td>1</td> <td>1</td> <td>1</td> <td>MIPI-DBI Type C Option 3 (4-Line SPI)</td> </tr> </tbody> </table>	IM2	IM1	IM0	Description	0	0	0	MIPI-DBI Type B 24-bit bus (DB_EN=1)	0	0	0	MIPI-DBI Type B 24-bit bus (DB_EN=0)	0	0	1	MIPI-DBI Type B 9-bit bus	0	1	0	MIPI-DBI Type B 16-bit bus	0	1	1	MIPI-DBI Type B 8-bit bus	1	0	1	MIPI-DBI Type C Option 1 (3-Line SPI)	1	1	0	MIPI-DSI	1	1	1	MIPI-DBI Type C Option 3 (4-Line SPI)	I
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39	IM1																																						
40	IM0																																						
41	IOVCC	Power supply (1.8-3.3V).	P																																				
42	VCI	Power supply (2.8-3.3V).	P																																				
43	VCI	Power supply (2.8-3.3V).	P																																				
44	TE	Tearing effect output.	O																																				
45	ID	Obligate.	I																																				

I: Input, O: Output, P: Power

### 3.2 CTP Pin Assignment

NO.	Symbol	Description	I/O
1	RST	Reset signal input pin.	I
2	INT	CTP interrupt request.	I
3	SDA	Serial data input/output pin.	I/O
4	SCL	Serial clock signal pin.	I
5	GND	Ground.	P
6	VDD	Power supply 3.3V.	P

## 4. LCD Optical Characteristics

### 4.1 Optical Specifications

Item	Symbol	Condition	Min	Typ.	Max	Unit	Note	
Color Gamut	S(%)		--	60	--	%	(5)	
Contrast Ratio	CR		--	700	--	%	(2)	
Response Time	Rising	TR	--	25	35	ms	(4)	
	Falling	TF						
Color Filter Chromaticity	White	WX	$\theta=0$ Normal viewing angle	0.2800	0.3100	0.3400	--	(5)(6)
		WY		0.3200	0.3500	0.3800		
	Red	RX		0.6300	0.6600	0.6900		
		RY		0.2950	0.3250	0.3550		
	Green	GX		0.2470	0.2770	0.3070		
		GY		0.5380	0.5680	0.5980		
	Blue	BX		0.1150	0.1450	0.1750		
		BY		0.0420	0.0720	0.1020		
Viewing Angle	Hor.	$\Theta$ L	CR $\geq$ 10	--	80	--	degree	(1)(6)
		$\Theta$ R		--	80	--		
	Ver.	$\Theta$ T		--	80	--		
		$\Theta$ B		--	80	--		
Option View Direction	ALL						(1)	

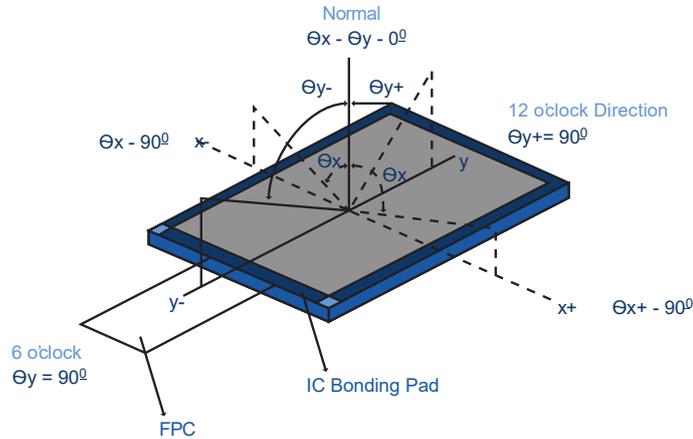
#### Measuring Conditions:

1. Dark Room
2. Ambient Temperature of 25 $\pm$ 2°C
3. 15 Minute Warm up

## Optical Specification Reference Notes:

### (1) Definition of Viewing Angle:

The viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3,9 o'clock direction and the vertical or 6,12 o'clock direction with respect to the optical axis which is normal to the LCD surface.



### (2) Definition of Contrast Ratio:

Measured at the center point of panel. The contrast ratio (Cr) measured on a module, is the ratio between the luminance (Lw) in a full white area (R=G=B=1) and the luminance (Ld) in a dark area (R=G=B=0).

$$Cr = \frac{Lw}{Ld}$$

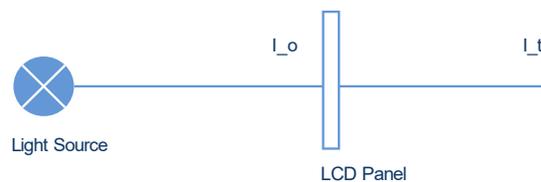
### (3) Definition of Transmittance (T%):

The transmittance of the panel including the polarizers is measured with electrical driving. The equation for transmittance Tr is:

$$Tr = \frac{I_t}{I_o} \times 100\%$$

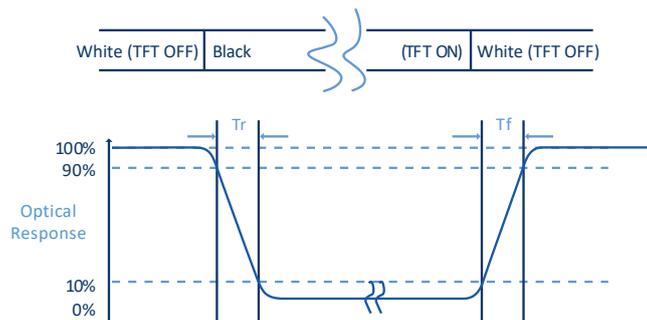
$I_o$  = the brightness of the light source.

$I_t$  = the brightness after panel transmission



### (4) Definition of Response Time (Tr, Tf):

The rise time 'Tr' is defined as the time for luminance to change from 90% to 10% as a result of a change of the electrical condition. The fall time 'Tf' is defined as the time for luminance to change from 10% to 90% as a result of a change of the electrical condition.



### (5) Definition of Color Gamut:

Measuring machine CFT-01. NTSC's Primaries: R(x,y,Y), G(x,y,Y), B(x,y,Y). FPM520 of Westar Display Technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics. The color chromaticity shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.

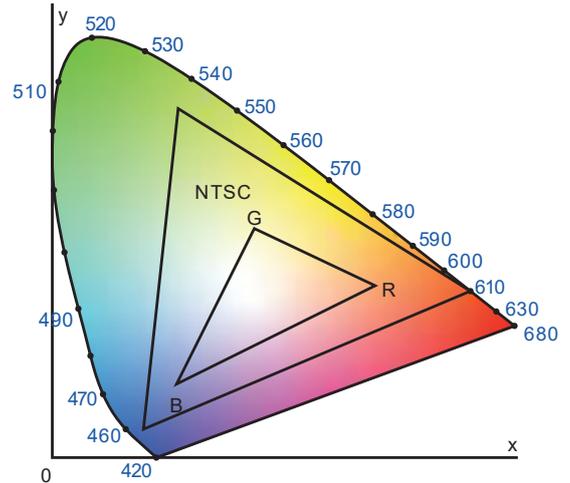
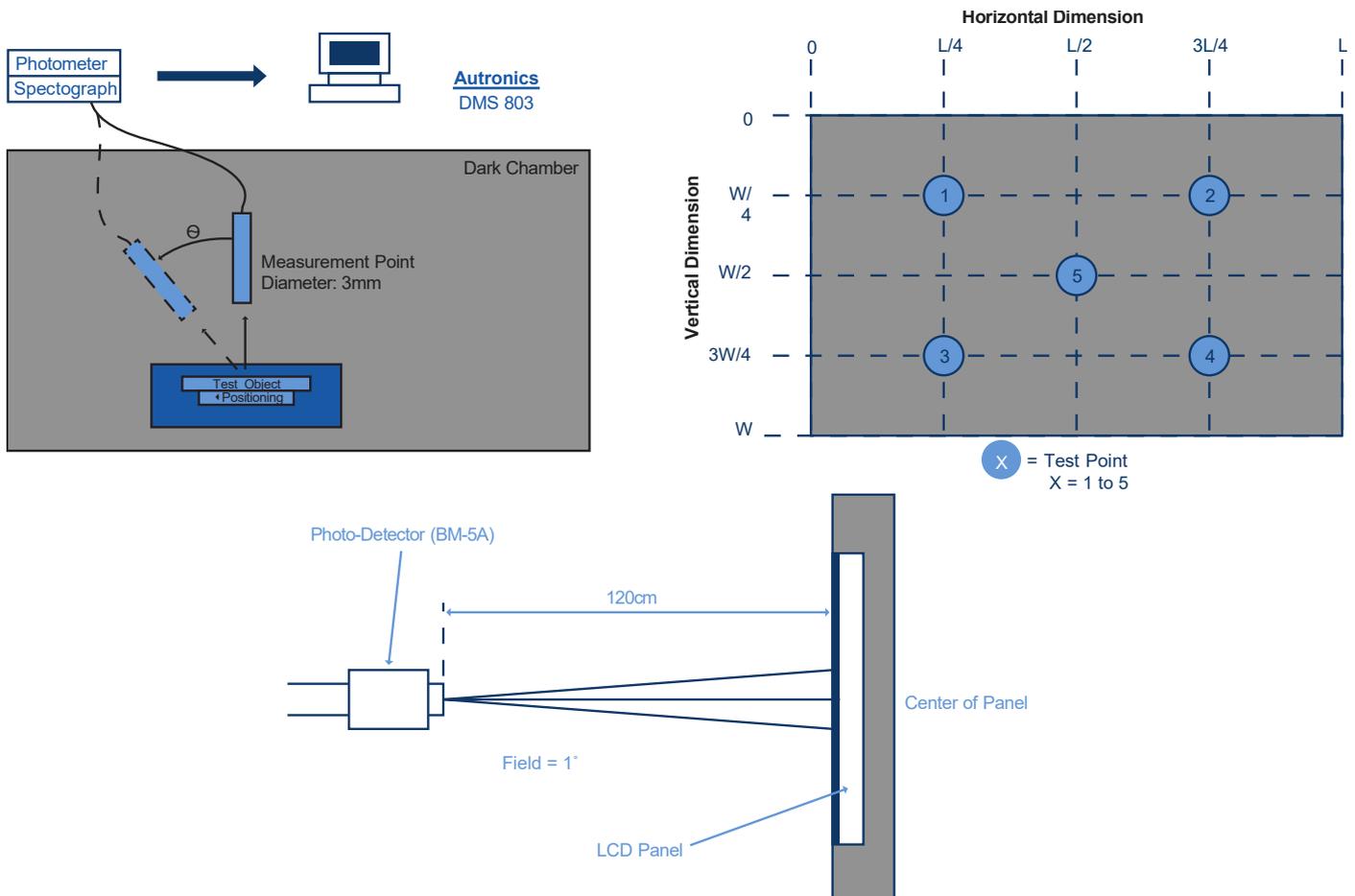


Fig. 1931 CIE Chromaticity Diagram

$$\text{Color Gamut: } S = \frac{\text{Area of RGB Triangle}}{\text{Area of NTSC Triangle}} \times 100\%$$

### (6) Definition of Optical Measurement Setup:

The LCD module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 20 minutes.



## 5. TFT Electrical Characteristics

### 5.1 Absolute Maximum Rating (Ta=25°C, VSS=0V)

Characteristics	Symbol	Min	Max	Unit
Digital Supply Voltage	VCC	0.3	3.3	V
Supply Voltage (Logic)	IOVCC	0.3	3.3	V
Operating Temperature	T <sub>OP</sub>	-20	+70	°C
Storage Temperature	T <sub>ST</sub>	-30	+80	°C

*NOTE: If the absolute maximum rating of the above parameters is exceeded, even momentarily, the quality of the product may be degraded. Absolute maximum ratings specify the values which the product may be physically damaged if exceeded. Be sure to use the product within the range of the absolute maximum ratings.*

### 5.2 DC Electrical Characteristics

Characteristics	Symbol	Min	Typ.	Max	Unit	Note
Digital Supply Voltage	VCC	2.8	--	3.3	V	--
Supply Voltage (Logic)	IOVCC	1.65	1.8	3.3	V	--
Normal Mode Current Consumption	IDD	--	15	--	mA	--
Level Input Voltage	V <sub>IH</sub>	0.7*IOVCC	--	IOVCC	V	--
	V <sub>IL</sub>	-0.3	--	0.3*IOVCC	V	--
Level Output Voltage	V <sub>OH</sub>	0.8*IOVCC	--	IOVCC	V	--
	V <sub>OL</sub>	0	--	0.2*IOVCC	V	--

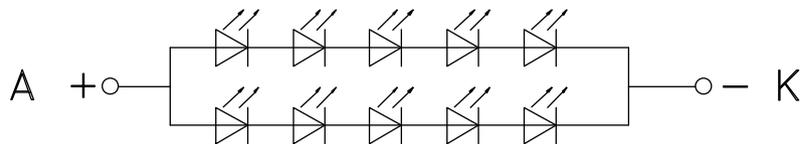
### 5.3 LED Backlight Characteristics

The backlight system is edge lighting type with 10 LED Chips

Item	Symbol	Min	Typ.	Max	Unit	Note
Forward Current	$I_F$	--	60	--	mA	--
Forward Voltage	$V_F$	14.0	--	16.5	V	--
LCM Luminance	$L_v$	950	1200	--	cd/m <sup>2</sup>	(3)
LED Lifetime	$H_r$	30000	--	--	hour	(1)(2)
Uniformity	Avg	--	80	--	%	(3)

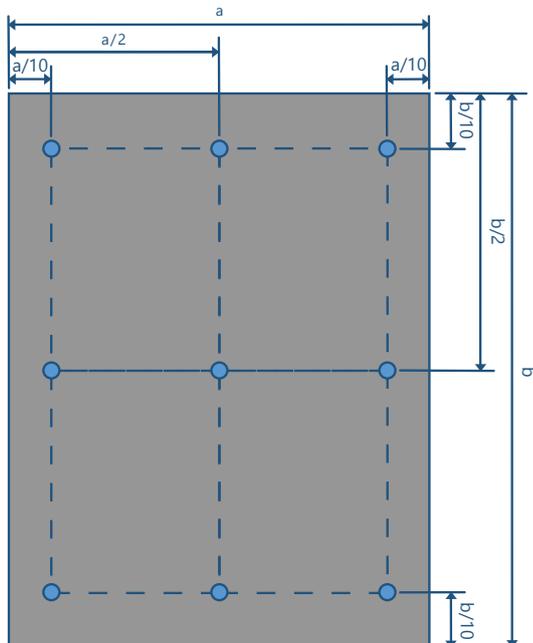
**Note 1:** LED lifetime ( $H_r$ ) can be defined as the time in which it continues to operate under the condition:  $T_a=25 \pm 3^\circ\text{C}$ , typical  $I_L$  value indicated in the above table until the brightness becomes less than 50%.

**Note 2:** The "LED lifetime" is defined as the module brightness decrease to 50% original brightness at  $T_a=25^\circ\text{C}$  and  $I_F = 60\text{mA}$ . The LED lifetime could be decreased if operating  $I_F$  is larger than 60mA. The constant current driving method is suggested.



**BL CIRCUIT DIAGRAM**

**Note 3:** Luminance Uniformity of these 9 points is defined as below:



$$\text{Luminance} = \frac{(\text{Total Luminance of 9 Points})}{9}$$

$$\text{Uniformity} = \frac{\text{Minimum Luminance in 9 Points (1-9)}}{\text{Maximum Luminance in 9 Points (1-9)}}$$

## 6. TFT AC Characteristics

### 6.1 High Speed Data Transmission: Data-Clock Timing

For high-speed data transmission data-clock timing diagrams and tables, see table and diagram on page 330 of the data sheet for controller IC ILI9488. The data sheet can be found here:

<https://focuslcds.com/wp-content/uploads/Drivers/ILI9488.pdf>

### 6.2 High Speed Data Transmission

For high-speed data transmission timings diagrams and tables, see diagram and table on page 331 of the data sheet for controller IC ILI9488. The data sheet can be found here:

<https://focuslcds.com/wp-content/uploads/Drivers/ILI9488.pdf>

### 6.3 Turnaround Procedure

For turnaround procedure diagrams and tables, see page 72 of the data sheet for controller IC ILI9488. The data sheet can be found here: <https://focuslcds.com/wp-content/uploads/Drivers/ILI9488.pdf>

### 6.4 Switching the Clock Lane Between Clock Transmission and Low-Power Mode

For switching the clock lane between clock transmission and low-power mode tables and diagrams, see page 334 of the data sheet for controller IC ILI9488. The data sheet can be found here:

<https://focuslcds.com/wp-content/uploads/Drivers/ILI9488.pdf>

### 6.5 Reset Timing Characteristics

For the reset timing characteristics diagrams and tables, see table and diagram on page 305 of the data sheet for controller IC ILI9488. The data sheet can be found here:

<https://focuslcds.com/wp-content/uploads/Drivers/ILI9488.pdf>

## 7. CTP Electrical Characteristics

### 7.1 Absolute Maximum Rating

Item	Symbol	Min	Max	Unit	Note
Power Supply Voltage	VDD	-0.3	3.47	V	
Operating Temperature	T <sub>OP</sub>	-20	+70	°C	
Storage Temperature	T <sub>ST</sub>	-30	+80	°C	

### 7.2 DC Electrical Characteristics

Ambient temperature: 25°C, AVDD=2.8V, VDDIO=1.8V or VDDIO=AVDD

Item	Min	Typ.	Max	Unit	Note
Operating Current	--	12.82	--	mA	
Digital Input Low Voltage/VIL	-0.3	--	0.25*VDDIO	V	
Digital Input High Voltage/VIH	0.75*VDDIO	--	VDDIO+0.3	V	
Digital Output Low Voltage/VOL	--	--	0.15*VDDIO	V	
Digital Output High Voltage/VOH	0.85*VDDIO	--	--	V	

### 7.3 AC Characteristics

Ambient temperature: 25°C, AVDD=2.8V, VDDIO=1.8V

Parameter	Min	Typ.	Max	Unit
OSC Oscillation Frequency	59	60	61	MHZ
I/O Output Rise Time, Low to High	--	14	--	ns
I/O Output Fall Time, High to Low	--	14	--	ns

### 7.4 I2CTiming

For I2C timing diagrams and tables, see page 9 of the data sheet for touch panel IC GT911. The data sheet for GT911 can be found here: <https://focuslcds.com/content/GT911.pdf>

## 7.5 Power-On Timing

For the power on timing diagram, see the first diagram on page 10 of the data sheet for touch panel IC GT911. The data sheet for GT911 can be found here: <https://focuslcds.com/content/GT911.pdf>

## 7.6 Timing for Host Resetting GT911

For the host resetting timing diagram, see the second diagram on page 10 of the data sheet for touch panel IC GT911. The data sheet for GT911 can be found here: <https://focuslcds.com/content/GT911.pdf>

## 7.7 Timing for Setting Slave Address to 0x28/0x29

For this setting slave address timing diagram, see the third diagram on page 10 of the data sheet for touch panel IC GT911. The data sheet for GT911 can be found here: <https://focuslcds.com/content/GT911.pdf>

## 7.8 Timing for Setting Slave Address to 0xBA/0xBB

For this setting slave address timing diagram and instructions, see pages 11-12 of the data sheet for touch panel IC GT911. The data sheet for GT911 can be found here: <https://focuslcds.com/content/GT911.pdf>

## 8. Quality Inspection Standards

For TFT quality inspection standards, please see the following link: <https://focuslcds.com/lcd-resources/tft-quality-inspection-standards/>

## 9. Cautions and Handling Precautions

### 9.1 Handling and Operating the Module

1. When the module is assembled, it should be attached to the system firmly. Do not warp or twist the module during assembly work.
2. Protect the module from physical shock or any force. In addition to damage, this may cause improper operation or damage to the module and back-light unit.
3. Note that polarizer is very fragile and could be easily damaged. Do not press or scratch the surface.
4. Do not allow drops of water or chemicals to remain on the display surface. If you have the droplets for a long time, staining and discoloration may occur.
5. If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
6. The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane. Do not use ketene type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
7. If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs, or clothes, it must be washed away thoroughly with soap.
8. Protect the module from static; it may cause damage to the CMOS ICs.
9. Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
10. Do not disassemble the module.
11. Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
12. Pins of I/F connector shall not be touched directly with bare hands.
13. Do not connect, disconnect the module in the "Power ON" condition.
14. Power supply should always be turned on/off by the item Power On Sequence & Power Off Sequence.

### 9.2 Storage and Transportation

1. Do not leave the panel in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 0 to 35 °C and relative humidity of less than 70%.
2. Do not store the TFT-LCD module in direct sunlight.
3. The module shall be stored in a dark place. When storing the modules for a long time, be sure to adopt effective measures for protecting the modules from strong ultraviolet radiation, sunlight, or fluorescent light.
4. It is recommended that the modules should be stored under a condition where no condensation is allowed. Formation of dewdrops may cause an abnormal operation or a failure of the module. In particular, the greatest possible care should be taken to prevent any module from being operated where condensation has occurred inside.
5. This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed.