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E43RC1-I-LS950-R

Product Description

- | | |
|--|---|
| <ul style="list-style-type: none">• 4.3-inch TFT (105.40x67.15mm)• 800x480 Pixels• 6-Bit LVDS Interface• Special Temperature Range• All Viewing Directions | <ul style="list-style-type: none">• IPS, Transmissive• Resistive Touch Panel• 950 NITS• TFT IC: ST72611• RoHS Compliant |
|--|---|

Revision History

Date	Rev. No	Page	Summary
07/31/2026	1.0	All	First issue

Description

This is a color active matrix TFT (Thin Film Transistor) LCD (Liquid Crystal Display) that uses amorphous silicon TFT as a switching device. This model is composed of a transmissive type TFT-LCD Panel, driver circuit, 4-wire resistive touch panel, and backlight unit. The resolution of the 4.3" TFT-LCD contains 800x480 pixels and can display up to 16.7M colors.

Features

Input Voltage: 3.3V

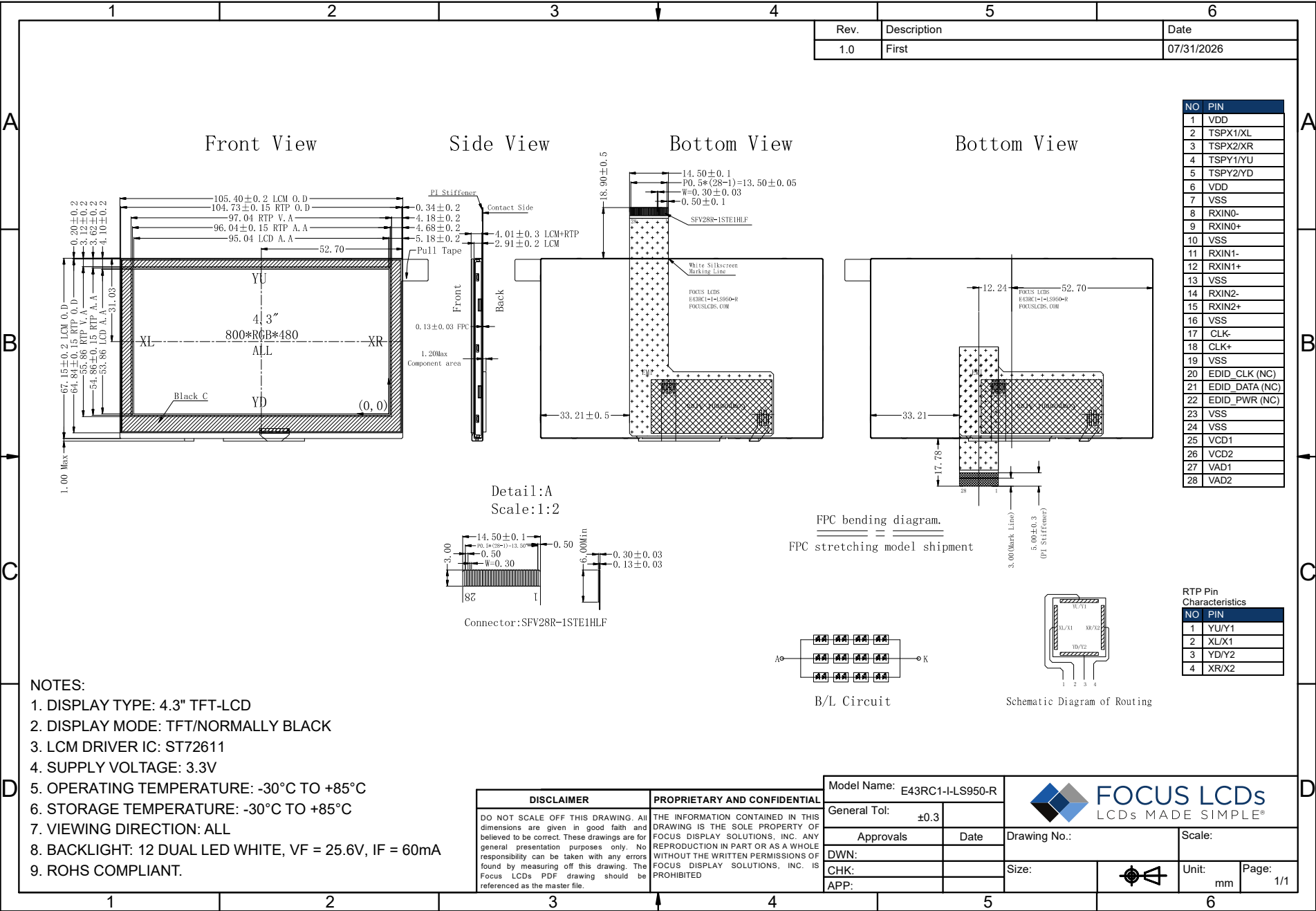
TFT Interface: 6-Bit LVDS

General Information Items	Specification	Unit	Note
	Main Panel		
TFT Display Area (AA)	95.04(H) x 53.86(V) (4.3 inch)	mm	--
RTP View Area	97.04(H) x 55.86(V)	mm	--
Driver Element	TFT active matrix	--	--
Display Colors	16.7M	colors	--
Number of Pixels	800(RGB)x480	dots	--
TFT Pixel Arrangement	RGB vertical stripe	--	--
Pixel Pitch	0.1188 (H) x 0.1122 (V)	mm	--
Viewing Angle	ALL	o'clock	--
TFT Controller IC	ST72611	--	--
Display Mode	Transmissive/Normally Black	--	--
Operating Temperature	-30 to +85	°C	--
Storage Temperature	-30 to +85	°C	--

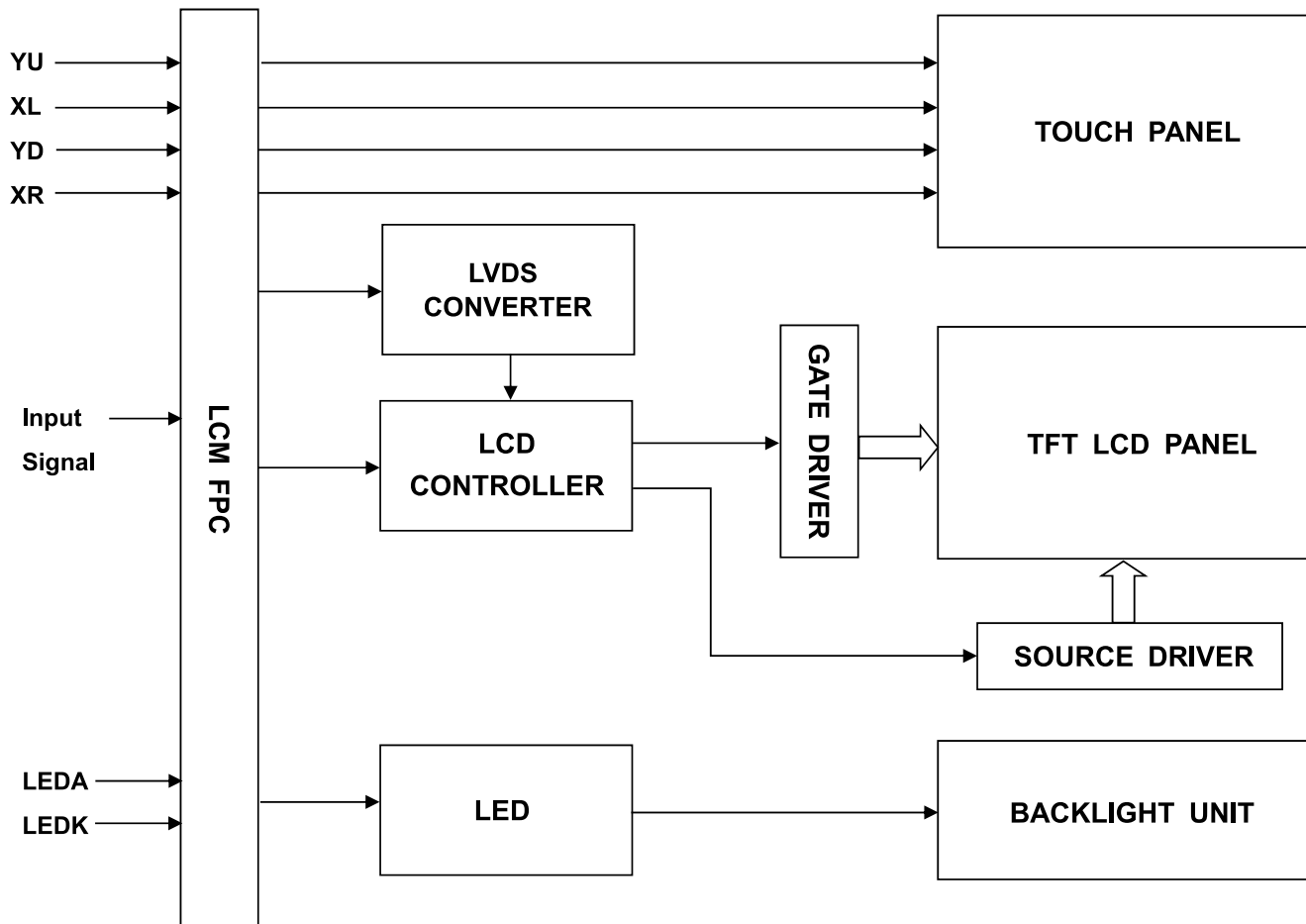
Mechanical Information

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal (H)	--	105.40	--	mm	--
	Vertical (V)	--	67.15	--	mm	--
	Depth (D)	--	4.01	--	mm	--
Weight		--	52	--	g	Approximate

1. Outline Dimensions



2. Block Diagram



3. Input Terminal Pin Assignment

3.1 TFT Pin Assignment

NO.	Symbol	Description	I/O
1	VDD	Supply voltage (3.3V).	P
2	TSPX1/XL	Touch screen signal: X_Left.	I
3	TSPX2/XR	Touch screen signal: X_Right.	I
4	TSPY1/YU	Touch screen signal: Y_Up.	I
5	TSPY2/YD	Touch screen signal: Y_Down.	I
6	VDD	Supply voltage (3.3V).	P
7	VSS	Ground.	P
8	RXIN0-	- LVDS differential data input.	I
9	RXIN0+	+ LVDS differential data input.	I
10	VSS	Ground.	P
11	RXIN1-	- LVDS differential data input.	I
12	RXIN1+	+ LVDS differential data input.	I
13	VSS	Ground.	P
14	RXIN2-	- LVDS differential data input.	I
15	RXIN2+	+ LVDS differential data input.	I
16	VSS	Ground.	P
17	CLK-	- LVDS differential clock input.	I
18	CLK+	+ LVDS differential clock input.	I
19	VSS	Ground.	P
20	EDID_CLK	No connection.	--
21	EDID_DATA	No connection.	--
22	EDID_PWR	No connection.	--
23	VSS	Ground.	P
24	VSS	Ground.	P
25	VCD1	LED power supply (cathode 1).	P
26	VCD2	LED power supply (cathode 2).	P
27	VAD1	LED power supply (anode 1).	P
28	VAD2	LED power supply (anode 2).	P

3.2 RTP Pin Assignment

NO.	Symbol	Description	I/O
1	XL	Touch panel left glass terminal.	A/D
2	YD	Touch panel bottom film terminal.	A/D
3	XR	Touch panel right glass terminal.	A/D
4	YU	Touch panel top film terminal.	A/D

4. LCD Optical Characteristics

4.1 Optical Specifications

Item		Symbol	Condition	Min	Typ.	Max	Unit	Note
Color Gamut		S(%)		--	50	--	%	(5)
Contrast Ratio		CR	θ=0 Normal viewing angle	--	1200	--	%	(2)
Response Time	Rising	T _R		--	30	--	ms	(4)
	Falling	T _F						
Color Filter Chromaticity	White	W _X		0.2978	0.3178	0.3378		(5)(6)
		W _Y		0.3292	0.3492	0.3692		
	Red	R _X		0.5622	0.6022	0.6422		
		R _Y		0.3280	0.3680	0.4080		
	Green	G _X		0.3030	0.3430	0.3830		
		G _Y		0.5184	0.5584	0.5984		
	Blue	B _X		0.1118	0.1518	0.1918		
		B _Y	0.0852	0.1252	0.1652			
Viewing Angle	Hor.	Θ _L	CR>10	--	80	--	degree	(1)(6)
		Θ _R		--	80	--		
	Ver.	Θ _T		--	80	--		
		Θ _B		--	80	--		
Option View Direction		ALL						(1)

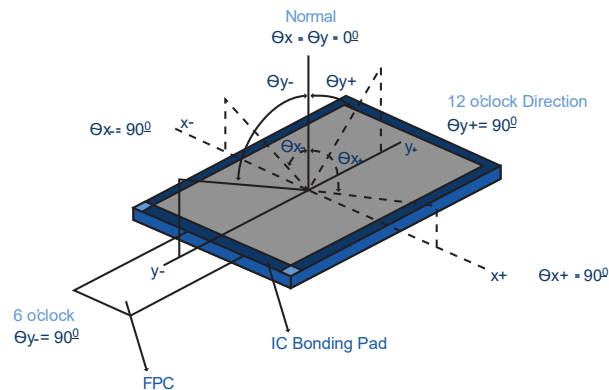
Measuring Conditions:

1. Dark Room
2. Ambient Temperature of $25\pm 2^\circ\text{C}$
3. 15 Minute Warm up

Optical Specification Reference Notes:

(1) Definition of Viewing Angle:

The viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3,9 o'clock direction and the vertical or 6,12 o'clock direction with respect to the optical axis which is normal to the LCD surface.



(2) Definition of Contrast Ratio:

Measured at the center point of panel. The contrast ratio (Cr) measured on a module, is the ratio between the luminance (Lw) in a full white area (R=G=B=1) and the luminance (Ld) in a dark area (R=G=B=0).

$$Cr = \frac{Lw}{Ld}$$

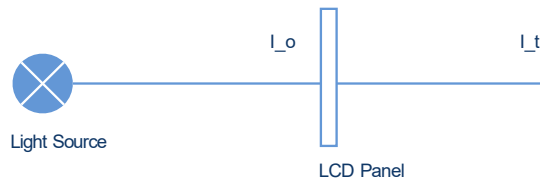
(3) Definition of Transmittance (T%):

The transmittance of the panel including the polarizers is measured with electrical driving. The equation for transmittance Tr is:

$$Tr = \frac{It}{Io} \times 100\%$$

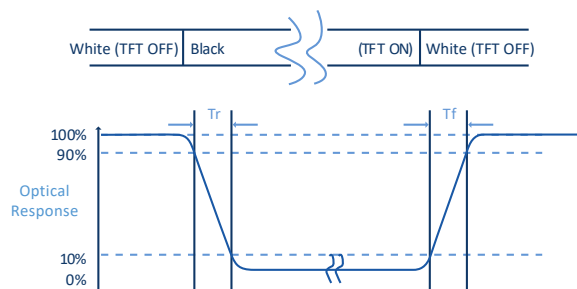
Io = the brightness of the light source.

It = the brightness after panel transmission



(4) Definition of Response Time (TR, TF):

The rise time 'Tr' is defined as the time for luminance to change from 90% to 10% as a result of a change of the electrical condition. The fall time 'Tf' is defined as the time for luminance to change from 10% to 90% as a result of a change of the electrical condition.



(5) Definition of Color Gamut:

Measuring machine CFT-01. NTSC's Primaries: $R(x,y,Y)$, $G(x,y,Y)$, $B(x,y,Y)$. FPM520 of Westar Display Technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics. The color chromaticity shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.

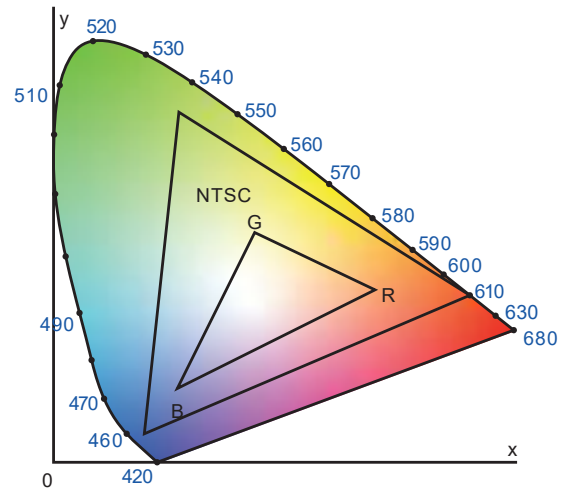
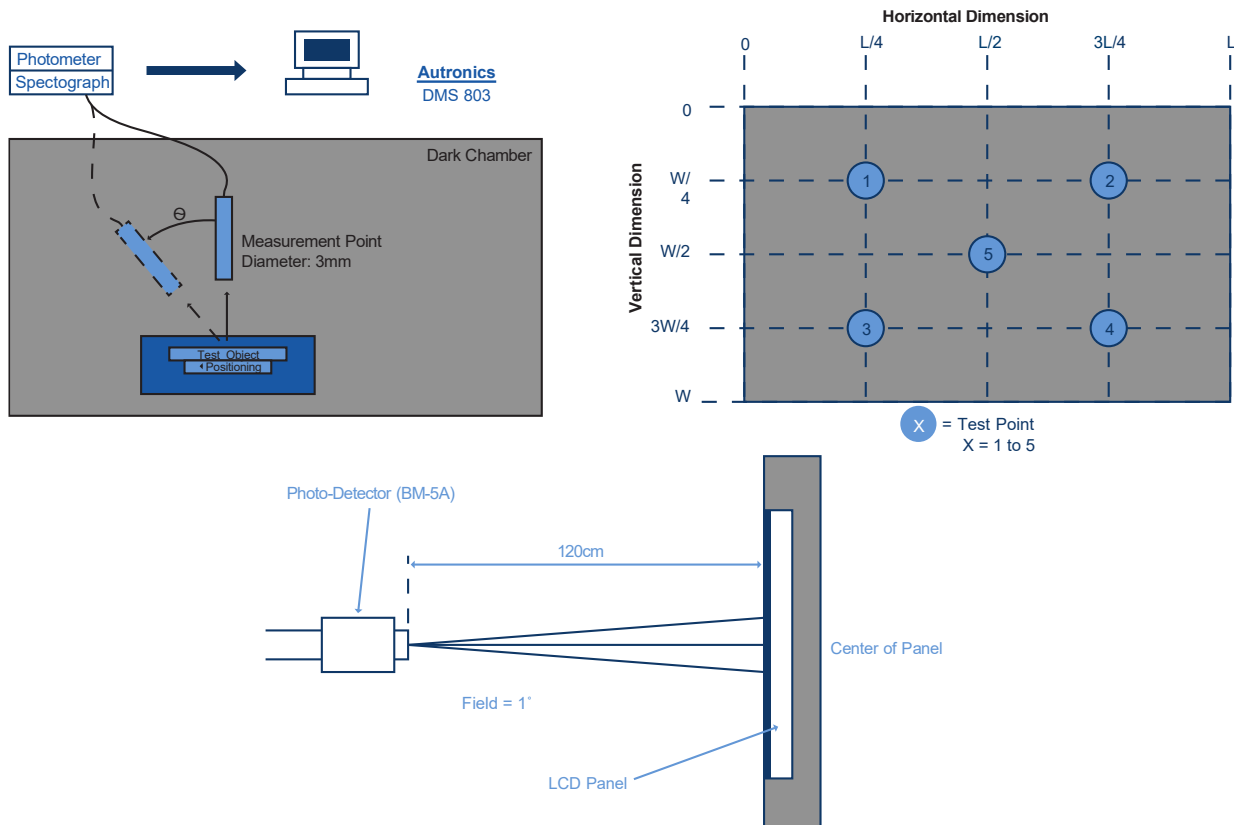


Fig. 1931 CIE Chromaticity Diagram

$$\text{Color Gamut: } S = \frac{\text{Area of RGB Triangle}}{\text{Area of NTSC Triangle}} \times 100\%$$

(6) Definition of Optical Measurement Setup:

The LCD module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 20 minutes.



5. TFT Electrical Characteristics

5.1 Absolute Maximum Rating (Ta=25°C, VSS=0V)

Characteristics	Symbol	Min	Max	Unit
Digital Supply Voltage	VDD	-0.3	4.0	V
Operating Temperature	T _{OP}	-30	+85	°C
Storage Temperature	T _{ST}	-30	+85	°C

NOTE: If the absolute maximum rating of the above parameters is exceeded, even momentarily, the quality of the product may be degraded. Absolute maximum ratings specify the values which the product may be physically damaged if exceeded. Be sure to use the product within the range of the absolute maximum ratings.

5.2 DC Electrical Characteristics

Characteristics	Symbol	Min	Typ.	Max	Unit	Note
Digital Supply Voltage	VDD	3.0	3.3	3.6	V	--
Normal Mode Current Consumption	IDD	--	40	80	mA	--
Level Input Voltage	V _{IH}	0.7*VDD	--	VDD	V	--
	V _{IL}	GND	--	0.3*VDD	V	--
Level Output Voltage	V _{OH}	VDD-0.4	--	VDD	V	--
	V _{OL}	GND	--	GND+0.4	V	--

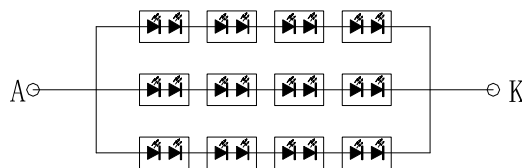
5.3 LED Backlight Characteristics

The backlight system is edge lighting type with 12 Dual LED Chips

Item	Symbol	Min	Typ.	Max	Unit	Note
Forward Current	I_F	--	60	--	mA	--
Forward Voltage	V_F	--	25.6	--	V	--
LCM Luminance	L_V	850	950	--	cd/m ²	(3)
LED Lifetime	Hr	--	50000	--	hour	(1)(2)
Uniformity	Avg	80	--	--	%	(3)

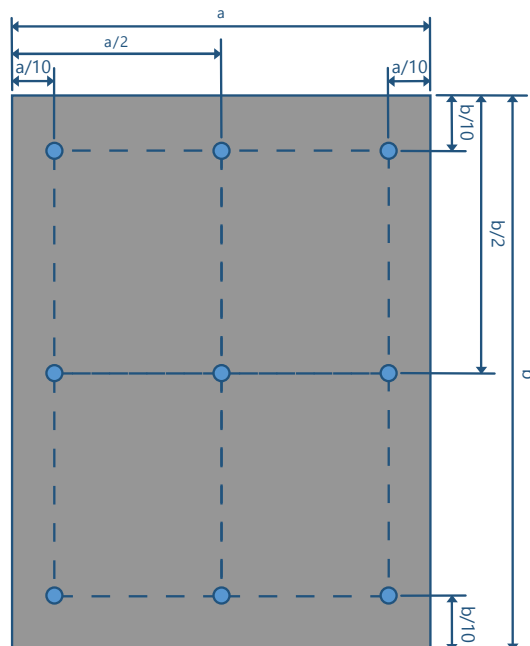
Note 1: LED lifetime (Hr) can be defined as the time in which it continues to operate under the condition: $T_a=25 \pm 3^\circ\text{C}$, typical IL value indicated in the above table until the brightness becomes less than 50%.

Note 2: The "LED lifetime" is defined as the module brightness decrease to 50% original brightness at $T_a=25^\circ\text{C}$ and $I_F = 60\text{mA}$. The LED lifetime could be decreased if operating I_F is larger than 60mA. The constant current driving method is suggested.



BL CIRCUIT DIAGRAM

Note 3: Luminance Uniformity of these 9 points is defined as below:

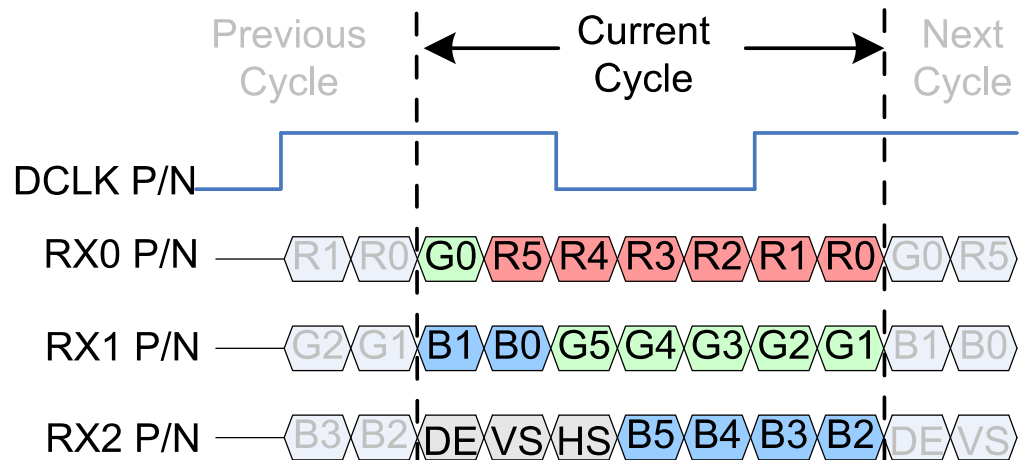


$$\text{Luminance} = \frac{(\text{Total Luminance of 9 Points})}{9}$$

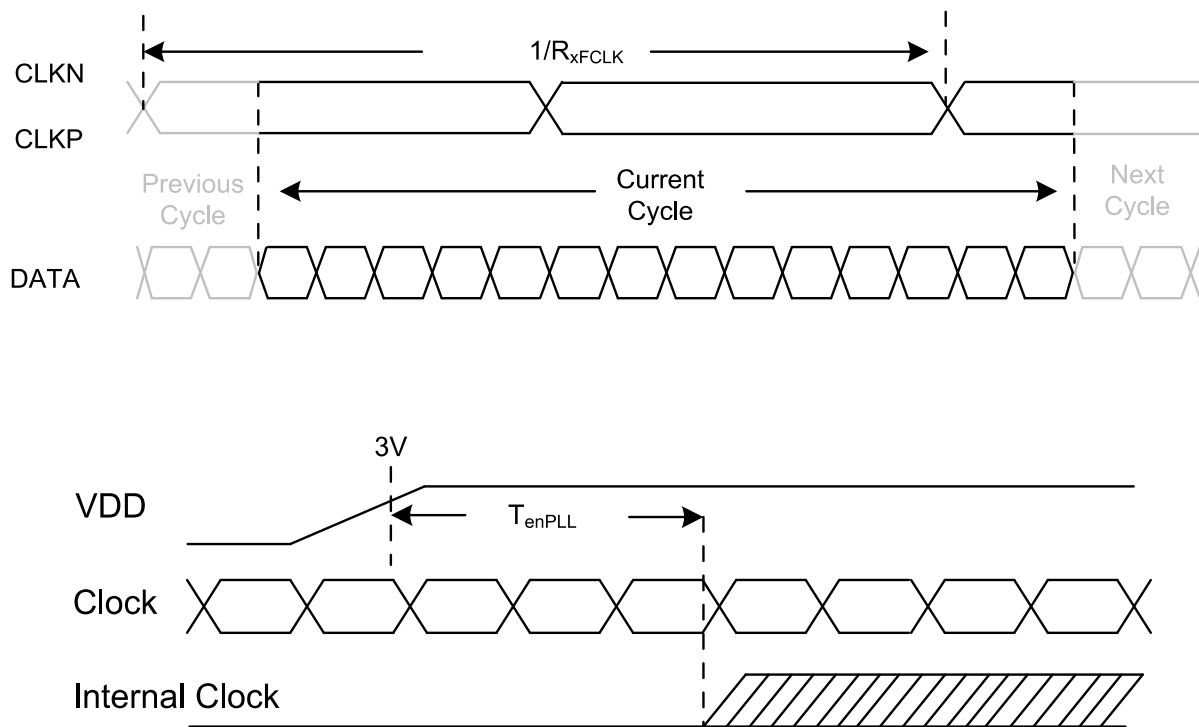
$$\text{Uniformity} = \frac{\text{Minimum Luminance in 9 Points (1-9)}}{\text{Maximum Luminance in 9 Points (1-9)}}$$

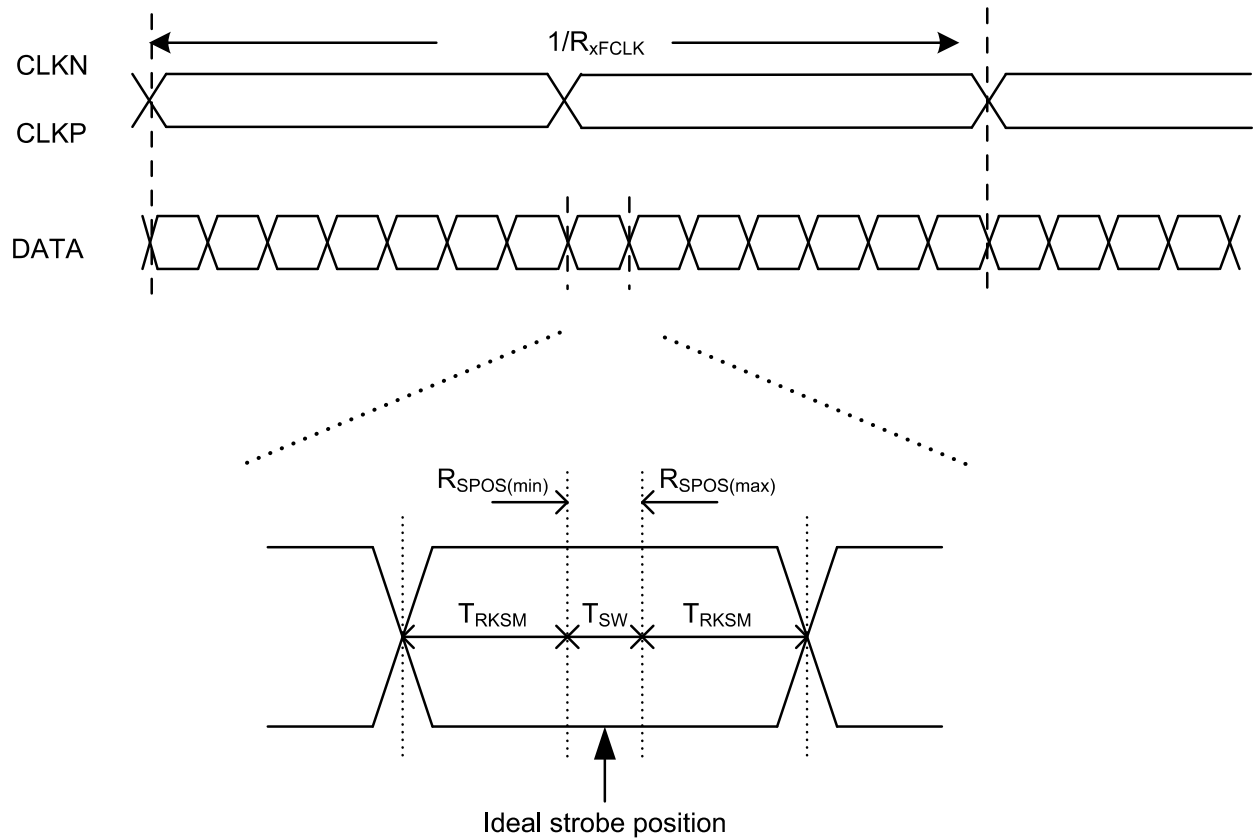
6. TFT AC Characteristics

6.1 LVDS Interface - 3 Lane Vesa Data Format Color Bit Map



6.2 System Bus Timing for LVDS Interface





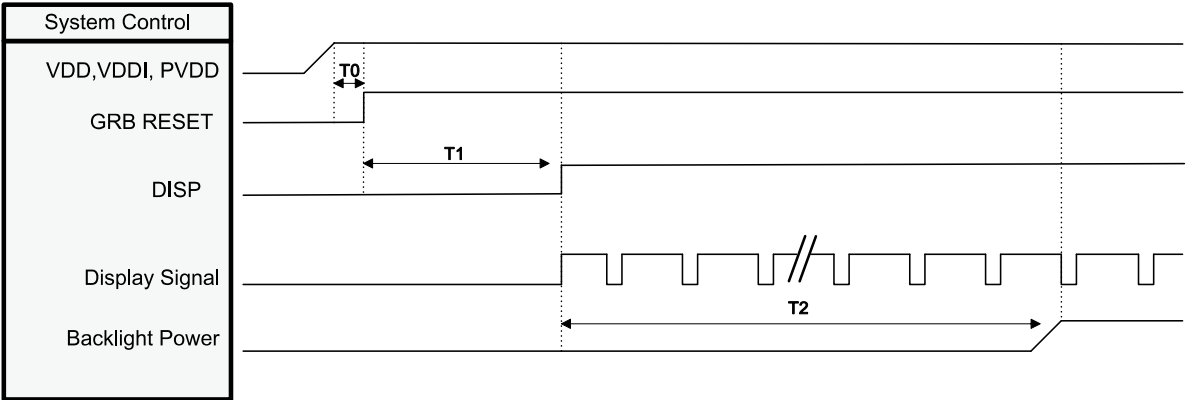
T_{RKSM} : Receiver strobe margin
 R_{SPOS} : Receiver strobe position
 T_{SW} : Strobe width (internal DATA sampling window)

LVDS Input Timing (PVDD=VDD=VDDI= 3.3V, AGND= 0V, TA=25°C).

Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
Clock Frequency	R_{XCLK}	23	25	27	MHz	
Input Data Skew Margin	T_{RSKM}	400	-	-	ps	
Clock High Time	T_{LVCH}	$4/(7 \times R_{XCLK})$			ns	
Clock Low Time	T_{LVCL}	$3/(7 \times R_{XCLK})$			ns	
PLL Wake-up Time	T_{enPLL}	-	-	150	us	
LVDS Spread Spectrum Clocking (SSC) Tolerance of LVDS Receiver						
Modulation Frequency	SSC_{MF}	-	-	100	KHz	
Modulation Rate	SSC_{MR}	-	-	+/-3	%	

Note. The maximum R_{XCLK} Frequency is 27MHz.

6.3 Power On Sequence

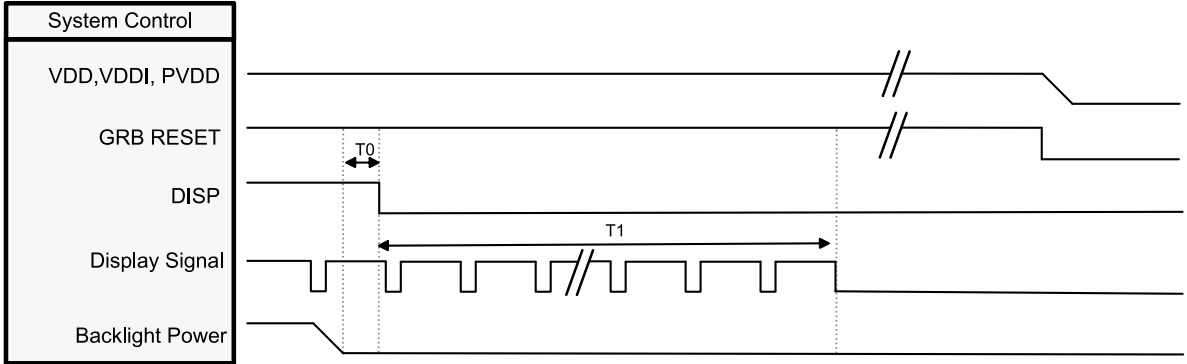


Symbol	Description	Min. Time	Unit
T0	System power stability to GRB RESET signal	0	ms
T1	GRB RESET= "High" to DISP="High"	10	ms
T2	Display Signal output to Backlight Power on	250	ms

Note: RGB interface Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0]

Note: LVDS interface Display signal: DCLK P/N; RX[3:0]P/N

6.4 Power Off Sequence



Symbol	Description	Min. Time	Unit
T0	Backlight Power off to DISP="Low"	5	ms
T1	DISP="Low" to IC internal voltage discharge complete	100	ms

Note: RGB interface Display signal: DCLK; VSYNC; HSYNC; DE; DR[7:0]; DG[7:0]; DB[7:0]

Note: LVDS interface Display signal: DCLK P/N; RX[3:0]P/N

7. Quality Inspection Standards

For TFT quality inspection standards, please see the following link:

<https://focuslcds.com/lcd-resources/tft-quality-inspection-standards/>

8. Cautions and Handling Precautions

8.1 Handling and Operating the Module

1. When the module is assembled, it should be attached to the system firmly. Do not warp or twist the module during assembly work.
2. Protect the module from physical shock or any force. In addition to damage, this may cause improper operation or damage to the module and back-light unit.
3. Note that polarizer is very fragile and could be easily damaged. Do not press or scratch the surface.
4. Do not allow drops of water or chemicals to remain on the display surface. If you have the droplets for a long time, staining and discoloration may occur.
5. If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
6. The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane. Do not use ketene type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
7. If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs, or clothes, it must be washed away thoroughly with soap.
8. Protect the module from static; it may cause damage to the CMOS ICs.
9. Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
10. Do not disassemble the module.
11. Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
12. Pins of I/F connector shall not be touched directly with bare hands.
13. Do not connect, disconnect the module in the "Power ON" condition.
14. Power supply should always be turned on/off by the item Power On Sequence & Power Off Sequence.

8.2 Storage and Transportation

1. Do not leave the panel in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 0 to 35°C and relative humidity of less than 70%.
2. Do not store the TFT-LCD module in direct sunlight.
3. The module shall be stored in a dark place. When storing the modules for a long time, be sure to adopt effective measures for protecting the modules from strong ultraviolet radiation, sunlight, or fluorescent light.
4. It is recommended that the modules should be stored under a condition where no condensation is allowed. Formation of dewdrops may cause an abnormal operation or a failure of the module. In particular, the greatest possible care should be taken to prevent any module from being operated where condensation has occurred inside.
5. This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed.