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TFT | OLED | CHARACTER | GRAPHIC | UWVD | SEGMENT | CUSTOM

TFT Display Module

Part Number

E45RA-I-RW500-N

Overview:

- 4.5-inch TFT (61.54x110.09mm)
- 480x854 Pixels
- 3SPI + 16/18/24-bit RGB Interface
- Wide Temperature Range
- All Viewing Angles

- IPS, Transmissive
- No Touch Panel
- 500 NITS
- TFT IC: ILI9806E
- RoHS Compliant

Description

This is a color active matrix TFT (Thin Film Transistor) LCD (Liquid Crystal Display) that uses amorphous silicon TFT as a switching device. This model is composed of a transmissive type TFT-LCD Panel, driver circuit, back-light unit. The resolution of a 4.5" TFT-LCD contains 480x854 pixels, and can display up to 16.7M colors.
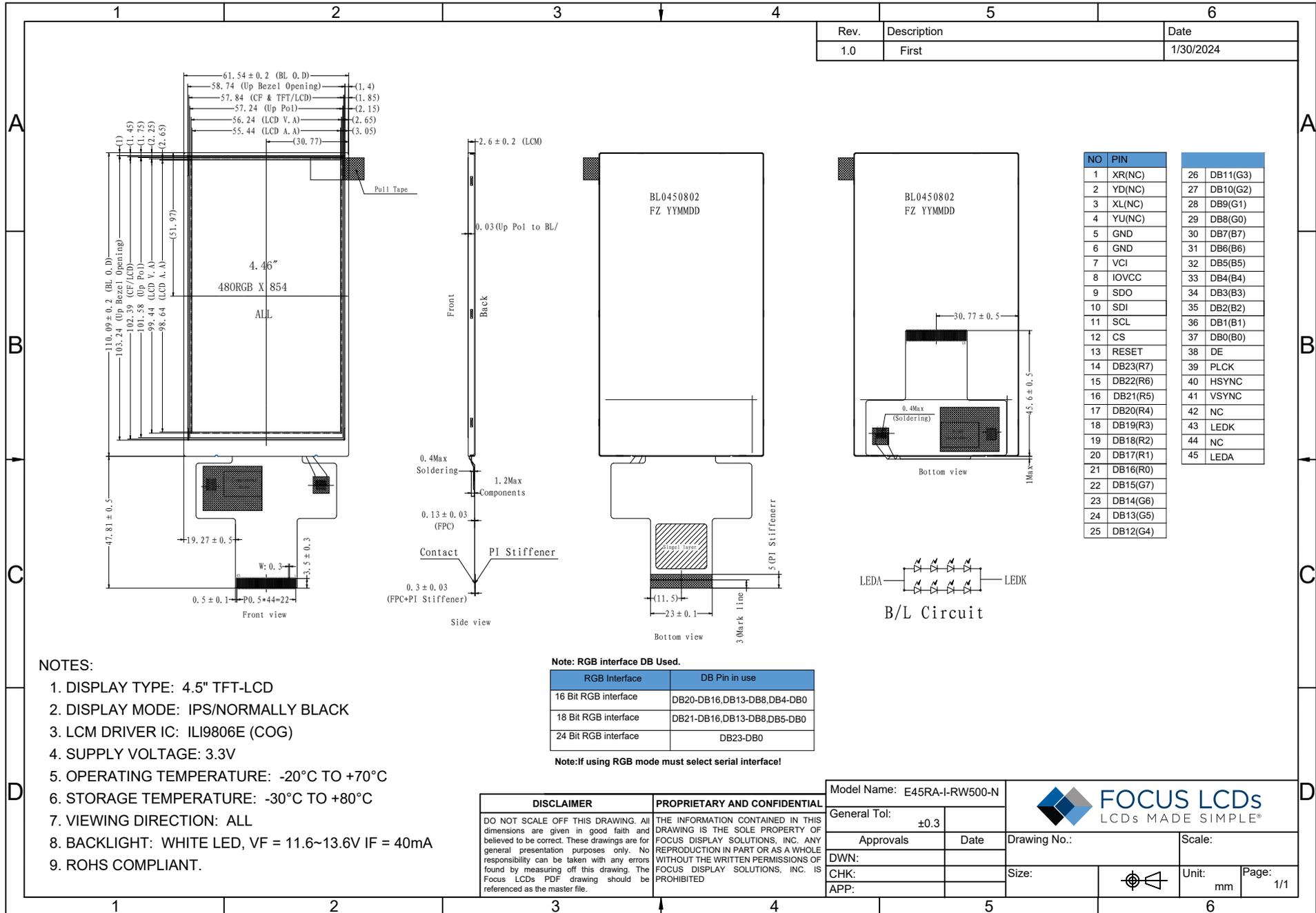
Features

Input Voltage: 3.3V
TFT Interface: 3-SPI+16/18/24-bit RGB

General Information Items	Specification	Unit	Note
	Main Panel		
TFT Display Area (AA)	55.44(H) x 98.64(V) (4.5 inch)	mm	--
Driver Element	TFT active matrix	--	--
Display Colors	16.7M	colors	--
Number of Pixels	480(RGB)x854	dots	--
TFT Pixel Arrangement	RGB vertical stripe	--	--
Pixel Pitch	0.1155 (H) x 0.1155 (V)	mm	--
Viewing Angle	ALL	o'clock	--
TFT Controller IC	ILI9806E	--	--
Display Mode	Transmissive/Normally Black	--	--
Operating Temperature	-20 to +70	°C	--
Storage Temperature	-30 to +80	°C	--

Mechanical Information

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal (H)	--	61.54	--	mm	--
	Vertical (V)	--	110.09	--	mm	--
	Depth (D)	--	2.60	--	mm	--
Weight		--	33	--	g	--

1. Outline Dimensions

Rev.	Description	Date
1.0	First	1/30/2024

Dimension callouts (Front view):

- 61.54 ± 0.2 (BL O.D)
- 58.74 (Up Bezel Opening) (1.4)
- 57.84 (CF & TFT/LCD) (1.85)
- 57.24 (Up Pol) (2.15)
- 56.24 (LCD V.A) (2.65)
- 55.44 (LCD A.A) (3.05)
- (30.77)
- (1) (1.45) (1.75) (2.25) (2.65)
- (51.97)
- 110.09 ± 0.2 (BL O.D)
- 103.24 (Up Bezel Opening)
- 102.39 (CF/LCD)
- 101.58 (Up Pol)
- 99.44 (LCD V.A)
- 98.64 (LCD A.A)

Pull Tape

4.46"
480RGB X 854
ALL

- 47.81 ± 0.5
- 19.27 ± 0.5
- 3.5 ± 0.3
- W: 0.3
- 0.5 ± 0.1 — P0.5*44=22

Front view

Side view:
- 2.6 ± 0.2 (LCM)
- 0.03 (Up Pol to BL/)
- Front / Back
- 0.4Max Soldering
- 1.2Max Components
- 0.13 ± 0.03 (FPC)
- 0.3 ± 0.03 (FPC+PI Stiffener)
- Contact / PI Stiffener

Back / Bottom view:
- BL0450802 FZ YYMMDD
- 30.77 ± 0.5
- 45.6 ± 0.5
- 0.4Max (Soldering)
- 1Max
- Singel layer
- .5 (PI Stiffenerr)
- (11.5)
- 23 ± 0.1
- 3-Mark line
- Bottom view
- LEDA — LEDK
- B/L Circuit

Pin table:

NO	PIN		NO	PIN
1	XR(NC)		26	DB11(G3)
2	YD(NC)		27	DB10(G2)
3	XL(NC)		28	DB9(G1)
4	YU(NC)		29	DB8(G0)
5	GND		30	DB7(B7)
6	GND		31	DB6(B6)
7	VCI		32	DB5(B5)
8	IOVCC		33	DB4(B4)
9	SDO		34	DB3(B3)
10	SDI		35	DB2(B2)
11	SCL		36	DB1(B1)
12	CS		37	DB0(B0)
13	RESET		38	DE
14	DB23(R7)		39	PLCK
15	DB22(R6)		40	HSYNC
16	DB21(R5)		41	VSYNC
17	DB20(R4)		42	NC
18	DB19(R3)		43	LEDK
19	DB18(R2)		44	NC
20	DB17(R1)		45	LEDA
21	DB16(R0)			
22	DB15(G7)			
23	DB14(G6)			
24	DB13(G5)			
25	DB12(G4)			

Note: RGB interface DB Used.

RGB Interface	DB Pin in use
16 Bit RGB interface	DB20-DB16,DB13-DB8,DB4-DB0
18 Bit RGB interface	DB21-DB16,DB13-DB8,DB5-DB0
24 Bit RGB interface	DB23-DB0

Note:If using RGB mode must select serial interface!

NOTES:

1. DISPLAY TYPE: 4.5" TFT-LCD
2. DISPLAY MODE: IPS/NORMALLY BLACK
3. LCM DRIVER IC: Ili9806E (COG)
4. SUPPLY VOLTAGE: 3.3V
5. OPERATING TEMPERATURE: -20°C TO +70°C
6. STORAGE TEMPERATURE: -30°C TO +80°C
7. VIEWING DIRECTION: ALL
8. BACKLIGHT: WHITE LED, VF = 11.6~13.6V IF = 40mA
9. ROHS COMPLIANT.

Model Name: E45RA-I-RW500-N

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General Tol: ±0.3

Approvals	Date
DWN:	
CHK:	
APP:	

Drawing No.:
Size:
Scale:
Unit: mm
Page: 1/1

2. Block Diagram

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3. Input Terminal Pin Assignment

3.1 TFT Pin Assignment

Recommended TFT Connector: FH12S-45S-0.5SH(55)

NO.	Symbol	Description	I/O
1	XR(NC)	--	--
2	YD(NC)	--	--
3	XL(NC)	--	--
4	YU(NC)	--	--
5	GND	Ground.	P
6	GND	Ground.	P
7	VCI	Supply voltage (3.3V).	P
8	IOVCC	I/O power supply voltage.	P
9	SDO	Serial data output pin in serial bus system interface. If not used, please open this pin.	O
10	SDI	Serial data input pin used for the SPI Interface. SDI: Serial data input pin. SDA: Serial data input/output bidirectional pin.	I
11	SCL	Serial clock input.	I
12	CS	Chip select signal. Low: chip can be accessed High: chip cannot be accessed.	I
13	RESET	Reset pin. Setting either pin low initializes the LSI. Must be reset after power is supplied.	I
14~37	DB23-DB16 (R7-R0) DB15-DB8 (G7-G0) DB7-DB0 (B7-B0)	Data bus PINS. When operating in DPI interface mode, RGB data bus. 24-bit bus: use DB23-DB0 18-bit bus: use DB0-DB5, DB8-DB13, DB16-DB21 16-bit bus: use DB0-DB4, DB8-DB13, DB16-DB20 If not using PINS, please must connect to GND.	I/O
38	DE	A DATA ENABLE signal for DPI I/F mode.	I
39	PCLK	A DATA ENABLE signal for DPI I/F mode.	I
40	HSYNC	Line synchronizing signal.	I
41	VSYNC	Serves VS signal pin on RGB interface. (Input pad).	I
42	NC	--	--
43	LEDK	Cathode pin of backlight.	P
44	NC	--	--
45	LEDA	Anode pin of backlight.	P

I: Input, O: Output, P: Power

4. LCD Optical Characteristics

4.1   Optical Specifications

Item		Symbol	Condition	Min	Typ.	Max	Unit	Note
Color Gamut		S(%)		--	62	--	%	(5)
Transmittance		T%		--	4.25	--	%	
Contrast Ratio		CR		800	1000	--	%	(2)
Response Time	Rising	TR		--	15	25	ms	(4)
	Falling	TF						
Color Filter Chromaticity	White	WX	θ=0 Normal viewing angle	0.2657	0.3057	0.3457		(5)(6)
		WY		0.2948	0.3348	0.3748		
	Red	RX		0.5840	0.6240	0.6640		
		RY		0.3070	0.3470	0.3870		
	Green	GX		0.2840	0.3240	0.3640		
		GY		0.5452	0.5852	0.6252		
	Blue	BX		0.1079	0.1479	0.1879		
		BY		0.0215	0.0615	0.1015		
Viewing Angle	Hor.	ΘL	CR≥10	70	80	--	degree	(1)(6)
		ΘR		70	80	--		
	Ver.	ΘT		70	80	--		
		ΘB		70	80	--		
Option View Direction		ALL						(1)

Measuring Conditions:

1. Dark Room
2. Ambient Temperature of 25±2°C
3. 15 Minute Warm up

Optical Specification Reference Notes:

(1) Definition of Viewing Angle:

The viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3,9 o'clock direction and the vertical or 6,12 o'clock direction with respect to the optical axis which is normal to the LCD surface.
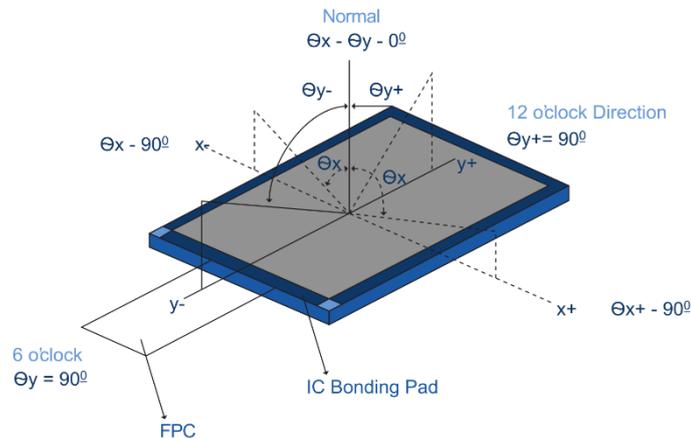
(2) Definition of Contrast Ratio::

Measured at the center point of panel. The contrast ratio (Cr) measured on a module, is the ratio between the luminance (Lw) in a full white area (R=G=B=1) and the luminance (Ld) in a dark area (R=G=B=0).

$$Cr = \frac{Lw}{Ld}$$

(3) Definition of Transmittance(T%):

The transmittance of the panel including the polarizers is measured with electrical driving. The equation for transmittance Tr is:

$$Tr = \frac{It}{Io} \ x \ 100\%$$

Io = the brightness of the light source.

It = the brightness after panel transmission
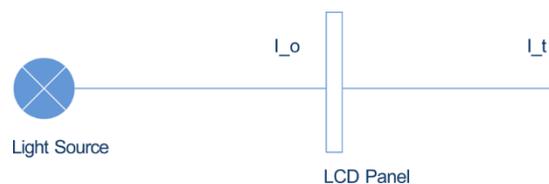
(4) Definition of Response Time(T_R, T_F):

The rise time 'Tr' is defined as the time for luminance to change from 90% to 10% as a result of a change of the electrical condition. The fall time 'Tf' is defined as the time for luminance to change from 10% to 90% as a result of a change of the electrical condition.

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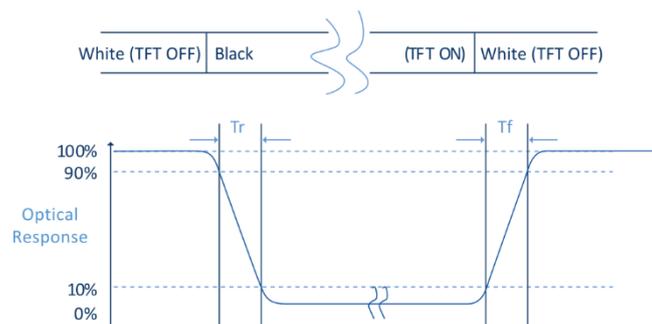
(5) Definition of Color Gamut:

Measuring machine CFT-01. NTSC's Primaries: R(x,y,Y),G(x,y,Y), B(x,y,Y). FPM520 of Westar Display Technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.The color chromaticity shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
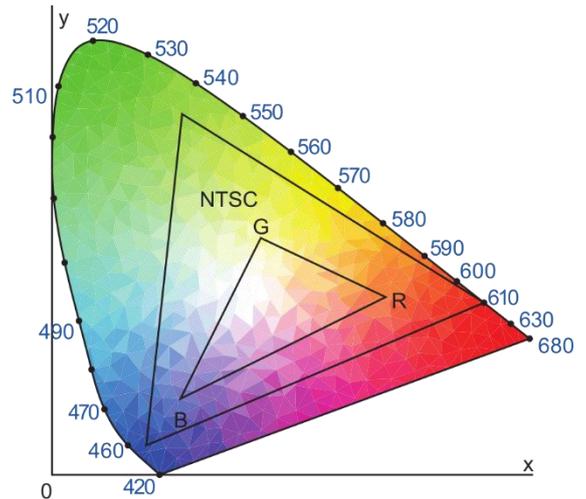

Fig. 1931 CIE Chromacity Diagram

Color Gamut: $S = \dfrac{\text{Area of RGB Triangle}}{\text{Area of NTSC Triangle}} \times 100\%$

(6) Definition of Optical Measurement Setup:

The LCD module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 20 minutes.
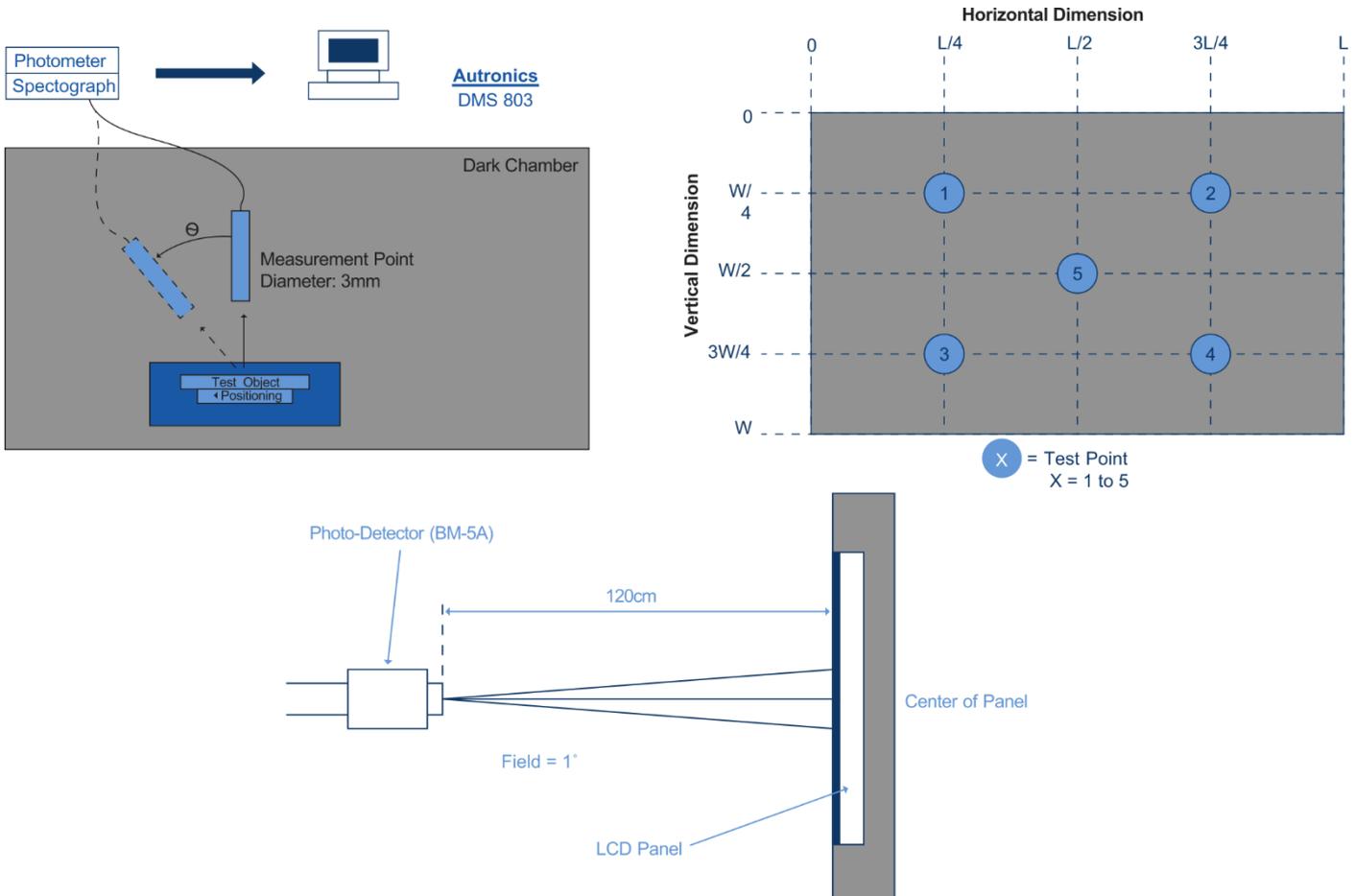
5. TFT Electrical Characteristics

5.1   Absolute Maximum rating (Ta=25°C, VSS=0V)

Characteristics	Symbol	Min	Max	Unit
Digital Supply Voltage	VCI	-0.3	4.6	V
Supply Voltage (Logic)	IOVCC	-0.3	4.6	V
Operating Temperature	TOP	-20	+70	°C
Storage Temperature	TST	-30	+80	°C

NOTE: If the absolute maximum rating of the above parameters is exceeded, even momentarily, the quality of the product may be degraded. Absolute maximum ratings specify the values which the product may be physically damaged if exceeded. Be sure to use the product within the range of the absolute maximum ratings.

5.2   DC Electrical Characteristics

Characteristics	Symbol	Min	Typ.	Max	Unit	Note
Digital Supply Voltage	VCI	2.5	2.8/3.3	3.6	V	--
Supply Voltage (Logic)	IOVCC	1.65	1.8	3.6	V	--
Normal Mode Current Consumption	IDD	--	35	70	mA	--
Level Input Voltage	VIH	0.7*IOVCC	--	IOVCC	V	--
	VIL	-0.3	--	0.3*IOVCC	V	--
Level Output Voltage	VOH	0.8*IOVCC	--	IOVCC	V	--
	VOL	GND	--	0.2*IOVCC	V	--

## 5.3   LED Backlight Characteristics

*The backlight system is edge lighting type with 8 LED Chips*

Item	Symbol	Min	Typ.	Max	Unit	Note
Forward Current	I_F	--	40	--	mA	--
Forward Voltage	V_F	11.6	12.8	13.6	V	--
LCM Luminance	L_V	450	500	--	cd/m2	IF=40mA (3)
LED Lifetime	H_r	--	50000	--	hour	(1)(2)
Uniformity	Avg	80	--	--	%	(3)

Note 1: *LED lifetime (Hr) can be defined as the time in which it continues to operate under the condition: Ta=25 ±3°C, typical IL value indicated in the above table until the brightness becomes less than 50%.*

Note 2: *The "LED lifetime" is defined as the module brightness decrease to 50% original brightness at Ta=25° C and IF = 40mA. The LED lifetime could be decreased if operating IF is larger than 40mA. The constant current driving method is suggested.*

BL CIRCUIT DIAGRAM
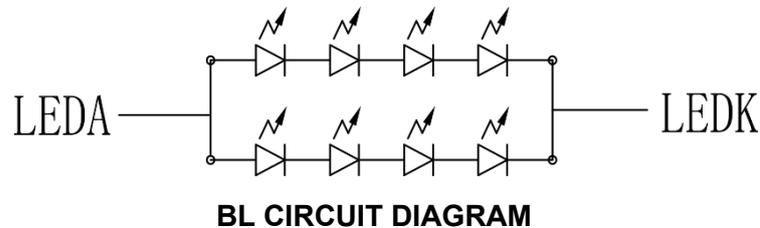
Note 3: Luminance Uniformity of these 9 points is defined as below:
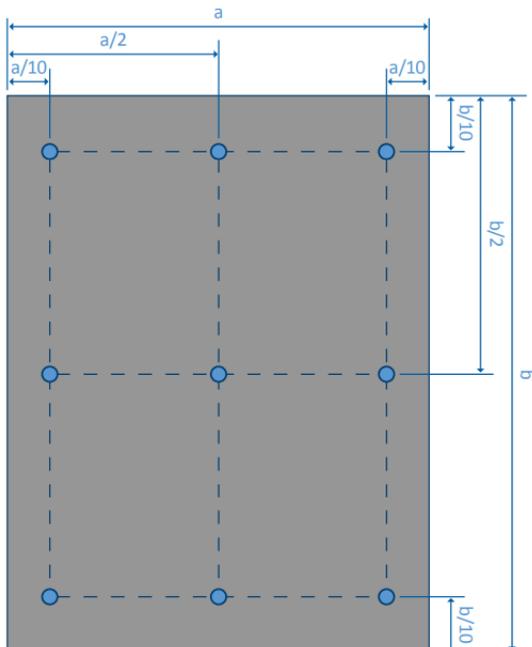
$$Luminance = \frac{(Total\ Luminance\ of\ 9\ Points)}{9}$$

$$Uniformity = \frac{Minimum\ Luminance\ in\ 9\ Points\ (1-9)}{Maximum\ Luminance\ in\ 9\ Points\ (1-9)}$$

6. TFT AC Characteristics

6.1　High Speed Data Transmission: Data-Clock Timing

For high-speed data transmission data-clock timing diagrams and tables, see table and diagram on page 315 of the data sheet for controller ILI9806E. The data sheet can be found here:

https://focuslcds.com/wp-content/uploads/Drivers/ILI9806E.pdf

6.2　High Speed Data Transmission

For high speed data transmission diagrams and tables, see the first table and diagram page 64 of the data sheet for controller ILI9806E. The data sheet can be found here:

https://focuslcds.com/wp-content/uploads/Drivers/ILI9806E.pdf

6.3　Turnaround Procedure

For turnaround procedure diagrams and tables, see the second table and diagram page 69 of the data sheet for controller ILI9806E. The data sheet can be found here:

https://focuslcds.com/wp-content/uploads/Drivers/ILI9806E.pdf

6.4　Switching the Clock Lane Between Clock Transmission and Low-Power Mode

For switching the clock lane between clock transmission and low-power mode tables and diagrams, see page 53 of the data sheet for controller ILI9806E. The data sheet can be found here:

https://focuslcds.com/wp-content/uploads/Drivers/ILI9806E.pdf

6.5　Reset Timing Characteristics

For the reset timing characteristics table and diagram, see page 283 *for the data sheet for controller ILI9806E. The data sheet can be found here:*

https://focuslcds.com/wp-content/uploads/Drivers/ILI9806E.pdf

7. Quality Inspection Standards

For TFT quality inspection standards, please see the following link:

https://focuslcds.com/lcd-resources/tft-quality-inspection-standards/

8. Cautions and Handling Precautions

8.1　Handling and Operating the Module

1. When the module is assembled, it should be attached to the system firmly. Do not warp or twist the module during assembly work.

2. Protect the module from physical shock or any force. In addition to damage, this may cause improper operation or damage to the module and back-light unit.

3. Note that polarizer is very fragile and could be easily damaged. Do not press or scratch the surface.

4. Do not allow drops of water or chemicals to remain on the display surface. If you have the droplets for a long time, staining and discoloration may occur.

5. If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.

6. The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane. Do not use ketene type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.

7. If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs, or clothes, it must be washed away thoroughly with soap.

8. Protect the module from static; it may cause damage to the CMOS ICs.

9. Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.

10. Do not disassemble the module.

11. Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.

12. Pins of I/F connector shall not be touched directly with bare hands.

13. Do not connect, disconnect the module in the "Power ON" condition.

14. Power supply should always be turned on/off by the item Power On Sequence & Power Off Sequence

8.2　Handling and Operating the Module

1. Do not leave the panel in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 0 to 35 ℃ and relative humidity of less than 70%

2. Do not store the TFT-LCD module in direct sunlight.

3. The module shall be stored in a dark place. When storing the modules for a long time, be sure to adopt effective measures for protecting the modules from strong ultraviolet radiation, sunlight, or fluorescent light.

4. It is recommended that the modules should be stored under a condition where no condensation is allowed. Formation of dewdrops may cause an abnormal operation or a failure of the module. In particular, the greatest possible care should be taken to prevent any module from being operated where condensation has occurred inside.

5. This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed.