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TFT | OLED | CHARACTER | GRAPHIC | UWVD | SEGMENT | CUSTOM

TFT Display Module

Part Number

E50RE-RW650-C

Overview:

- 5-inch TFT (132.72x87.80mm)
- 800x480 Pixels
- 16/18/24-Bit RGB Interface
- Wide Temperature Range
- 12:00 Viewing Angle
- Transmissive, Normally White
- Capacitive Touch Panel
- 650 NITS
- TFT IC: ILI5960/ILI6122
- CTP IC: GT911
- RoHS Compliant

Description

This is a color active matrix TFT (Thin Film Transistor) LCD (Liquid Crystal Display) that uses amorphous silicon TFT as a switching device. This model is composed of a transmissive type TFT-LCD Panel, driver circuit, capacitive touch panel and backlight unit. The resolution of the 5.0" TFT-LCD contains 800x480 pixels and can display up to 65K/262K/16.7M colors.

Features

Input Voltage: 3.3V

TFT Interface: 16/18/24-bit RGB

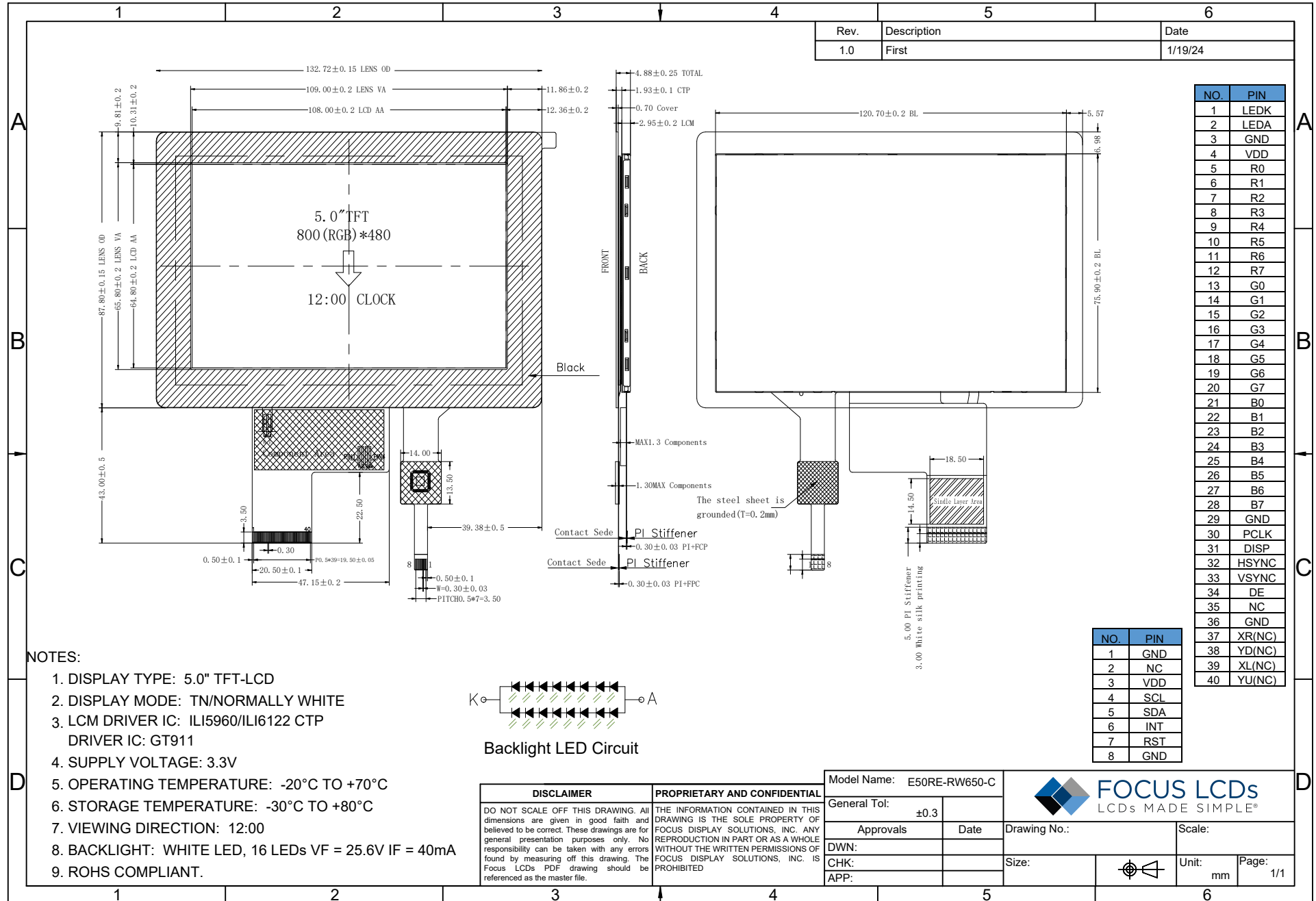
CTP Interface: I2C

General Information Items	Specification	Unit	Note
	Main Panel		
TFT Display Area (AA)	108.00(H) x 64.80(V) (5.0 inch)	mm	--
CTP View Area	109.00(H) x 65.80(V)	mm	--
Driver Element	TFT active matrix	--	--
Display Colors	65K/262K/16.7M	colors	--
Number of Pixels	800(RGB)x480	dots	--
TFT Pixel Arrangement	RGB vertical stripe	--	--
Pixel Pitch	0.135 (H) x 0.135 (V)	mm	--
Viewing Angle	12:00	o'clock	--
TFT Controller IC	ILI5960/ILI6122	--	--
CTP Driver IC	GT911	--	--
Display Mode	Transmissive/Normally White	--	--
Touch Points	5-point and Gestures	--	--
Operating Temperature	-20 to +70	°C	--
Storage Temperature	-30 to +80	°C	--

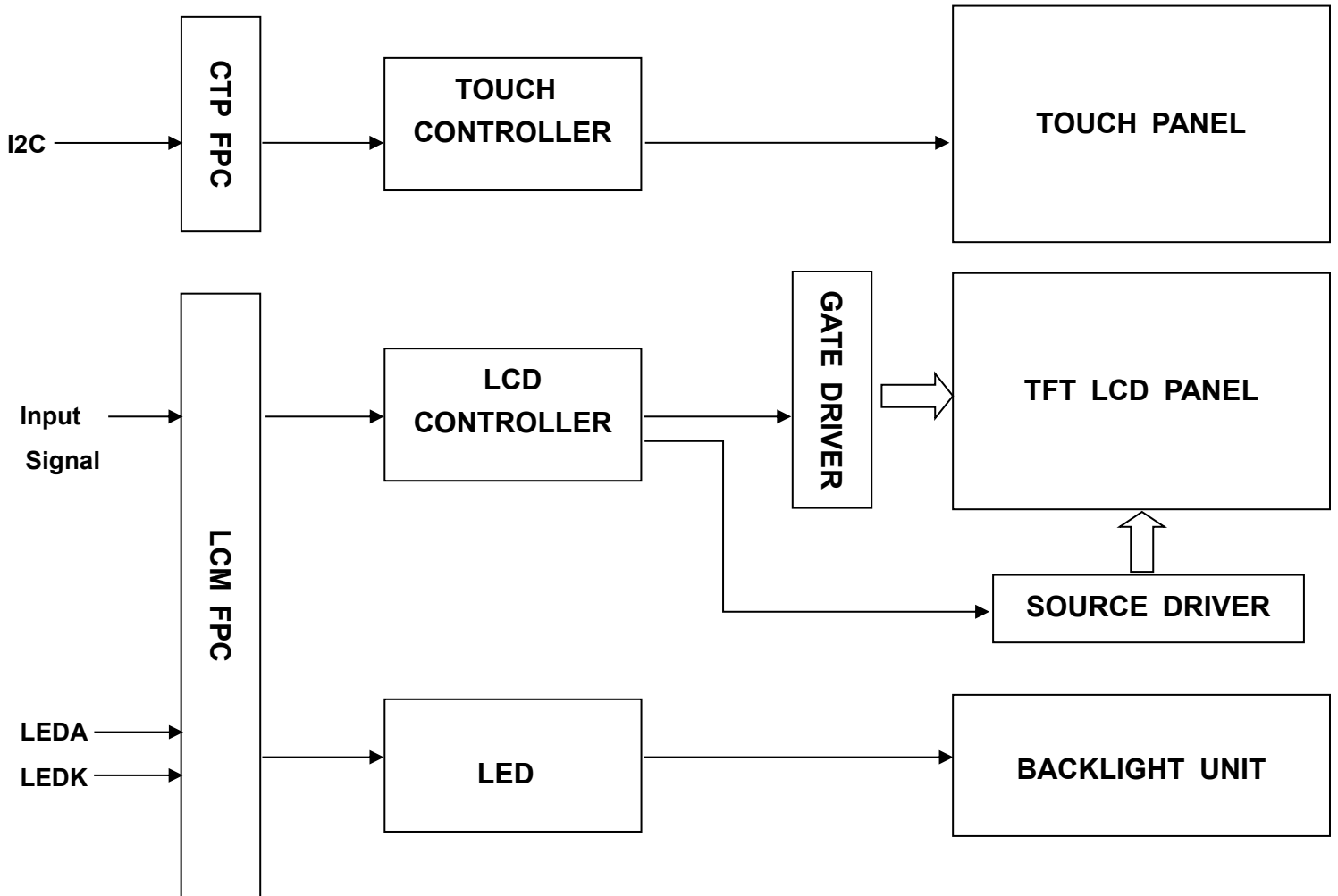
Mechanical Information

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal (H)	--	132.72	--	mm	--
	Vertical (V)	--	87.80	--	mm	--
	Depth (D)	--	4.88	--	mm	--
Weight		--	100	--	g	Approximate

1. Outline Dimensions



2. Block Diagram



3. Input Terminal Pin Assignment

3.1 TFT Pin Assignment

Connector on FPC: FH12S-40S-0.5SH(0.5)

NO.	Symbol	Description	I/O
1	LEDK	Cathode pin of backlight.	P
2	LEDA	Anode pin of backlight.	P
3	GND	Ground.	P
4	VDD	Supply Voltage (3.3V).	P
5	R0	Red data input.	I
6	R1	Red data input.	I
7	R2	Red data input.	I
8	R3	Red data input.	I
9	R4	Red data input.	I
10	R5	Red data input.	I
11	R6	Red data input.	I
12	R7	Red data input.	I
13	G0	Green data input.	I
14	G1	Green data input.	I
15	G2	Green data input.	I
16	G3	Green data input.	I
17	G4	Green data input.	I
18	G5	Green data input.	I
19	G6	Green data input.	I
20	G7	Green data input.	I
21	B0	Blue data input.	I
22	B1	Blue data input.	I
23	B2	Blue data input.	I
24	B3	Blue data input.	I
25	B4	Blue data input.	I
26	B5	Blue data input.	I
27	B6	Blue data input.	I
28	B7	Blue data input.	I
29	GND	Ground.	P
30	PCLK	Clock signal, latching data at the rising edge.	I
31	DISP	Standby setting for testing, connect to VDD in normal operation mode. If connected to GND, the IC is in standby mode.	I
32	HSYNC	Horizontal sync input. Negative polarity.	I
33	VSYNC	Vertical sync input. Negative polarity.	I
34	DE	Data input enable. Active high to enable the data input bus under DE mode.	I
35	NC	Not connected.	--
36	GND	Ground.	P

I: Input, O: Output, P: Power

3.1 TFT Pin Assignment (continued)

NO.	Symbol	Description	I/O
37	XR(NC)	Not connected.	--
38	YD(NC)	Not connected.	--
39	XL(NC)	Not connected.	--
40	YU(NC)	Not connected.	--

3.2 CTP Pin Assignment

NO.	Symbol	Description	I/O
1	GND	Ground.	P
2	NC	Not connected.	--
3	VDD	Supply voltage.	P
4	SCL	I2C clock input.	I
5	SDA	I2C data input and output.	I/O
6	INT	External interrupt to the host.	I
7	RST	External reset, low is active.	I
8	GND	Ground.	P

4. LCD Optical Characteristics

4.1 Optical Specifications

Item		Symbol	Condition	Min	Typ.	Max	Unit	Note
Color Gamut		S(%)		--	45	--	%	(5)
Transmittance		T%		3.55	4.48	--	%	
Contrast Ratio		CR	$\theta=0$ Normal viewing angle	400	500	--	%	(2)
Response Time	Rising	T_R		--	10	20	ms	(4)
	Falling	T_F		--	20	30		
Color Filter Chromaticity	White	W_X		0.2559	0.2959	0.3359	(5)(6)	
		W_Y		0.2722	0.3122	0.3522		
	Red	R_X		0.5040	0.5440	0.5840		
		R_Y		0.2930	0.3330	0.3730		
	Green	G_X		0.2975	0.3375	0.3775		
		G_Y		0.5201	0.5601	0.6001		
	Blue	B_X		0.1086	0.1486	0.1886		
		B_Y	0.0480	0.0880	0.1280			
Viewing Angle	Hor.	Θ_L	CR \geq 10	--	70	--	degree	(1)(6)
		Θ_R		--	70	--		
	Ver.	Θ_T		--	50	--		
		Θ_B		--	70	--		
Option View Direction		12:00						(1)

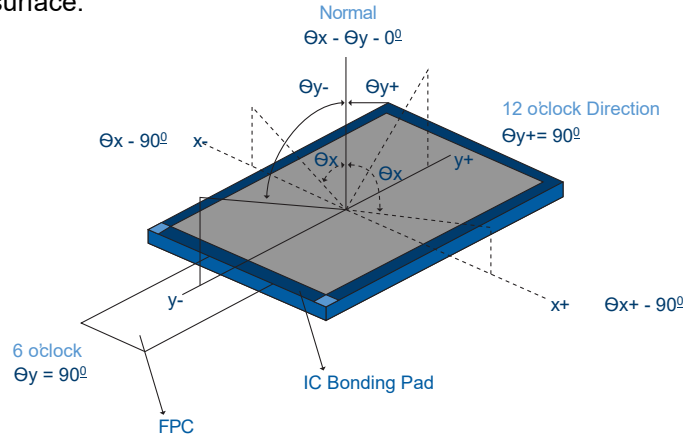
Measuring Conditions:

1. Dark Room
2. Ambient Temperature of 25 \pm 2°C
3. 15 Minute Warm up

Optical Specification Reference Notes:

(1) Definition of Viewing Angle:

The viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3,9 o'clock direction and the vertical or 6,12 o'clock direction with respect to the optical axis which is normal to the LCD surface.



(2) Definition of Contrast Ratio:

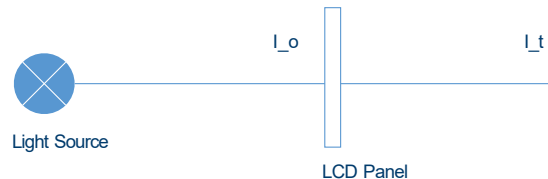
Measured at the center point of panel. The contrast ratio (Cr) measured on a module, is the ratio between the luminance (Lw) in a full white area (R=G=B=1) and the luminance (Ld) in a dark area (R=G=B=0).

$$Cr = \frac{Lw}{Ld}$$

(3) Definition of Transmittance (T%):

The transmittance of the panel including the polarizers is measured with electrical driving. The equation for transmittance Tr is:

$$Tr = \frac{It}{Io} \times 100\%$$

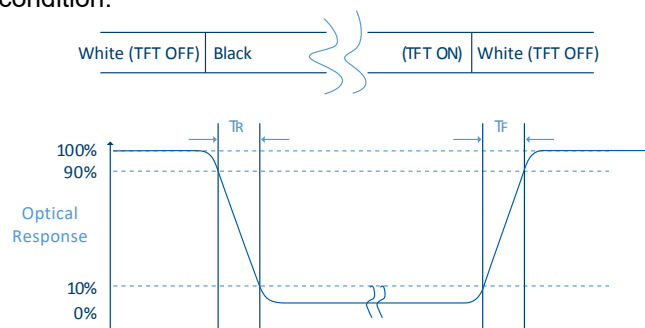


Io = the brightness of the light source.

It = the brightness after panel transmission

(4) Definition of Response Time (TR, TF):

The rise time 'Tr' is defined as the time for luminance to change from 90% to 10% as a result of a change of the electrical condition. The fall time 'Tf' is defined as the time for luminance to change from 10% to 90% as a result of a change of the electrical condition.



(5) Definition of Color Gamut:

Measuring machine CFT-01. NTSC's Primaries: R(x,y,Y), G(x,y,Y), B(x,y,Y). FPM520 of Westar Display Technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics. The color chromaticity shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.

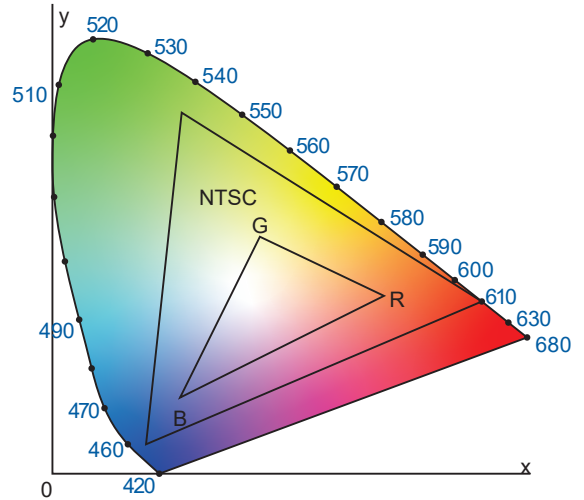
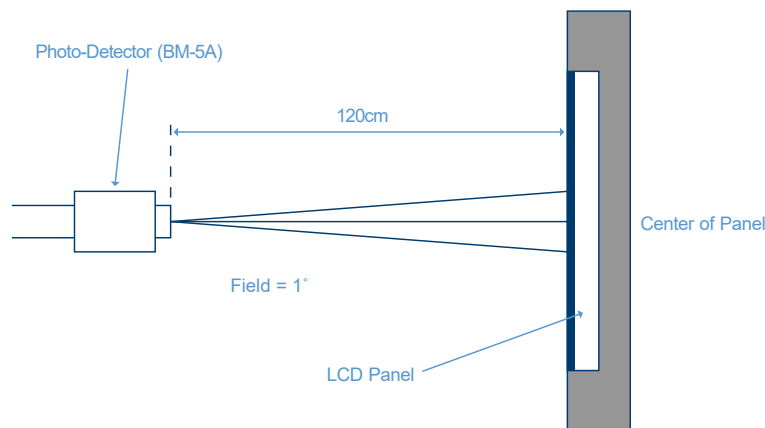
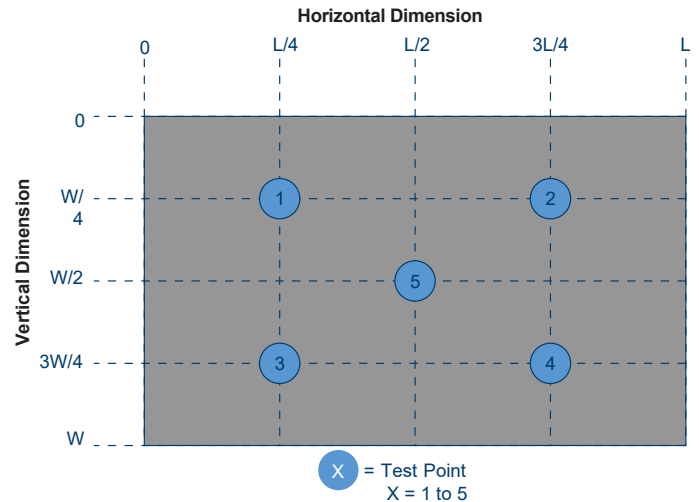
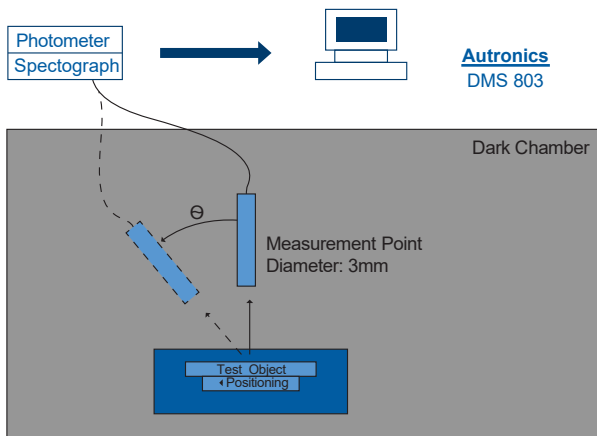


Fig. 1931 CIE Chromaticity Diagram

Color Gamut: $S = \frac{\text{Area of RGB Triangle}}{\text{Area of NTSC Triangle}} \times 100\%$

(6) Definition of Optical Measurement Setup:

The LCD module should be stabilized at a given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 20 minutes.



5. TFT Electrical Characteristics

5.1 Absolute Maximum Rating (Ta=25°C, VSS=0V)

Characteristics	Symbol	Min	Max	Unit
Digital Supply Voltage	VDD	-0.5	5.0	V
Operating Temperature	TOP	-20	+70	°C
Storage Temperature	TST	-30	+80	°C

NOTE: If the absolute maximum rating of the above parameters is exceeded, even momentarily, the quality of the product may be degraded. Absolute maximum ratings specify the values which the product may be physically damaged if exceeded. Be sure to use the product within the range of the absolute maximum ratings.

5.2 DC Electrical Characteristics

Characteristics	Symbol	Min	Typ.	Max	Unit	Note
Digital Supply Voltage	VCI	3.0	3.3	3.6	V	--
Normal Mode Current Consumption	IDD	--	100	200	mA	--
Level Input Voltage	V _{IH}	0.7*V _{DD}	--	V _{DD}	V	--
	V _{IL}	GND	--	0.3*V _{DD}	V	--
Level Output Voltage	V _{OH}	V _{DD} -0.04	--	--	V	--
	V _{OL}	GND	--	GND+0.4	V	--

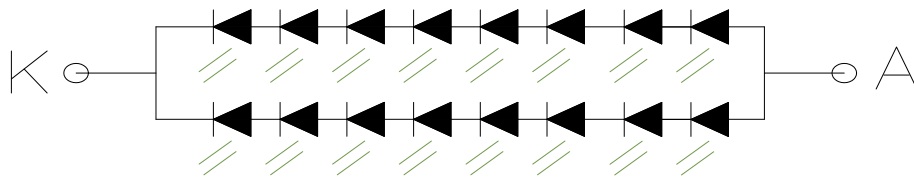
5.3 LED Backlight Characteristics

The backlight system is edge lighting type with 16 LED Chips

Item	Symbol	Min	Typ.	Max	Unit	Note
Forward Current	I_F	30	40	--	mA	--
Forward Voltage	V_F	--	25.6	--	V	--
LCM Luminance	L_V	600	650	--	cd/m ²	(3)
LED Lifetime	H_r	50000	--	--	hour	(1)(2)
Uniformity	Avg	80	--	--	%	(3)

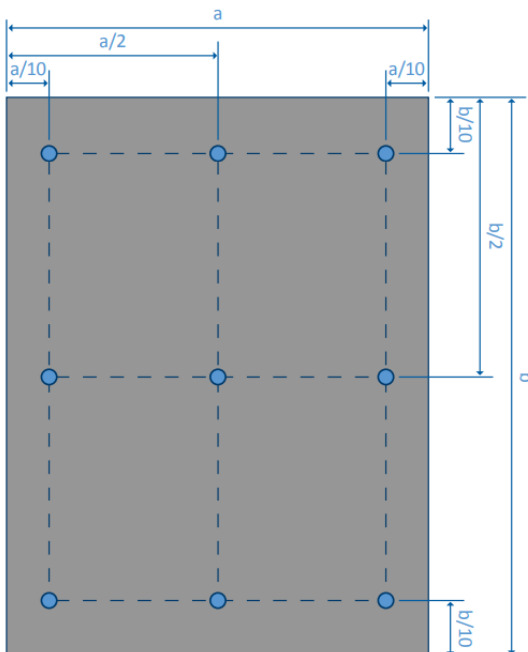
Note 1: LED lifetime (Hr) can be defined as the time in which it continues to operate under the condition: $T_a=25\pm 3^\circ\text{C}$, typical IL value indicated in the above table until the brightness becomes less than 50%.

Note 2: The "LED lifetime" is defined as the module brightness decrease to 50% original brightness at $T_a=25^\circ\text{C}$ and $I_F = 40\text{mA}$. The LED lifetime could be decreased if operating I_F is larger than 40mA. The constant current driving method is suggested.



Backlight LED Circuit

Note 3: Luminance Uniformity of these 9 points is defined as below:



$$\text{Luminance} = \frac{\text{Total Luminance of 9 Points}}{9}$$

$$\text{Uniformity} = \frac{\text{Minimum Luminance in 9 Points(1-9)}}{\text{Maximum Luminance in 9 Points(1-9)}}$$

6. AC Characteristics

6.1 AC Timing Characteristics

Parameter	Symbol	Min	Typ.	Max	Unit	Conditions
VDD Power ON slew rate	t _{POR}	--	--	20	ms	0V-0.9VDD
RSTB pulse width	t _{RST}	10	--	--	us	CLKIN=50MHz
CLKIN cycle time	t _{CPH}	20	--	--	ns	
CLKIN pulse duty	t _{CWH}	40	50	60	%	
VSD setup time	t _{VST}	8	--	--	ns	
VSD hold time	t _{VHD}	8	--	--	ns	
HSD setup time	t _{HST}	8	--	--	ns	
HSD hold time	t _{HHD}	8	--	--	ns	
Data setup time	t _{DST}	8	--	--	ns	D0[7:0], D1[7:0], D2[7:0] to CLKIN
Data hold time	t _{DHD}	8	--	--	ns	D0[7:0], D1[7:0], D2[7:0] to CLKIN
DE setup time	t _{EST}	8	--	--	ns	
DE hold time	t _{EHD}	8	--	--	ns	
Output stable time	t _{SST}	--	--	6	us	10% to 90% target voltage, CL=120pF, R=10kΩ
CLKIN frequency	f _{CLK}	--	40	50	MHz	VDD=3.0-3.6V
CLKIN cycle time	t _{CLK}	20	25	--	ns	
CLKIN pulse duty	t _{CWH}	40	50	60	%	Tclk
Time from HSD to Source output	t _{HSD}	--	20	--	CLKIN	
Time from HSD to LD	t _{HLD}	--	20	--	CLKIN	
Time from HSD to STV	t _{HSTV}	--	20	--	CLKIN	
Time from HSD toCKV	t _{HCKV}	--	20	--	CLKIN	
Time from HSD to OEV	t _{HOEV}	--	4	--	CLKIN	
LD pulse width	t _{WLD}	--	10	--	CLKIN	
CKV pulse width	t _{WCKV}	--	66	--	CLKIN	
OEV pulse width	t _{WEOV}	--	74	--	CLKIN	

Table 6.1: AC Timing Characteristics Table

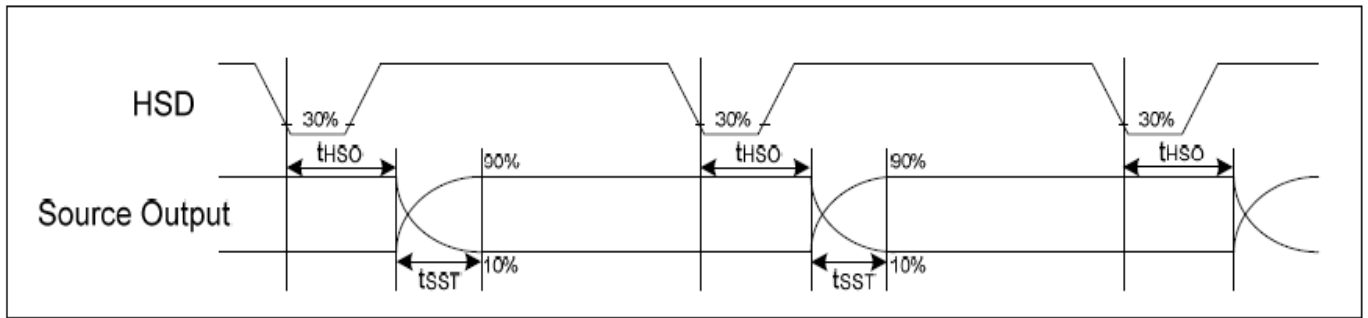


Figure 6.1: Source Output Timing Diagram (Cascade)

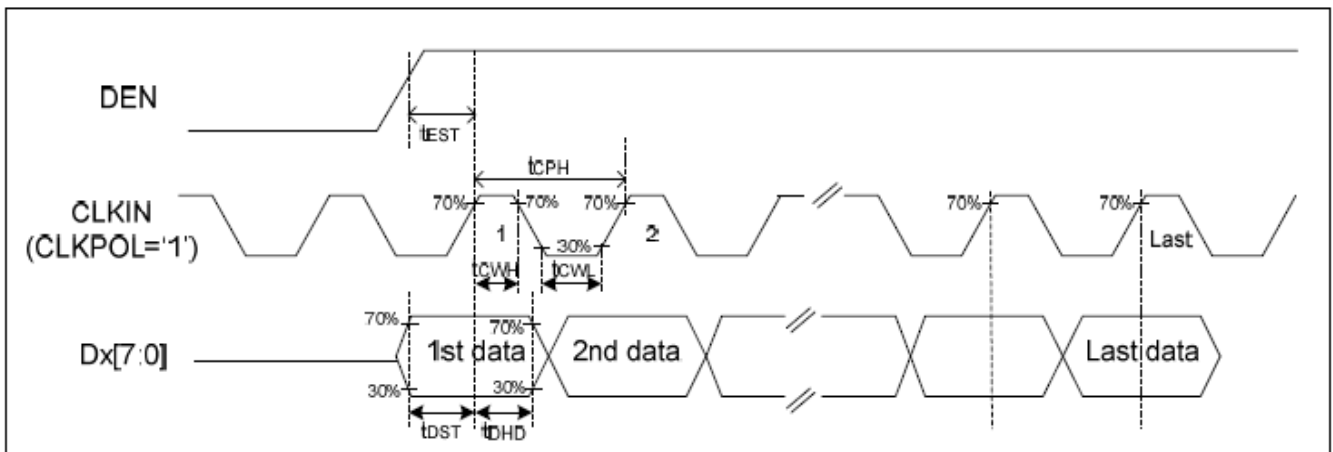


Figure 6.2: DE Mode (Mode='1') Timing Diagram

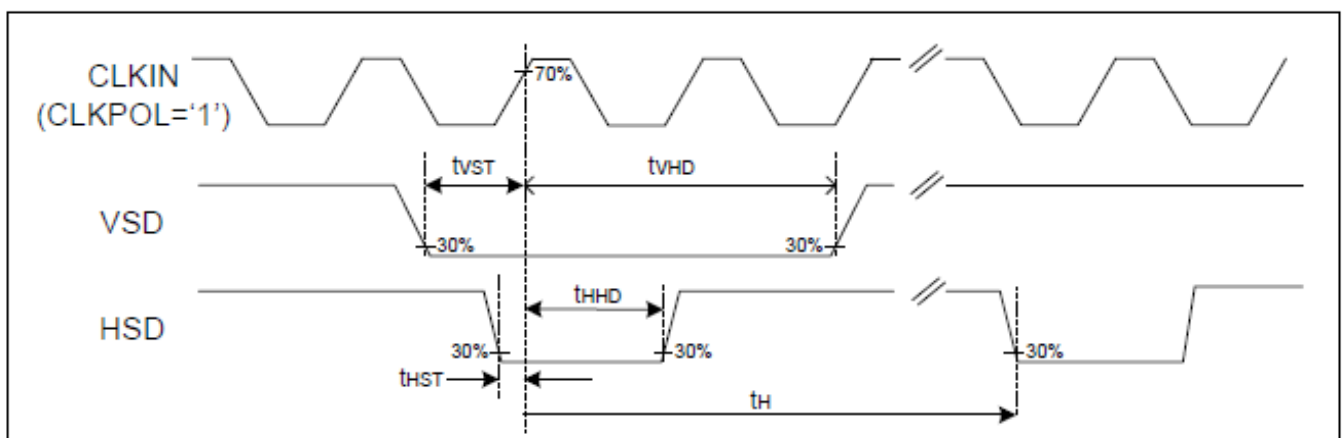
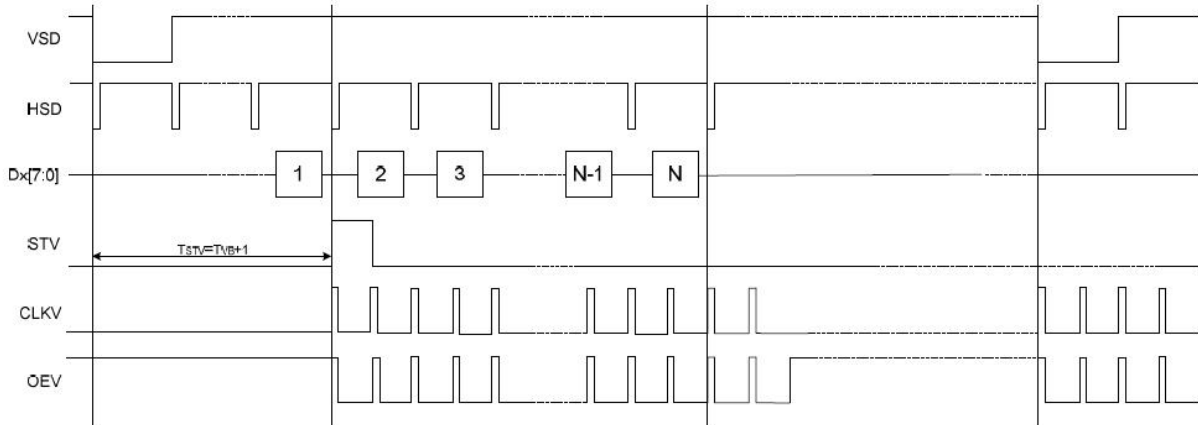
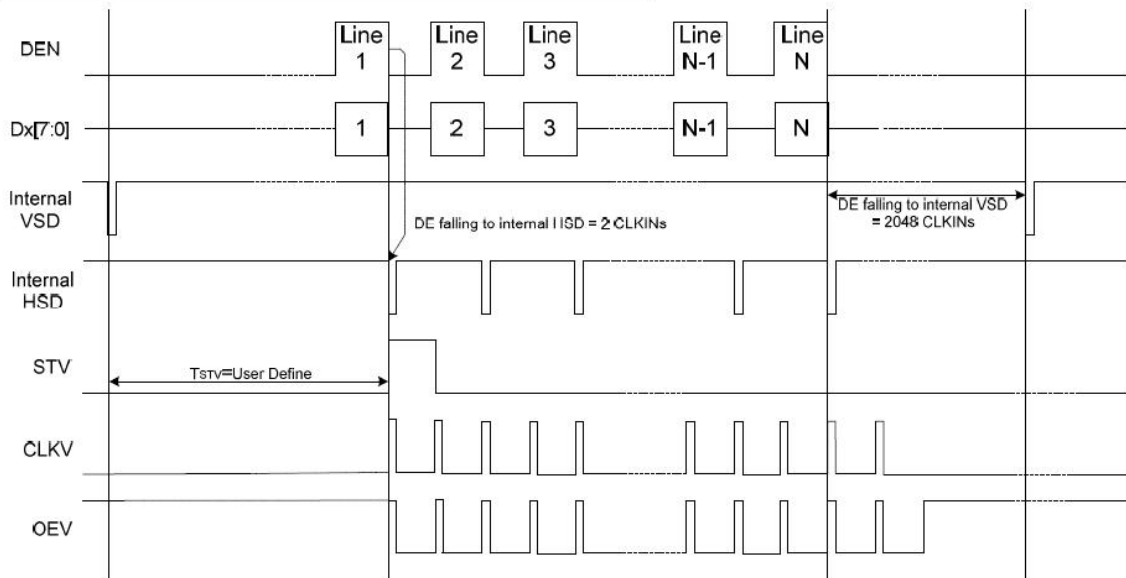


Figure 6.3: SYNC Mode (Mode='0') Timing Diagram

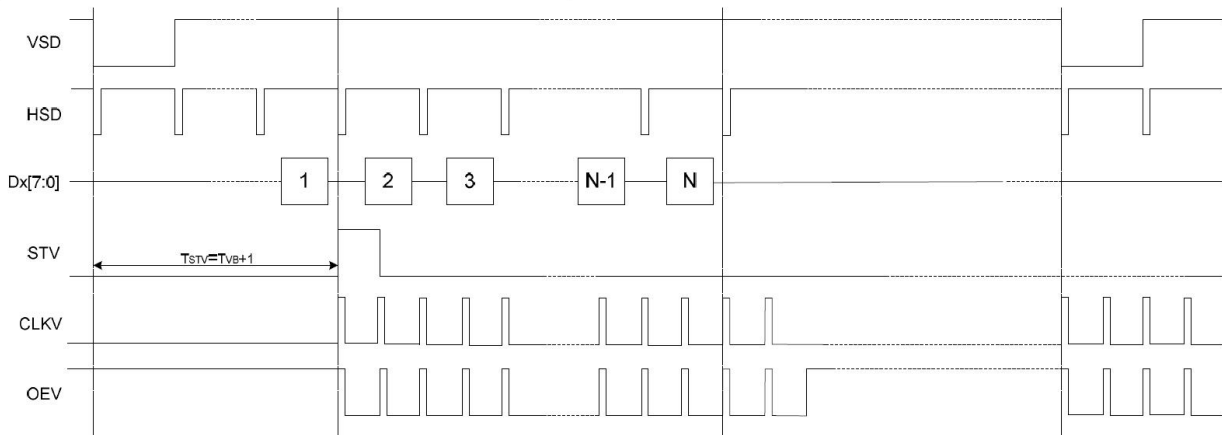
Vertical Timing Diagram of SYNC Mode (Dual Gate)



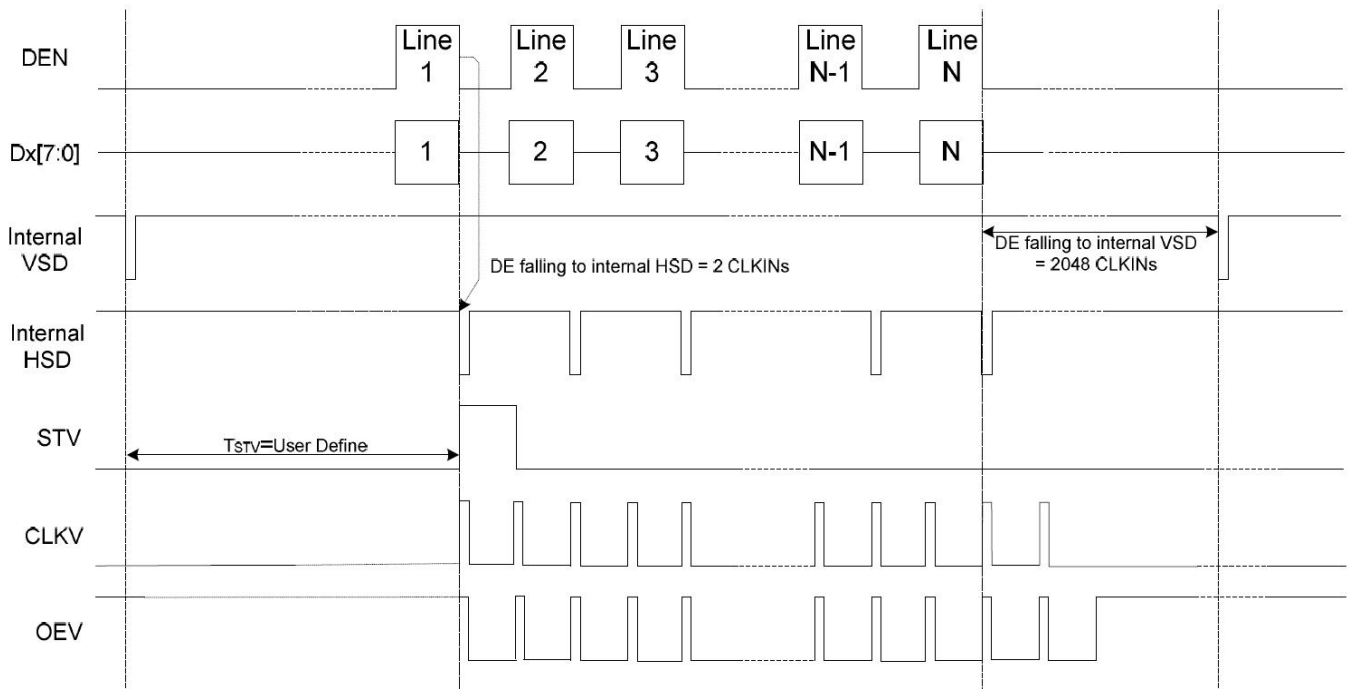
Vertical Timing Diagram of DE Mode (Dual Gate)



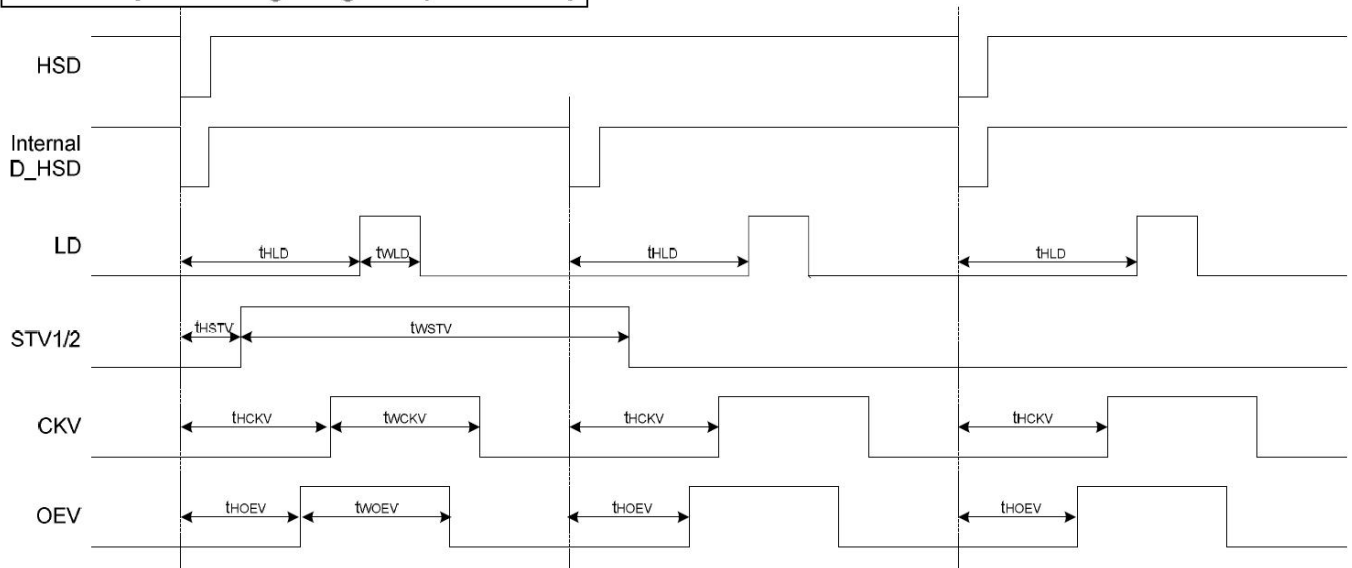
Vertical Timing Diagram of SYNC Mode (Dual Gate)



Vertical Timing Diagram of DE Mode (Dual Gate)



Gate Output Timing Diagram (Dual Gate)



7.0 CTP Electrical Characteristics

7.1 Absolute Maximum Rating

Item	Symbol	Min	Max	Unit	Note
Power Supply Voltage	VDD	-0.3	3.47	V	
Operating Temperature	TOP	-20	+70	°C	
Storage Temperature	TST	-30	+80	°C	

7.2 DC Electrical Characteristics

Ambient temperature: 25°C

Item	Min	Typ.	Max	Unit	Note
Digital Supply Voltage/VDD	2.8	--	3.3	V	
I/O Digital Supply Voltage/VDDIO	1.8	--	3.3	V	
Normal Mode Operating Current	--	8	14.5	mA	
Green Mode Operating Current	--	3.3	--	mA	
Sleep Mode Operating Current	70	--	120	uA	
Doze Mode Operating Current	--	0.78	--	mA	
Digital Input Low Voltage/VIL	-0.3	--	0.25*VDDIO	V	
Digital Input High Voltage/VIH	0.75*VDDIO	--	VDDIO+0.3	V	
Digital Output Low Voltage/VOL	--	--	0.15*VDDIO	V	
Digital Output High Voltage/VOH	0.85*VDDIO	--	--	V	

7.3 AC Characteristics

Ambient temperature: 25°C, AVDD=2.8V, VDDIO=1.8V

Parameter	Min	Typ.	Max	Unit
OSC Oscillation Frequency	59	60	61	NHZ
I/O Output Rise Time, Low to High	--	14	--	ns
I/O Output Fall Time, High to Low	--	14	--	ns

7.4 I2C Timing

For I2C timing diagrams and tables, see page 9 of the data sheet for touch panel IC GT911. The data sheet for GT911 can be found here: <https://focuslcds.com/wp-content/uploads/drivers/GT911.pdf>

7.5 Power-On Timing

For the power on timing diagram, see the first diagram on page 10 of the data sheet for touch panel IC GT911. The data sheet for GT911 can be found here: <https://focuslcds.com/wp-content/uploads/drivers/GT911.pdf>

7.6 Timing for Host Resetting GT911

For the host resetting timing diagram, see the second diagram on page 10 of the data sheet for touch panel IC GT911. The data sheet for GT911 can be found here: <https://focuslcds.com/wp-content/uploads/drivers/GT911.pdf>

7.7 Timing for Setting Slave Address to 0x28/0x29

For this setting slave address timing diagram, see the third diagram on page 10 of the data sheet for touch panel IC GT911. The data sheet for GT911 can be found here: <https://focuslcds.com/wp-content/uploads/drivers/GT911.pdf>

7.8 Timing for Setting Slave Address to 0xBA/0xBB

For this setting slave address timing diagram and instructions, see pages 11-12 of the data sheet for touch panel IC GT911. The data sheet for GT911 can be found here: <https://focuslcds.com/wp-content/uploads/drivers/GT911.pdf>

8.0 Quality Inspection Standards

For TFT quality inspection standards, please see the following link: <https://focuslcds.com/lcd-resources/tft-quality-inspection-standards/>

9.0 Cautions and Handling Precautions

9.1 Handling and Operating the Module

1. When the module is assembled, it should be attached to the system firmly. Do not warp or twist the module during assembly work.
2. Protect the module from physical shock or any force. In addition to damage, this may cause improper operation or damage to the module and back-light unit.
3. Note that polarizer is very fragile and could be easily damaged. Do not press or scratch the surface.
4. Do not allow drops of water or chemicals to remain on the display surface. If you have the droplets for a long time, staining and discoloration may occur.
5. If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
6. The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane. Do not use ketene type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
7. If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs, or clothes, it must be washed away thoroughly with soap.
8. Protect the module from static; it may cause damage to the CMOS ICs.
9. Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
10. Do not disassemble the module.
11. Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
12. Pins of I/F connector shall not be touched directly with bare hands.
13. Do not connect, disconnect the module in the "Power ON" condition.
14. Power supply should always be turned on/off by the item Power On Sequence & Power Off Sequence.

9.2 Storage and Transportation

1. Do not leave the panel in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 0 to 35 °C and relative humidity of less than 70%
2. Do not store the TFT-LCD module in direct sunlight.
3. The module shall be stored in a dark place. When storing the modules for a long time, be sure to adopt effective measures for protecting the modules from strong ultraviolet radiation, sunlight, or fluorescent light.
4. It is recommended that the modules should be stored under a condition where no condensation is allowed. Formation of dewdrops may cause an abnormal operation or a failure of the module. In particular, the greatest possible care should be taken to prevent any module from being operated where condensation has occurred inside.
5. This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed.