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TFT | CHARACTER | UWVD | FSC | SEGMENT | CUSTOM | REPLACEMENT

TFT Display Module

Part Number

E70RA-HW520-C

Overview

7.0 inch TFT: 1024x600(165x100), LVDS, WHITE LED backlight, IPS, Wide temp, Transmissive, Capacitive Touch Screen, 520 NITS, HDMI connection, RoHS Compliant

1.Basic Specifications

* Description

This is a color active matrix TFT (Thin Film Transistor) LCD (liquid crystal display) that uses amorphous silicon TFT as a switching device. This module is composed of a Transmissive type TFT-LCD Panel, driver circuit, capacitance touch panel back-light unit. The resolution of a 7.0 " TFT-LCD contains 1024x600 pixels, and can display up to 16.7M colors.

1.1 TFT Features

General Information Items	Specification	Unit	Note
	Main Panel		
Display area(AA)	154.21(H)*85.92（V） (7.0inch)	mm	
Driver element	TFT active matrix	-	
Display colors	262K/16.7M	colors	
Number of pixels	1024(RGB)*600	dots	
Pixel arrangement	RGB vertical stripe	-	
Pixel pitch	0.1506(H)*0.1432(V)	mm	
Viewing angle	All	o'clock	
Controller IC	EK73215& EK79001	-	
LCM Interface	6/8 BIT LVDS	-	
Display mode	Transmissive /Normally Black	-	
Touch and LCM Bonding technology	Tape bonding	-	
Operating temperature	-20～+70	℃	
Storage temperature	-30～+80	℃	

1.2 CTP Features

General Information Items	Specification	Unit	Note
	Main Panel		
Resolution	1024(H)*600(V)	-	
Structure	G+G	-	
Controller IC	GT9271	-	
Interface	I2C	-	
Slave Address	0x5D(7bit) or 0x14(7bit)	-	Note1
Touch mode	Ten points and Gestures	-	-

Note1: For specific configuration method, please refer to section 8.2

1.3 Mechanical Information

Item		Min.	Typ.	Max.	Unit	Note
Module size	Horizontal(H)	-	165	-	mm	
	Vertical(V)	-	100	-	mm	
	Depth(D)	-	7.33	-	mm	
Weight		-	TBD	-	g	

2. Block Diagram

NOTES:

1. DISPLAY TYPE: 7" TFT-LCD, 16.7M COLORS
2. DISPLAY MODE: NORMALLY BLACK/IPS
3. VIEWING DIRECTION: ALL
4. LCM DRIVER IC: EK73215+EK79001 (COG)
   LCM Interface: LVDS Interface
5. Touch Mode: CTP
   Touch Driver IC: GT9271
   Touch Interface: IIC
   Touch and LCM Bonding technology: Tape bonding
6. VDD:3.3V
7. OPERATING TEMP: -20℃ TO 70℃
   STORAGE TEMP: -30℃ TO 80℃
8. BACK LIGHT: LED WHITE, 27 LED, 270mA, 9.6±0.3V
9. RoHS COMPLIANT.

Pin Assignment

NO.	Pin Name
1	NC
2	VDD
3	VDD
4	NC
5	RESET
6	STBYB
7	GND
8	RXIN0-
9	RXIN0+
10	GND
11	RXIN1-
12	RXIN1+
13	GND
14	RXIN2-
15	RXIN2+
16	GND
17	RXCLKIN-
18	RXCLKIN+
19	GND
20	RXIN3-
21	RXIN3+
22	GND
23	NC
24	GND
25	GND
26	NC
27	DIMO
28	SELB
29	NC
30	GND
31	LED-
32	LED-
33	L/R
34	U/D
35	NC
36	NC
37	NC
38	LED+
39	LED+
40	LED+

FPC Logic

PIN	Logic
1	GND
2	NC
3	VDD
4	SCL
5	SDA
6	INT
7	RST
8	GND

B/L Circuit

K(-)   A(+)

CTP & TFPC stretching mode shipment

BL0702703
HC X

7.0"
IPS
Block

5±0.3 (PI Stiffener)
3.5(White silk printing)
3(White silk printing)

Stainless steel stiffener (t=0.2)(Round)
Yellow tape (T=0.05mm) Soliding 0.4MAX

0.2(Stainless steel stiffener)
0.4Max(Soliding)
0.2(Stainless steel stiffener)
0.13±0.03(FPC)
PI Stiffener
0.3±0.03 (FPC+PI Stiffener)
0.3±0.03 (FPC+PI Stiffener)
Contact Side
1.3Max(CTP IC & Components)
2Max(Components)

ReV	Revision content description	Date
V0	FIRST	2018.03.13
V1		2018.04.16
V2		2018.08.14

TOLERANCE( ):
TOLERANCE UNLESS SPECIFIED: X.X±0.3
OTHERWISE SPECIFIED: X.XX±0.2

DRAWING NAME / PARTS NO. E70RA-HW520-C
Drawn / Checked / Approve
Scale 1:1   Unit: mm   Page: 1/1
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1	NC	--	--
2	VDD	Power supply for digital circuits	P
3	VDD		
4	NC	--	--
5	RESET	Global reset pin. Active low to enter reset state.	I
6	STBYB	Standby mode, Normally pulled high STBYB = "1", normal operation STBYB = "0", timing controller, source driver will turn off, all output are High-Z	I
7	GND	Ground	P
8	RXIN0-	- LVDS differential data input	I
9	RXIN0+	+ LVDS differential data input	I
10	GND	Ground	P
11	RXIN1-	- LVDS differential data input	I
12	RXIN1+	+ LVDS differential data input	I
13	GND	Ground	P
14	RXIN2-	- LVDS differential data input	I
15	RXIN2+	+ LVDS differential data input	I
16	GND	Ground	P
17	RXCLKN-	- LVDS differential clock input	I
18	RXCLKN+	+ LVDS differential clock input	I
19	GND	Ground	P
20	RXIN3-	- LVDS differential data input	I
21	RXIN3+	+ LVDS differential data input	I
22	GND	Ground	P
23	NC	--	--
24	NC	--	--
25	GND	Ground	P

26	NC	--	--
27	DIMO	Backlight dimmer signal for external controller. DIMO = "0", Turn off external backlight controller DIMO = "1", Logical control signal to turn on external backlight controller NOTE : If CABC OFF , DIMO = DIMI .Else DIMO is controlled by CABC If no used open.	O
28	SELB	Input data format selection. SLEB=0, 8-BIT LVDS. SLEB=1, 6-BIT LVDS。	I
29	NC	--	--
30	GND	Ground	P
31	LED-	LED Cathode	P
32	LED-		
33	L/R	Horizontal shift direction (source output) selection(NOTE1)	I
34	U/D	Vertical shift direction (gate output) selection(NOTE1)	I
35	NC	--	--
36	NC	--	--
37	NC	--	--
38	NC	--	--
39	LED+	LED Anode	P
40	LED+		

Note1: When L/R="0", set right to left scan direction.

When L/R="1", set left to right scan direction.

When U/D="0", set top to bottom scan direction.

When U/D="1", set bottom to top scan direction.
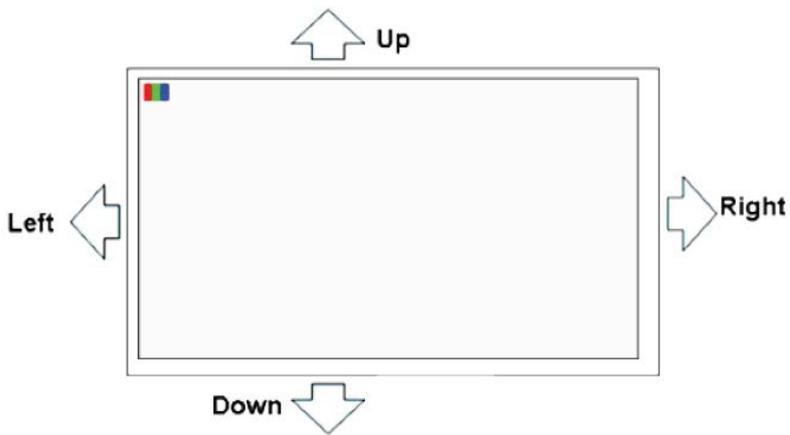
4.2 CTP PIN Define

NO.	SYMBOL	DISCRIPTION	I/O
1	GND	Ground	P
2	NC	No Connection	
3	VDD	Supply voltage	P
4	SCL	I2C clock input	I
5	SDA	I2C data input and output	I
6	INT	External interrupt to the host	I
7	RST	External Reset, Low is active	I
8	GND	Ground	P

5. LCD Optical Characteristics

5.1 Optical specification

Item		Symbol	Condition	Min.	Typ.	Max.	Unit.	Note
Contrast Ratio		CR	$\Theta=0$	600	800	--		
Response time	Rising	T_R+T_F	Normal viewing angle	--	25	40	msec	
	Falling							
Uniformity		S(%)		--	50	--	%	
Color Filter Chromacicity	White	W_X		0.2668	0.3068	0.3468		
		W_Y		0.2984	0.3384	0.3784		
	Red	R_X		0.5758	0.6158	0.6558		
		R_Y		0.2915	0.3315	0.3715		
	Green	G_X		0.2907	0.3307	0.3707		
		G_Y		0.5345	0.5745	0.6145		
	Blue	B_X		0.1066	0.1466	0.1866		
		B_Y		0.0738	0.1138	0.1538		
Viewing angle	Hor.	ΘL	CR>10	--	85	--		
		ΘR		--	85	--		
	Ver.	ΘU		--	85	--		
		ΘD		--	85	--		
Option View Direction		All						

5.2 Measuring Condition

■ Measuring surrounding : dark room

■ Ambient temperature : 25±2℃

■The measured value of luminace and color coordinate bases BM-7

5.3 Measuring Equipment

■ TOPCON BM-7

■ Measuring spot size : field 2°

Note (1) Definition of Viewing Angle
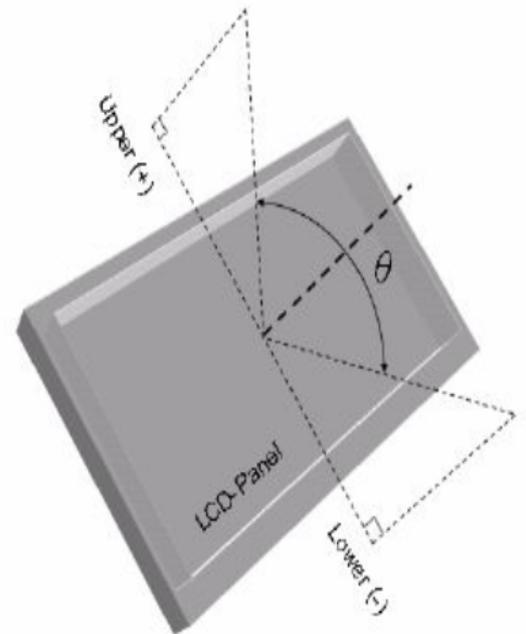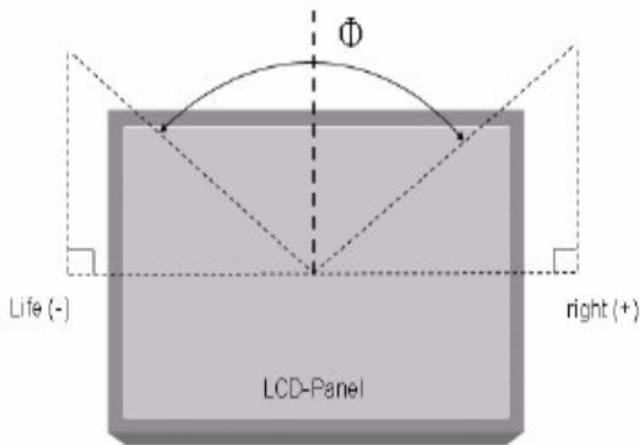
Note (2) Definition of Contrast Ratio(CR) :
  measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

Note (3) Definition of Response Time : Sum of T_r and T_f

Definition of Response Time
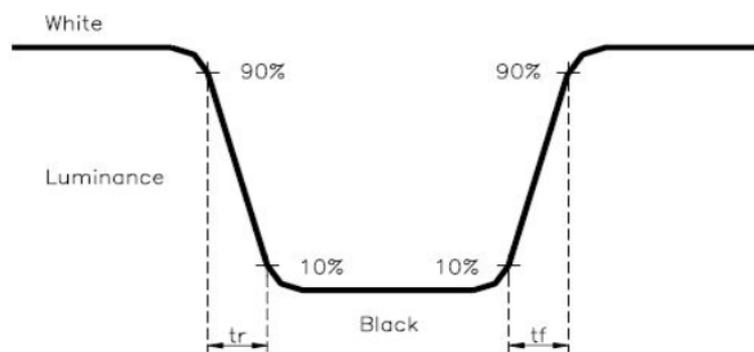
![FocusLCDs.com - LCDs MADE SIMPLE®]

Note (4) Definition of optical measurement setup

Photo-detector (BM-5A)

50cm

Field=2°

Center of panel

LCD panel
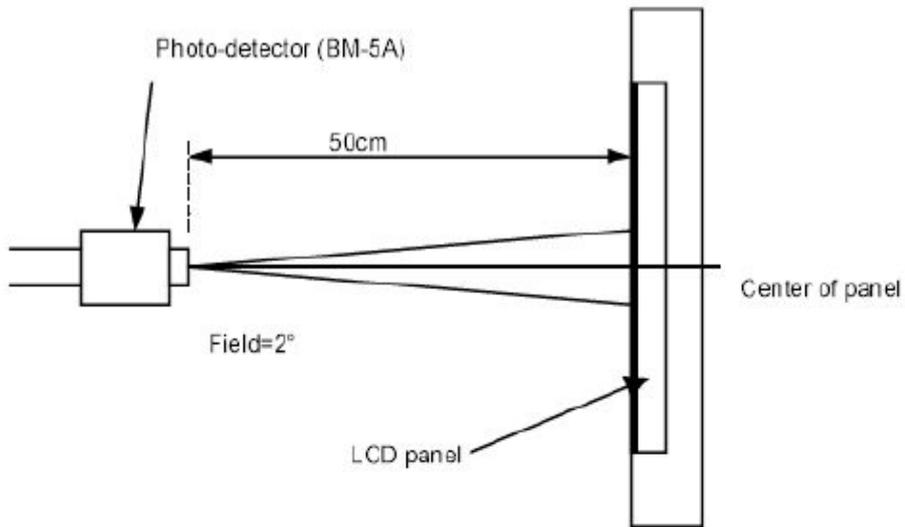
Note (5) Definition of brightness uniformity
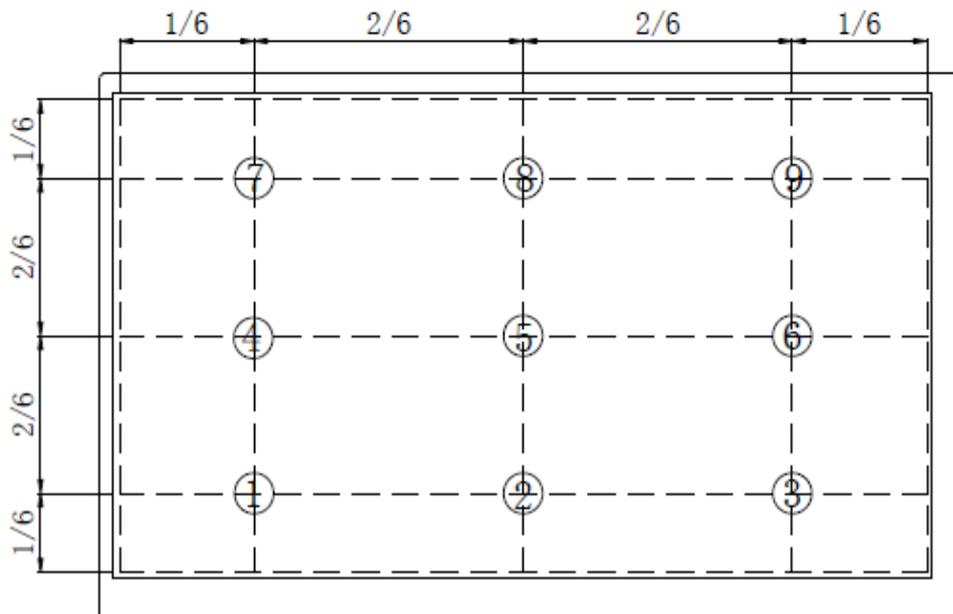
6. Electrical Characteristics

6.1 Absolute Maximum Rating

Characteristics	Symbol	Min.	Max.	Unit	Note
Digital Supply Voltage	VDD	-0.3	3.6	V	Note1
Operating temperature	T_{OP}	-20	+70	℃	
Storage temperature	T_{ST}	-30	+80	℃	

NOTE1: If the absolute maximum rating of even is one of the above parameters is exceeded even momentarily, the quality of the product may be degraded. Absolute maximum ratings, therefore, specify the values exceeding which the product may be physically damaged. Be sure to use the product within the range of the absolute maximum ratings.

6.2 DC Electrical Characteristics

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Note
Digital Supply Voltage	VDD	3.0	3.3	3.6	V	
Normal mode Current	IDD	--	120	--	mA	
Level input voltage	V_{IH}	0.7*VDD	--	VDD	V	
	V_{IL}	GND	--	0.3*VDD	V	
Level output voltage	V_{OH}	VDD-0.4	--	--	V	
	V_{OL}	--	--	GND+0.4	V	

6.3 LED Backlight Characteristics

The back-light system is edge-lighting type with 27 chips LED

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Forward Current	I_F	180	270	--	mA	
Forward Voltage	V_F	--	9.6	--	V	
LCM Luminance (I_F =180mA)	LV	350	400	--	cd/m2	Note3
LCM Luminance (I_F =270mA)	LV	470	520	--	cd/m2	Note3
LED life time	Hr	--	50000	--	Hour	Note1,2
Uniformity	Avg	80	--	--	%	Note3

Note1: LED life time (Hr) can be defined as the time in which it continues to operate under the condition:

Ta=25±3 ℃, typical IL value indicated in the above table until the brightness becomes less than 50%.

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at

Ta=25℃ and IL=270mA. The LED lifetime could be decreased if operating IL is larger than 270mA. The

constant current driving method is suggested.
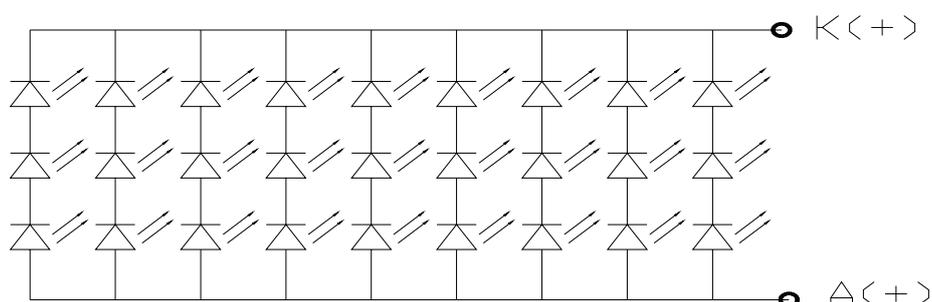
B/L Circuit

Note (3) Luminance Uniformity of these 9 points is defined as below:
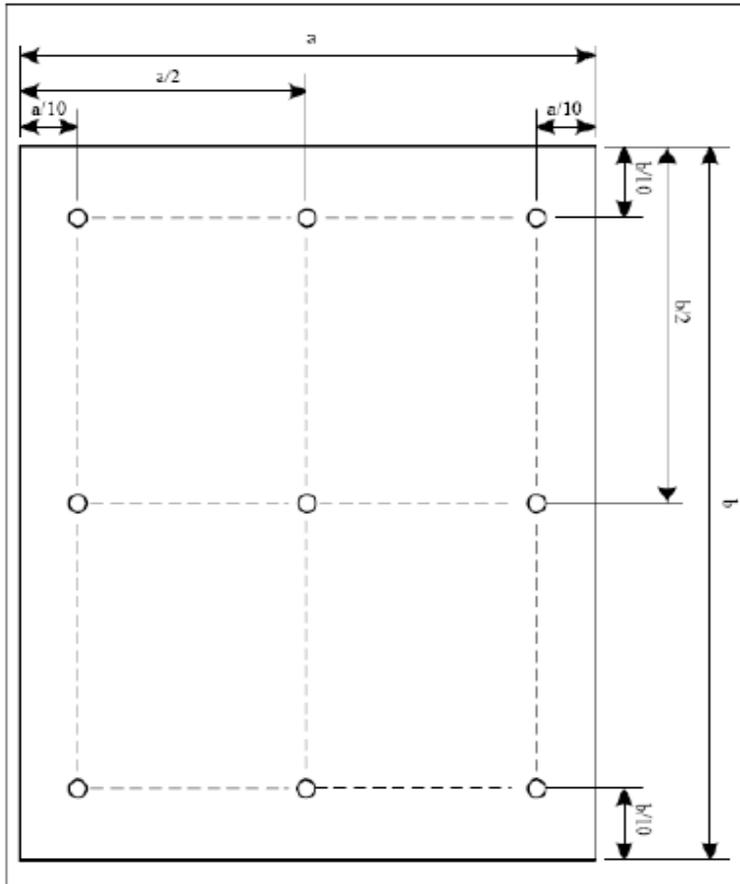
$$Uniformity = \frac{minimum\ luminance\ in\ 9\ points\ (1-9)}{maximum\ luminance\ in\ 9\ points\ (1-9)}$$

$$Luminance = \frac{Total\ Luminance\ of\ 9\ points}{9}$$

7. Timing Characteristics

7.1 AC Electrical Characteristics

Parameter	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Clock frequency	R_{xFCLK}	40.8	51.2	67.2	MHz	
Input data skew margin	T_{RSKM}	500	-	-	ps	
Clock high time	T_{LVCH}	-	$4/(7* R_{xFCLK})$	-	ns	
Clock low time	T_{LVCL}	-	$3/(7* R_{xFCLK})$	-	ns	

7.2 Input Clock and Data Timing Diagram

T_{RSKM}: Receiver strobe margin
R_{SPOS}: Receiver strobe position
T_{SW} : Strobe width (Internal data sampling window)
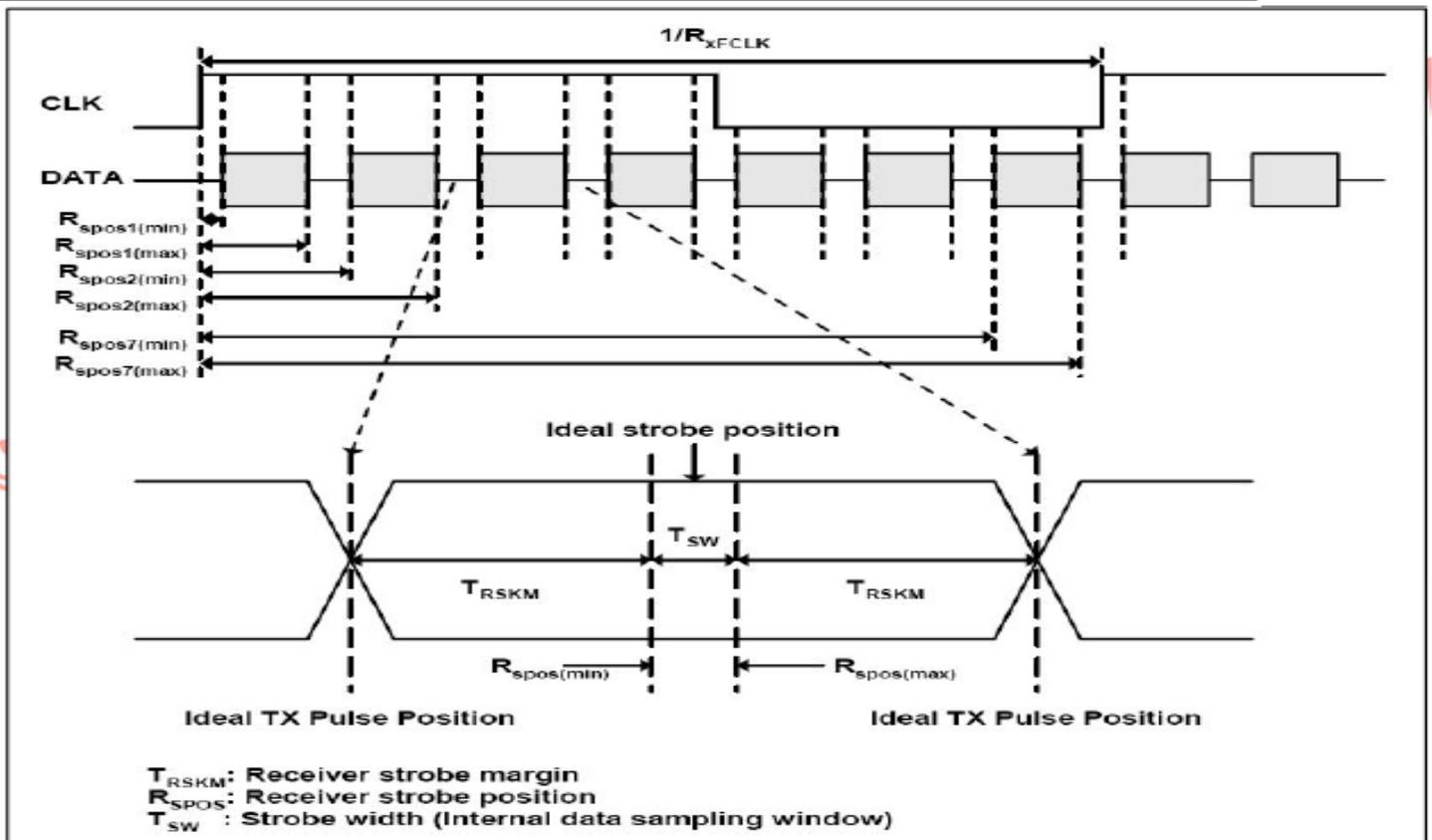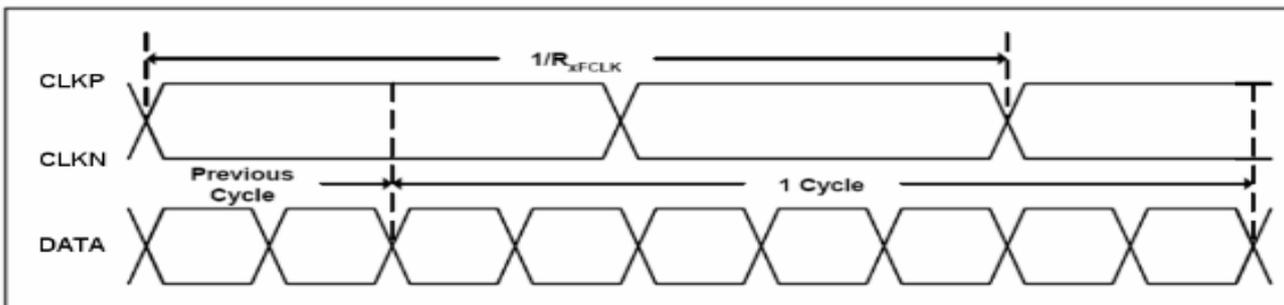
7.3 DC Electrical Characteristics

Parameter	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Differential input high Threshold voltage	R_{xVTH}	-	-	+0.1	V	R_{XVCM}=1.2V
Differential input low Threshold voltage	R_{xVTL}	-0.1	-	-	V	
Input voltage range (singled-end)	R_{xVIN}	0	-	2.4	V	
Differential input common mode voltage	R_{xVCM}	$|V_{ID}|/2$	-	$2.4-|V_{ID}|/2$	V	
Differential voltage	$|V_{ID}|$	0.2	-	0.6	V	
Differential input leakage current	RV_{xliz}	-10	-	+10	uA	
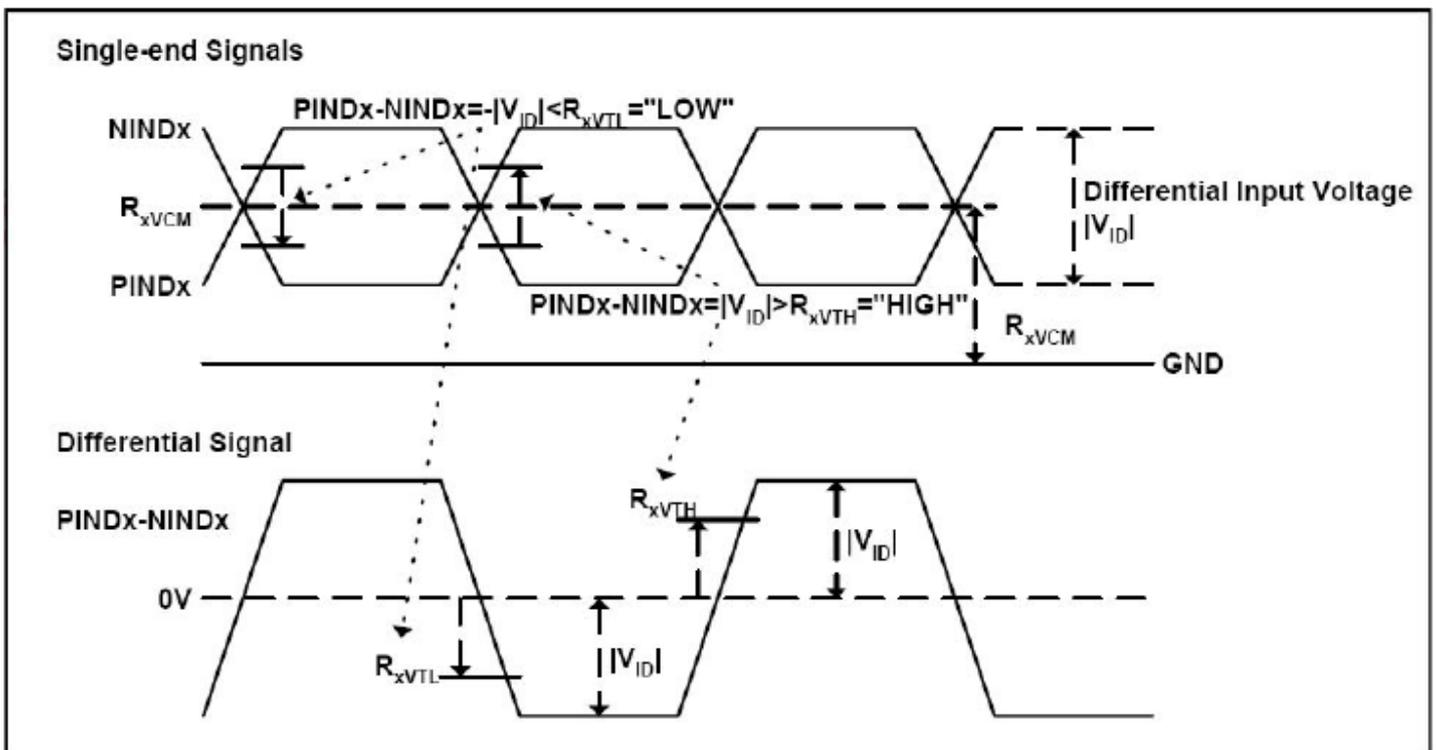
7.4 Timing

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Clock Frequency	fclk	40.8	51.2	67.2	MHz	Frame rate =60Hz
Horizontal display area	thd		1024		DCLK	
HS period time	th	1114	1344	1400	DCLK	
HS Blanking	thb	90	320	376	DCLK	
Vertical display area	tvd		600		H	
VS period time	tv	610	635	800	H	
VS Blanking	thb	10	35	200	H	

7.5 Data Input Format

6bit LVDS input

IND0	R0	G0	R5	R4	R3	R2	R1	R0	G0
IND1	G1	B1	B0	G5	G4	G3	G2	G1	B1
IND2	B2	DE	-	-	B5	B4	B3	B2	DE

8bit LVDS input

IND0	R0	G0	R5	R4	R3	R2	R1	R0	G0
IND1	G1	B1	B0	G5	G4	G3	G2	G1	B1
IND2	B2	DE	-	-	B5	B4	B3	B2	DE
IND3	R6	-	B7	B6	G7	G6	R7	R6	-
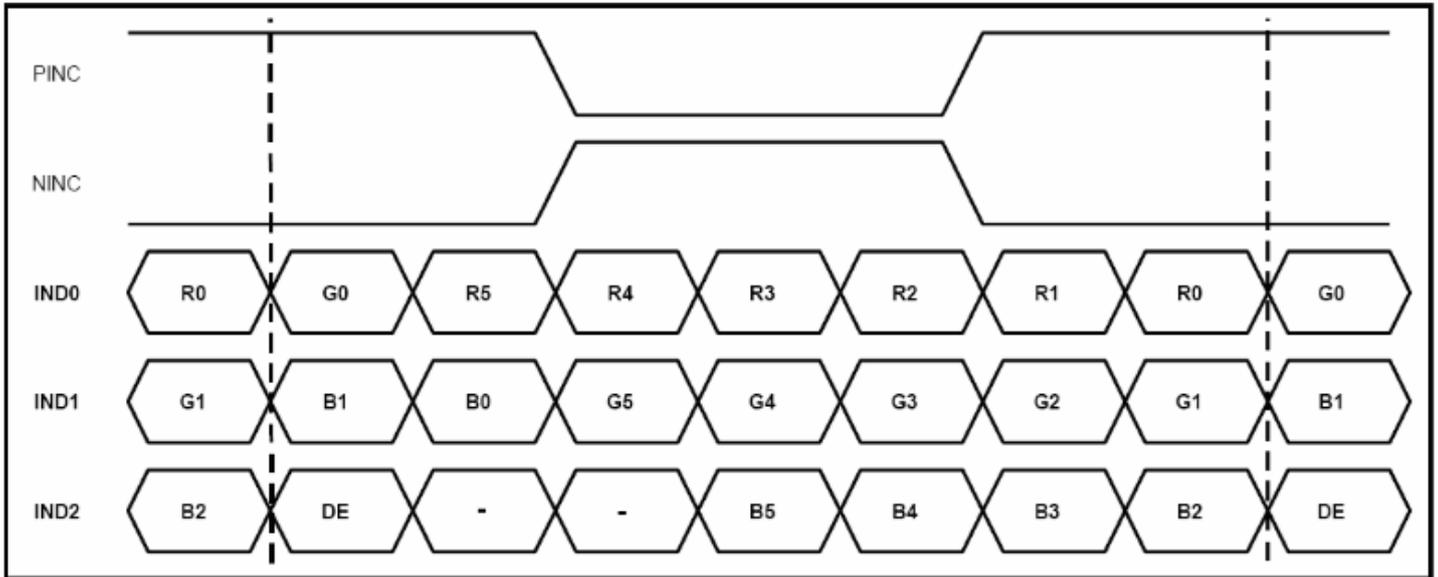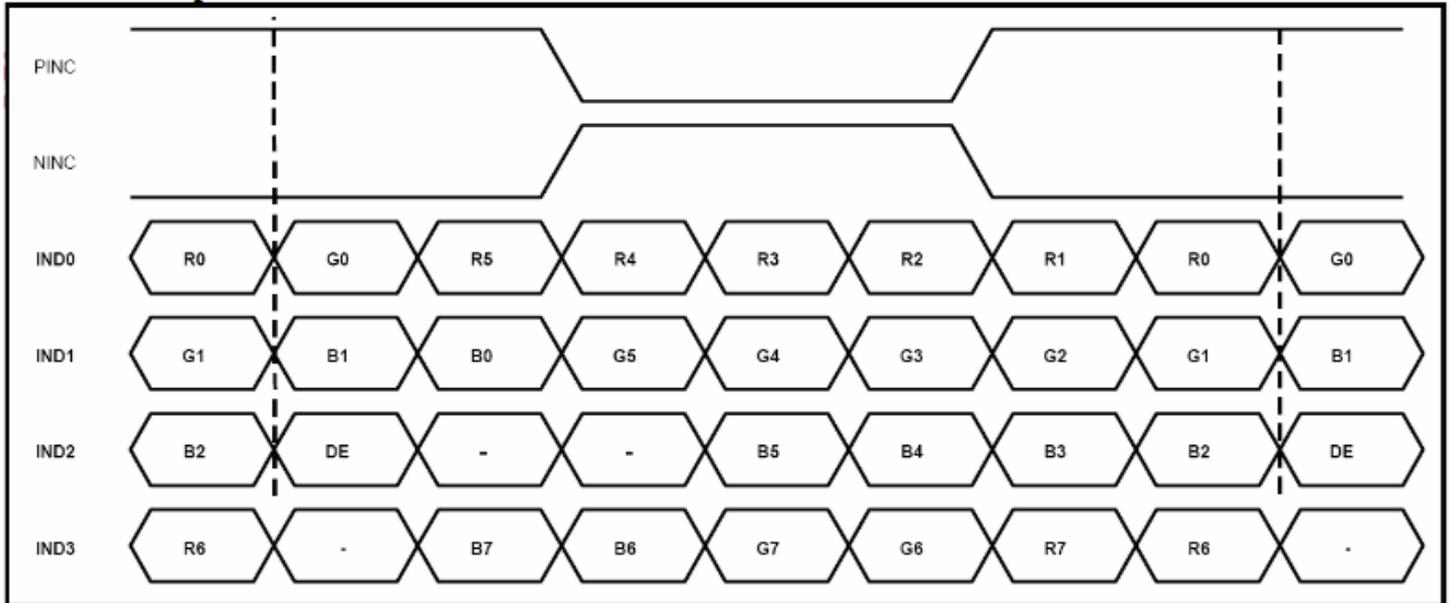
Note: Support DE timing mode only, SYNC mode not supported.

8. CTP Specification

8.1 Electrical Characteristics

8.1.1 Absolute Maximum Rating

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	VDD	2.66	3.47	V	1

NOTES:

If used beyond the absolute maximum ratings, GT9271 may be permanently damaged. It is strongly recom-manded that the device be used within the electrical characteristics in normal operations. If exposed to the condition not within the electrical characteristics, it may affect the reliability of the device.

8.1.2 DC Electrical Characteristics (Ta=25℃)

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Digital supply voltage	VDD	2.8	-	3.3	V	
Normal operation mode Current consumption	I_{opr}	-	13		mA	
Green mode Current consumption	I_{mon}	-	4.5	-	mA	
Sleep mode Current consumption	I_{slp}	70	-	120	uA	
Level input voltage	V_{IH}	$0.75V_{DD}$	-	$V_{DD}+0.3$	V	
	V_{IL}	-0.3	-	$0.25V_{DD}$	V	
Level output voltage	V_{OH}	$0.85V_{DD}$	-	-	V	
	V_{OL}	-	-	$0.15V_{DD}$	V	

8.2 AC Electrical Characteristics

GT9271 provides a standard I2C interface for SCL and SDA to communicate with the host. GT9271 always serves as slave device in the system with all communication being initialized by the host. It is strongly recommended that transmission rate be kept at or below 400Kbps. The I2C timing is shown below:
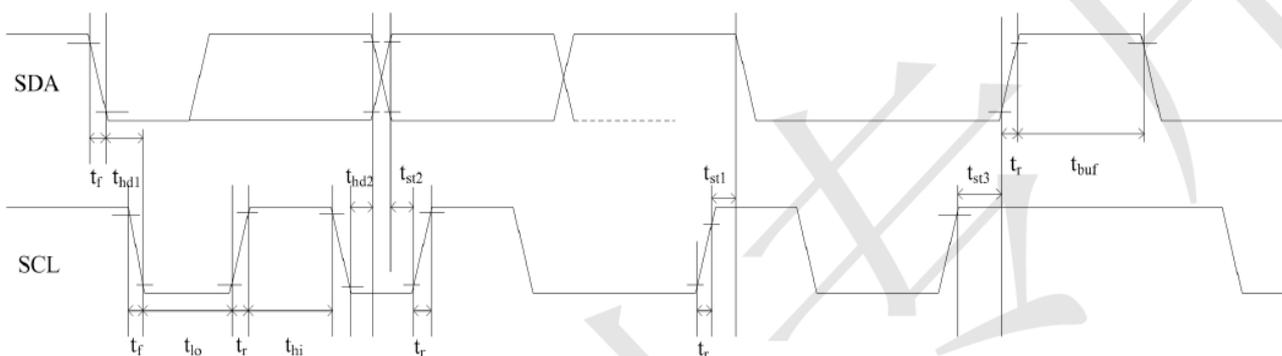
Test condition 1: 1.8V host interface voltage, 400Kbps transmission rate, 2K pull-up resistor

Parameter	Symbol	Min.	Max.	Unit
SCL low period	t_{lo}	1.3	-	us
SCL high period	t_{hi}	0.6	-	us
SCL setup time for Start condition	t_{st1}	0.6	-	us
SCL setup time for Stop condition	t_{st3}	0.6	-	us
SCL hold time for Start condition	t_{hd1}	0.6	-	us
SDA setup time	t_{st2}	0.1	-	us
SDA hold time	t_{hd2}	0	-	us

Test condition 2: 3.3V host interface voltage, 400Kbps transmission rate, 2K pull-up resistor

Parameter	Symbol	Min.	Max.	Unit
SCL low period	t_{lo}	1.3	-	us
SCL high period	t_{hi}	0.6	-	us
SCL setup time for Start condition	t_{st1}	0.6	-	us
SCL setup time for Stop condition	t_{st3}	0.6	-	us
SCL hold time for Start condition	t_{hd1}	0.6	-	us
SDA setup time	t_{st2}	0.1	-	us
SDA hold time	t_{hd2}	0	-	us

GT9271 supports two I2C slave addresses: 0xBA/0xBB and 0x28/0x29. The host can select the address by changing the status of Reset and INT pins during the power-on initialization phase. See the diagram below for configuration methods and timings:

Power-On Timing:
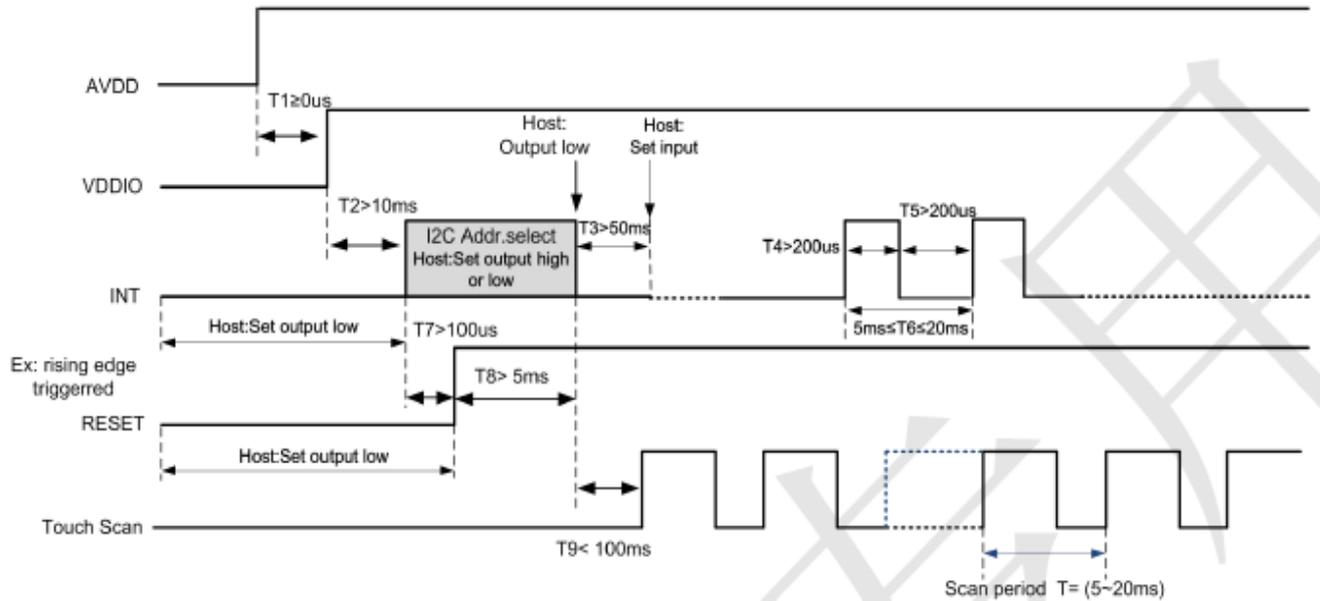
Timing for host resetting GT9271:
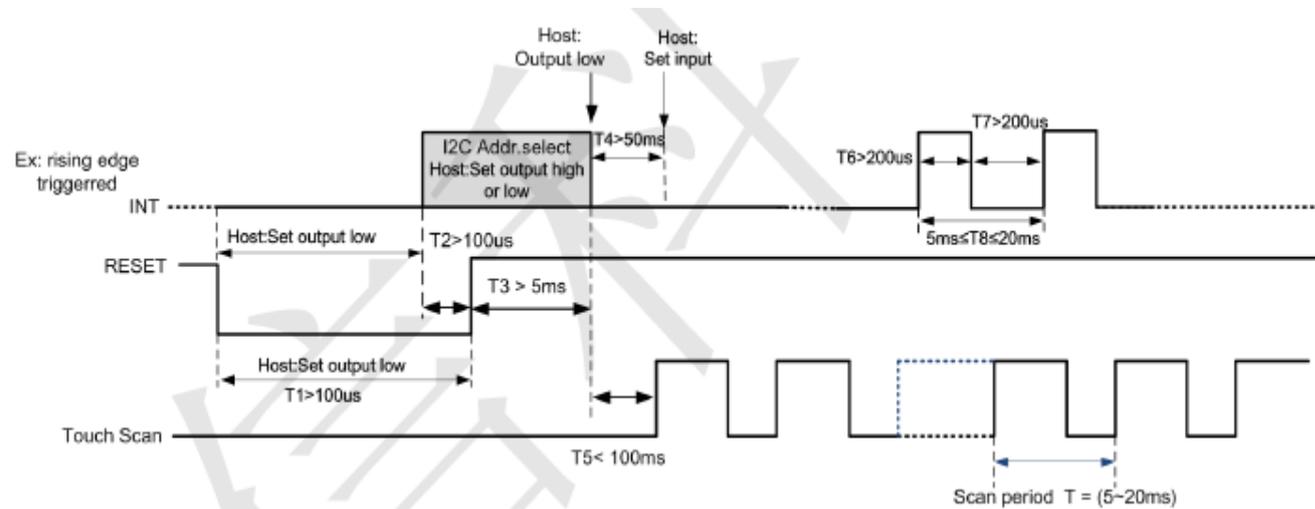
Timing for setting slave address to 0x28/0x29:

Reset

INT

| >100us | >5ms | >50ms |

1、Reset
Output low

2、INT
Output high

3、Reset
Output high

4、INT
Output low

5、INT
Input floating
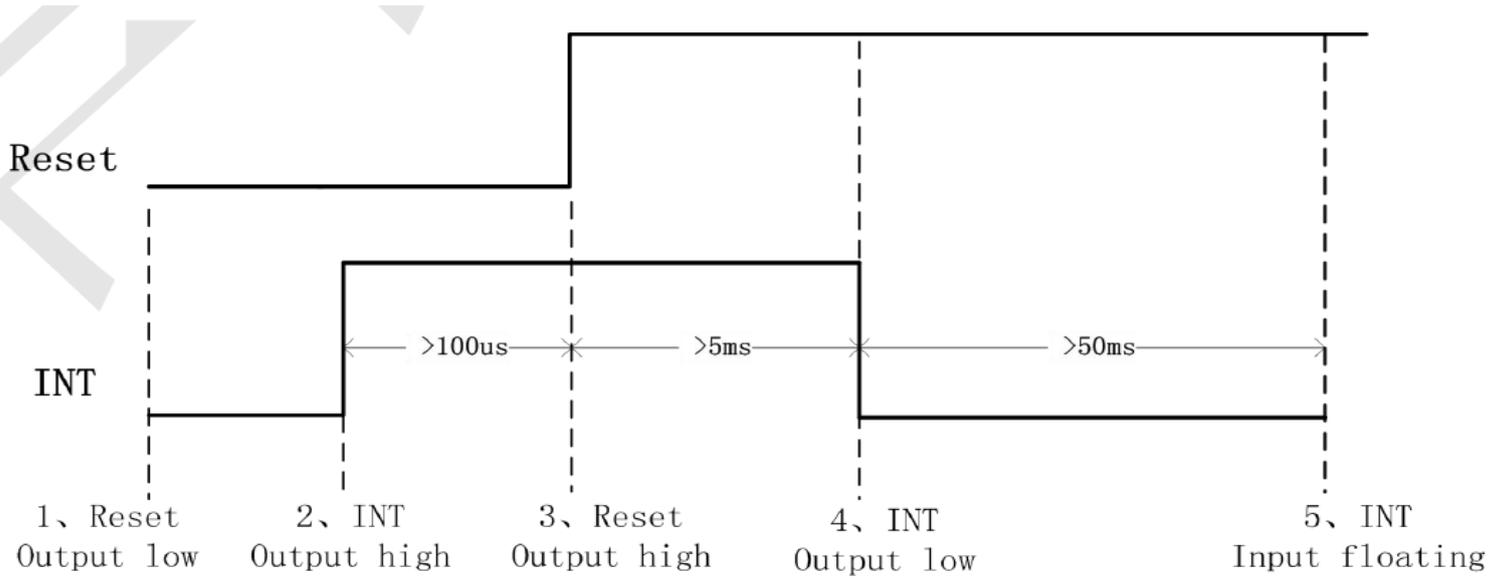
Timing for setting slave address to 0xBA/0xBB:

Reset

INT

| >100us | >5ms | >50ms |

1、Reset
Output low

2、INT
Output low

3、Reset
Output high

4、INT
Output low

5、INT
Input floating
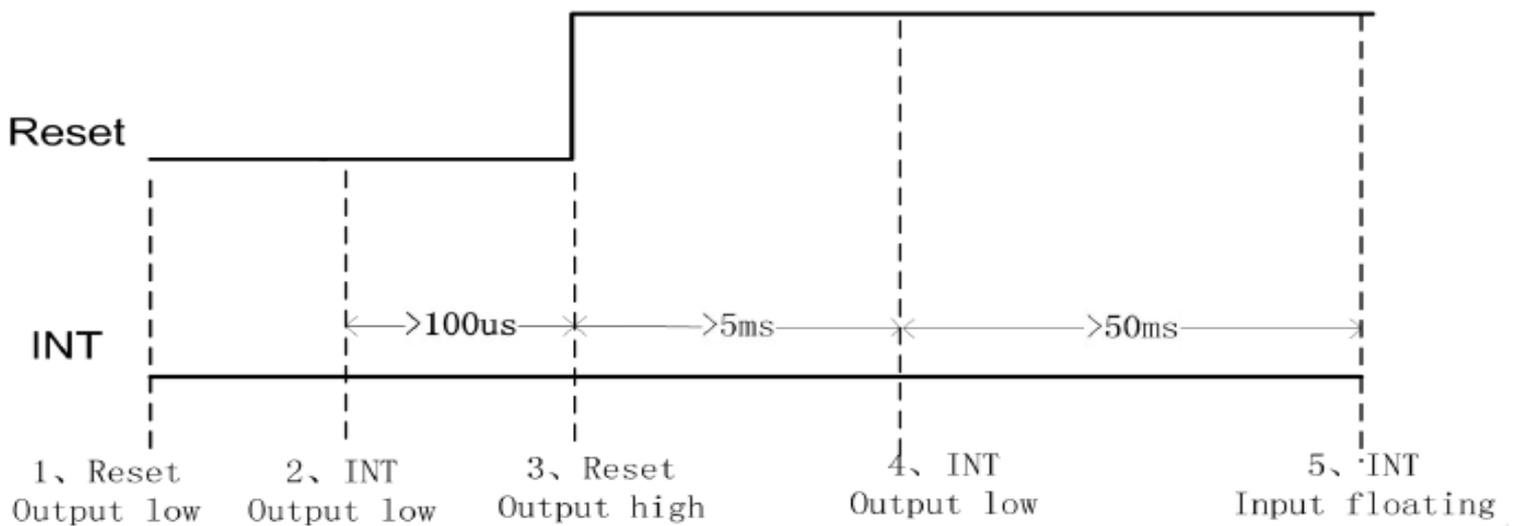
a) Data Transmission

(For example：slave address is 0xBA/0xBB)

Communication is always initiated by the host. Valid Start condition is signaled by pulling SDA line from high to low when SCL line is high. Data flow or address is transmitted after the Start condition.

All slave devices connected to I^{2}C bus should detect the 8-bit address issued after Start condition and send the correct ACK. After receiving matching address, GT9271 acknowledges by configuring SDA line as output port and pulling SDA line low during the ninth SCL cycle. When receiving unmatched address, namely, not 0XBA or 0XBB, GT9271 will stay in an idle state.

For data bytes on SDA, each of 9 serial bits will be sent on nine SCL cycles. Each data byte consists of 8 valid data bits and one ACK or NACK bit sent by the recipient. The data transmission is valid when SCL line is high.

When communication is completed, the host will issue the Stop condition which implies the transition of SDA line from low to high when SCL line is high.

b) Writing Data to GT9271

(For example：slave address is 0xBA/0xBB)

S	Address_W	A C K	Register_H	A C K	Register_L	A C K	Data_1	A C K	······	Data_n	A C K	E
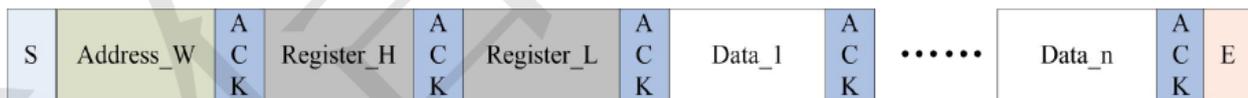
Timing for Write Operation

The diagram above displays the timing sequence of the host writing data onto GT9271. First, the host issues a Start condition. Then, the host sends 0XBA (address bits and R/W bit; R/W bit as 0 indicates Write operation) to the slave device.

After receiving ACK, the host sends the 16-bit register address (where writing starts) and the 8-bit data bytes (to be written onto the register).

The location of the register address pointer will automatically add 1 after every Write Operation. Therefore, when the host needs to perform Write Operations on a group of registers of continuous addresses, it is able to write continuously. The Write Operation is terminated when the host issues the Stop condition.

c) Reading Data from GT9271

(For example：slave address is 0xBA/0xBB)

| S | Address_W | ACK | Register_H | ACK | Register_L | ACK | E | S | Address_R | ACK | Data_1 | ACK | ······ | Data_n | NACK | E |

Set address pointer   Read data
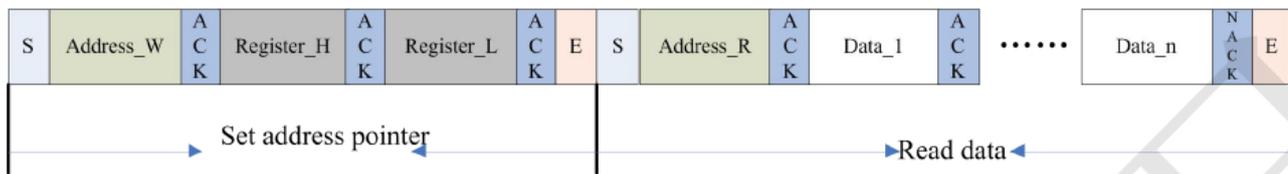
Timing for Read Operation

The diagram above is the timing sequence of the host reading data from GT9271. First, the host issues a Start condition and sends 0XBA (address bits and R/W bit; R/W bit as 0 indicates Write operation) to the slave device.

After receiving ACK, the host sends the 16-bit register address (where reading starts) to the slave device. Then the host sets register addresses which need to be read.

Also after receiving ACK, the host issues the Start condition once again and sends 0XBB (Read Operation). After receiving ACK, the host starts to read data.

GT9271 also supports continuous Read Operation and, by default, reads data continuously. Whenever receiving a byte of data, the host sends an ACK signal indicating successful reception. After receiving the last byte of data, the host sends a NACK signal followed by a STOP condition which terminates communication.

9. LCD Module Out-Going Quality Level

9.1 VISUAL & FUNCTION INSPECTION STANDARD

9.1.1 Inspection conditions

Inspection performed under the following conditions is recommended.

Temperature : 25±5℃

Humidity :   65%±10%RH

Viewing Angle : Normal viewing Angle.

Illumination: Single fluorescent lamp (300 to 700Lux)

Viewing distance:30-50cm
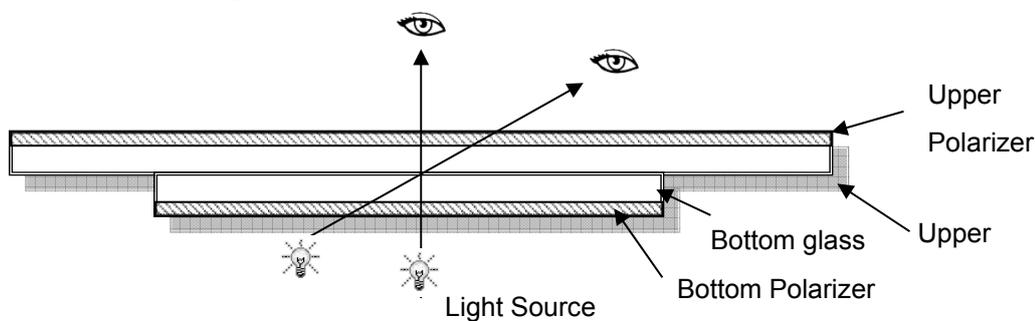
9.1.2 Definition
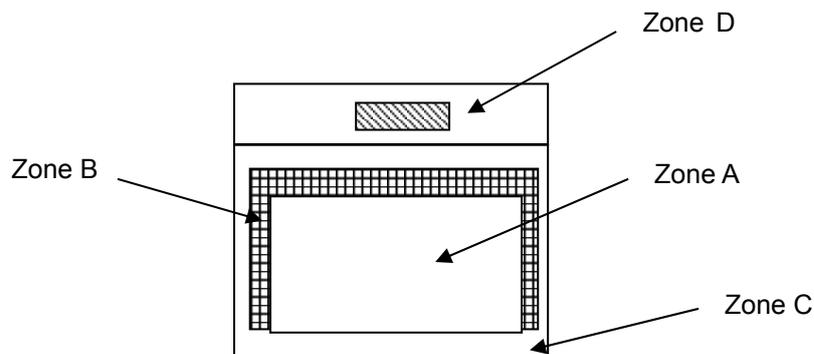
Zone A : Effective Viewing   Area(Character or Digit can be seen)

Zone B : Viewing Area except Zone A

Zone C : Outside (Zone A+Zone B) which can not be seen after assembly by customer .)

Zone D : IC Bonding Area

Note:As a general rule ,visual defects in Zone C can be ignored when it doesn't effect product function or appearance after assembly by customer

9.1.3 Sampling Plan

According to GB/T 2828-2003 ; , normal inspection, Class Ⅱ

AQL:

Major defect	Minor defect
0.65	1.5

LCD: Liquid Crystal Display , TP: Touch Panel , LCM: Liquid Crystal Module

No	Items to be inspected	Criteria	Classification of defects
1	Functional defects	1) No display, Open or miss line 2) Display abnormally, Short 3) Backlight no lighting, abnormal lighting. 4) TP no function	Major
2	Missing	Missing component	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	
4	Color tone	Color unevenness, refer to limited sample	Minor
5	Spot Line defect	Light dot，Dim spot,Polarizer　Bubble ; Polarizer accidented spot.	
6	Soldering appearance	Good soldering , Peeling off is not allowed.	
7	LCD/Polarizer/TP	Black/White spot/line, scratch, crack, etc.	

9.1.4 Criteria (Visual)

Number	Items	Criteria(mm)
1.0 LCD Crack/Broken NOTE: X: Length Y: Width Z: Height L: Length of ITO, T: Height of LCD	(1) The edge of LCD broken	 **X:** ≤3.0mm **Y:** <Inner border line of the seal **Z:** ≤T
	(2)LCD corner broken	 **X:** ≤3.0mm **Y:** ≤L **Z:** ≤T
	(3) LCD crack	 Crack Not allowed
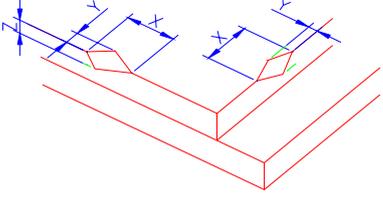
2.0	Spot defect X Φ=(X+Y)/2	① light dot （LCD/TP/Polarizer black/white spot , light dot, pinhole, dent, stain）

① light dot （LCD/TP/Polarizer black/white spot , light dot, pinhole, dent, stain）

Zone Size (mm)	Acceptable Qty		
	A	B	C
Φ≤0.10	Ignore		Ignore
0.10<Φ≤0.25	4(distance≧10mm)		
0.25<Φ≤0.35	3		
Φ>0.4	0		

②Dim spot （LCD/TP/Polarizer dim dot, light leakage、 dark spot）

Zone Size (mm)	Acceptable Qty		
	A	B	C
Φ≤0.1	Ignore		Ignore
0.10<Φ≤0.25	4(distance≧10mm)		
0.25<Φ≤0.35	3		
Φ>0.4	0		

③ Polarizer accidented spot

Zone Size (mm)	Acceptable Qty		
	A	B	C
Φ≤0.2	Ignore		Ignore
0.3<Φ≤0.5	3(distance≧10mm)		
Φ>0.5	1		

④Pixel bad points （light dot，Dim dot，color dot）

Zone Size (mm)	Acceptable Qty		
	A	B	C
Φ≤0.15	Ignore		Ignore
0.2<Φ≤0.3	2(distance≧10mm)		
Φ>0.4	1		

⑤ Polarizer   Bubble

Zone Size (mm)	Acceptable Qty		
	A	B	C
Φ≤0.2	Ignore		Ignore
0.3<Φ≤0.4	4(distance≧10mm)		
0.4<Φ≤0.5	3		
Φ>0.5	1		

		Width(mm)	Length(mm)	Acceptable Qty		
				A	B	C
3.0	Line defect (LCD/TP /Polarizer backlight black/white line, scratch, stain)	Φ≤0.05	Ignore	Ignore		Ignore
		0.05<W≤0.06	L≤5.0	N≤3		
		0.07<W≤0.08	L≤4.0	N≤2		
		0.08<W	Define as spot defect			

4.0	Electronic Components SMT	Not allow missing parts, solderless connection, cold solder joint, mis match, The positive and negative polarity opposite

5.0	Display color& Brightness	1. Color : Measuring the color coordinates, The measurement standard according to the datasheet or samples. 2. Brightness : Measuring the brightness of White screen, The measurement standard according to the datasheet or Samples.

6.0	LCD Mura	By 5% ND filter invisible.

			Size Φ(mm)	Acceptable Qty		
				A	B	C
7.0	RTP Related	TP film bubble/ accidented spot	Φ≤0.1	Ignore		Ignore
			0.1<Φ≤0.25	4 (distance≧10mm)		
			0.25<Φ≤0.35	3		
			Φ>0.4	1		

		Width(mm)	Length(mm)	Acceptable Qty		
				A	B	C
	TP film scratch	Φ≤0.05	Ignore	Ignore		Ignore
		0.05<W≤0.06	L≤5.0	N≤3		
		0.07<W≤0.08	L≤4.0	N≤2		
		0.08<W	Define as spot defect			

		Assembly deflection	beyond the edge of backlight ≤0.2mm	
		Bulge (undulation included)	The ITO film plumped below 0.40mm, it's ok. <0.4mm	
		Newton Ring	Newton Ring area>1/3 TP area NG Newton Ring area≤1/3 TP area OK	
		TP corner broken X：length Y：width Z：height	X｜Y｜Z X≤3mm｜Y≤3mm｜Z<COVER thickness * *Circuitry broken is not allowed.	

The TP corner broken table:

X	Y	Z
X≤3mm	Y≤3mm	Z<COVER thickness

*Circuitry broken is not allowed.
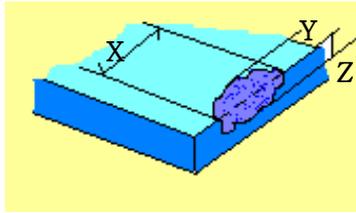
			X	Y	Z
		TP edge broken	X≤4mm	Y≤2mm	Z<COVER thickness
		X：length Y：width Z：height	* Circuitry broken is not allowed.		

Criteria (functional items)

Number	Items	Criteria (mm)
1	No display	Not allowed
2	Missing segment	Not allowed
3	Short	Not allowed
4	Backlight no lighting	Not allowed
5	TP no function	Not allowed

10. Reliability Test Result

Item	Condition	Inspection after test
High Temperature Operating	70℃,96H	Inspection after 2~4hours storage at room temperature, the sample shall be free from defects: 1.Air bubble in the LCD; 2.Non-display; 3.Missing segments/line; 4.Glass crack; 5.Current IDD is twice higher than initial value.
Low Temperature Operating	-20℃, 96HR	
High Temperature Storage	80℃, 96HR	
Low Temperature Storage	-30℃, 96HR	
High Temperature & High Operating	+60℃, 90% RH ,96 hours.	
Thermal Shock (Non-operation)	-30℃,30 min ↔ 80℃,30 min, Change time:5min 20CYC.	
ESD test	C=150pF, R=330,5points/panel Air:±8KV, 5times; Contact:±6KV, 5 times; (Environment: 15℃~35℃, 30%~60%).	
Vibration (Non-operation)	Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z. (6 hours for total) (Package condition).	
Box Drop Test	1 Corner 3 Edges 6 faces,80㎝(MEDIUM BOX)	

Remark:

1.The test samples should be applied to only one test item.

2.Sample size for each test item is 5~10pcs.

3.For Damp Proof Test, Pure water(Resistance＞10MΩ) should be used.

4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.

5.Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

11. Cautions and Handling Precautions

11.1 Handling and Operating the Module

(1) When the module is assembled, it should be attached to the system firmly.

Do not warp or twist the module during assembly work.

(2) Protect the module from physical shock or any force. In addition to damage, this may cause improper operation or damage to the module and back-light unit.

(3) Note that polarizer is very fragile and could be easily damaged. Do not press or scratch the surface.

(4) Do not allow drops of water or chemicals to remain on the display surface.

If you have the droplets for a long time, staining and discoloration may occur.

(5) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.

(6) The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane.

Do not use ketene type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.

(7) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs, or clothes, it must be washed away thoroughly with soap.

(8) Protect the module from static; it may cause damage to the CMOS ICs.

(9) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.

(10) Do not disassemble the module.

(11) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.

(12) Pins of I/F connector shall not be touched directly with bare hands.

(13) Do not connect, disconnect the module in the "Power ON" condition.

(14) Power supply should always be turned on/off by the item 6.1 Power On Sequence &6.2 Power Off Sequence

11.2 Storage and Transportation.

(1) Do not leave the panel in high temperature, and high humidity for a long time.

It is highly recommended to store the module with temperature from 0 to 35 ℃ and relative humidity of less than 70%

(2) Do not store the TFT-LCD module in direct sunlight.

(3) The module shall be stored in a dark place. When storing the modules for a long time, be sure to adopt effective measures for protecting the modules from strong ultraviolet radiation, sunlight, or fluorescent light.

(4) It is recommended that the modules should be stored under a condition where no condensation is allowed. Formation of dewdrops may cause an abnormal operation or a failure of the module.

In particular, the greatest possible care should be taken to prevent any module from being operated where condensation has occurred inside.

(5) This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed.